TOSHIBA

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC4021BP, TC4021BF

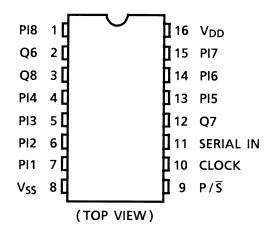
TC4021B 8-Stage Static Shift Register

(asynchronous parallel input or synchronous serial input/serial output)

TC4021B is 8 stage parallel in/serial out shift register, which can be used also for serial in/serial out operations. In the case of parallel operation, the data of PARALLEL IN is input to each F/F asynchronously with CLOCK and the output is obtained. In the case of serial operations, each F/F is triggered by rising edge of CLOCK. (asynchronous parallel or synchronous serial input)

Switching of PARALLEL operation and SERIAL operation is achieved by  $P/\overline{S}$  CONTROL input. When  $P/\overline{S}$  CONTROL input is "H", PARALLEL operation is designated and when it is "L", SERIAL operation is designated.

### **Pin Assignment**



### **Truth Table**

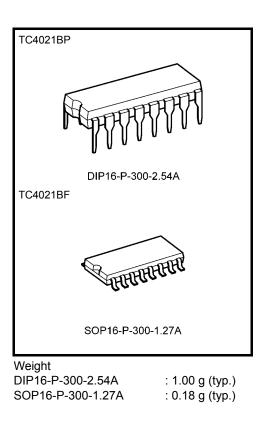
|        | Outputs∆ |     |     |    |           |        |  |
|--------|----------|-----|-----|----|-----------|--------|--|
|        | P/S      | PI1 | Pln | SI | Q1        | Qn     |  |
|        | L        | *   | *   | L  | L         | Qn – 1 |  |
|        | L        | *   | *   | Н  | H Qn – 1  |        |  |
| $\neg$ | L        | *   | *   | *  | No Change |        |  |
| *      | Н        | L   | L   | *  | L         | L      |  |
| *      | Н        | L   | Н   | *  | L         | Н      |  |
| *      | Н        | Н   | L   | *  | Н         | L      |  |
| *      | Н        | Н   | Н   | *  | Н         | Н      |  |

n: 2 to 8

 $\Delta$ : Q1 to Q5 internal

 $\Delta\Delta$ : Level change

\*: Don't care

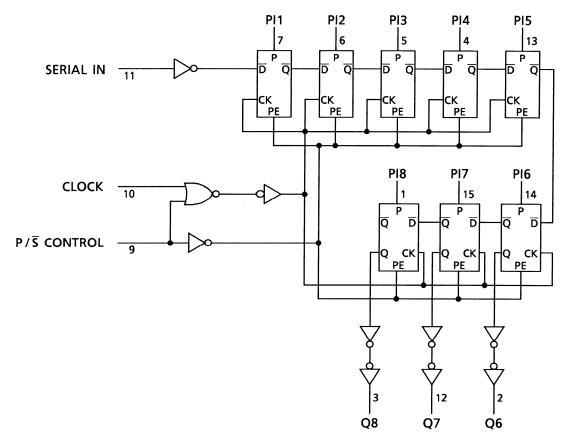


# Start of commercial production 1978-04

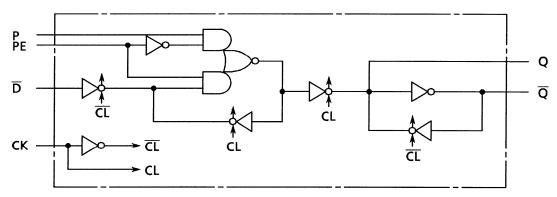
## **TOSHIBA**

### Logic Diagram

Parallel



Internal Flip Flop



### **Absolute Maximum Ratings (Note)**

| Characteristics             | Symbol           | Rating   | Unit |
|-----------------------------|------------------|--|------|
| DC supply voltage           | V <sub>DD</sub>  | $V_{SS}$ – 0.5 to $V_{SS}$ + 20                                    | V    |
| Input voltage               | VIN              | $V_{\mbox{\scriptsize SS}}-0.5$ to $V_{\mbox{\scriptsize DD}}+0.5$ | V    |
| Output voltage              | V <sub>OUT</sub> | $V_{\mbox{\scriptsize SS}}-0.5$ to $V_{\mbox{\scriptsize DD}}+0.5$ | V    |
| DC input current            | I <sub>IN</sub>  | ±10  | mA   |
| Power dissipation           | PD               | 300 (DIP)/180 (SOP)  | mW   |
| Operating temperature range | T <sub>opr</sub> | -40 to 85  | °C   |
| Storage temperature range   | T <sub>stg</sub> | –65 to 150   | °C   |

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Operating Ranges (V<sub>SS</sub> = 0 V) (Note)**

| Characteristics   | Symbol          | Test Condition | Min | Тур. | Max             | Unit |
|-------------------|-----------------|----------------|-----|------|-----------------|------|
| DC supply voltage | V <sub>DD</sub> | —              | 3   | _    | 18              | V    |
| Input voltage     | V <sub>IN</sub> | _              | 0   |      | V <sub>DD</sub> | V    |

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{DD}$  or  $V_{SS}$ .

### Static Electrical Characteristics ( $V_{SS} = 0 V$ )

|                              |                                 | Sym-                      | Test Condition                   | -40°C                  |       | 25°C |       |                   | 85°C |       |      |      |
|------------------------------|---------------------------------|---------------------------|----------------------------------|------------------------|-------|------|-------|-------------------|------|-------|------|------|
| Charac                       | icteristics bol                 |                           |                                  | V <sub>DD</sub><br>(V) | Min   | Max  | Min   | Тур.              | Max  | Min   | Max  | Unit |
| High-level output<br>voltage |                                 | I <sub>OUT</sub>   < 1 μΑ | 5                                | 4.95                   | _     | 4.95 | 5.00  | _                 | 4.95 | _     |      |      |
|                              | V <sub>OH</sub>                 |                           | 10                               | 9.95                   | _     | 9.95 | 10.00 | —                 | 9.95 | —     | V    |      |
|                              |                                 |                           | $V_{IN} = V_{SS}, V_{DD}$        | 15                     | 14.95 |      | 14.95 | 15.00             | _    | 14.95 | _    |      |
|                              |                                 |                           | I <sub>OUT</sub>   < 1 μΑ        | 5                      |       | 0.05 | _     | 0.00              | 0.05 | —     | 0.05 |      |
| Low-level voltage            | output                          | V <sub>OL</sub>           | $V_{IN} = V_{SS}, V_{DD}$        | 10                     | —     | 0.05 | —     | 0.00              | 0.05 | —     | 0.05 | V    |
| Ū                            |                                 |                           | VIN - VSS, VDD                   | 15                     | _     | 0.05 | —     | 0.00              | 0.05 |       | 0.05 |      |
|                              |                                 |                           | V <sub>OH</sub> = 4.6 V          | 5                      | -0.61 | _    | -0.51 | -1.0              | _    | -0.42 | —    |      |
|                              |                                 |                           | $V_{OH} = 2.5 V$                 | 5                      | -2.50 | _    | -2.10 | -4.0              | _    | -1.70 | —    |      |
| Output hig                   | h current                       | IOH                       | V <sub>OH</sub> = 9.5 V          | 10                     | -1.50 | _    | -1.30 | -2.2              | _    | -1.10 | —    | mA   |
|                              |                                 |                           | V <sub>OH</sub> = 13.5 V         | 15                     | -4.00 | _    | -3.40 | -9.0              | _    | -2.80 | —    |      |
|                              |                                 |                           | $V_{IN}=V_{SS},V_{DD}$           |                        |       |      |       |                   |      |       |      |      |
|                              |                                 | le:                       | $V_{OL} = 0.4 V$                 | 5                      | 0.61  |      | 0.51  | 1.5               |      | 0.42  | _    | mA   |
|                              | / current                       |                           | $V_{OL} = 0.5 V$                 | 10                     | 1.50  | _    | 1.30  | 3.8               | _    | 1.10  | —    |      |
| Output low current           | IOL                             | V <sub>OL</sub> = 1.5 V   | 15                               | 4.00                   | _     | 3.40 | 15.0  | _                 | 2.80 | —     |      |      |
|                              |                                 |                           | $V_{IN}=V_{SS},\ V_{DD}$         |                        |       |      |       |                   |      |       |      |      |
|                              |                                 | VIH                       | $V_{OUT} = 0.5 V, 4.5 V$         | 5                      | 3.5   | _    | 3.5   | 2.75              | _    | 3.5   | —    | V    |
| Input high                   | voltage                         |                           | V <sub>OUT</sub> = 1.0 V, 9.0 V  | 10                     | 7.0   | _    | 7.0   | 5.50              | _    | 7.0   | —    |      |
| input nigh                   | voltage                         |                           | $V_{OUT} = 1.5 V, 13.5 V$        | 15                     | 11.0  | _    | 11.0  | 8.25              | _    | 11.0  | —    |      |
|                              |                                 |                           | $ I_{OUT}  < 1 \ \mu A$          |                        |       |      |       |                   |      |       |      |      |
|                              |                                 | VIL                       | $V_{OUT} = 0.5 V, 4.5 V$         | 5                      |       | 1.5  | _     | 2.25              | 1.5  | —     | 1.5  | V    |
| Input low voltage            | V <sub>OUT</sub> = 1.0 V, 9.0 V |                           | 10                               | —                      | 3.0   | —    | 4.50  | 3.0               | —    | 3.0   |      |      |
| input low voltage            |                                 |                           | $V_{OUT} = 1.5 V, 13.5 V$        | 15                     | —     | 4.0  | —     | 6.75              | 4.0  | —     | 4.0  |      |
|                              |                                 |                           | $ I_{OUT}  < 1 \ \mu A$          |                        |       |      |       |                   |      |       |      |      |
| Input                        | "H" level                       | Ι <sub>ΙΗ</sub>           | V <sub>IH</sub> = 18 V           | 18                     | _     | 0.1  | _     | 10 <sup>-5</sup>  | 0.1  | —     | 1.0  | μA   |
| current                      | "L" level                       | ١ <sub>١L</sub>           | $V_{IL} = 0 V$                   | 18                     | _     | -0.1 | _     | -10 <sup>-5</sup> | -0.1 | —     | -1.0 | μη   |
|                              |                                 |                           | $V_{IN} = V_{SS}, V_{DD}$ (Note) | 5                      | _     | 5    |       | 0.005             | 5    | _     | 150  |      |
| Quiescent supply<br>current  |                                 | I <sub>DD</sub>           |                                  | 10                     | —     | 10   | —     | 0.010             | 10   | —     | 300  | μA   |
|                              |                                 |                           |                                  | 15                     | —     | 20   | —     | 0.020             | 20   | _     | 600  |      |

Note: All valid input combinations.

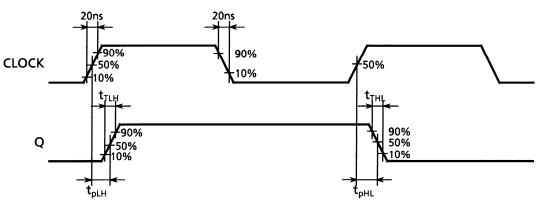
### Dynamic Electrical Characteristics (Ta = $25^{\circ}$ C, V<sub>SS</sub> = 0 V, C<sub>L</sub> = 50 pF)

| Characteristics                         | Symbol                | Test Condition | Min                 | Turp   | Max  | Unit |      |
|---|-----------------------|----------------|---------------------|--------|------|------|------|
| Characteristics                         | Symbol                |                | V <sub>DD</sub> (V) | IVIIII | Тур. | Wax  | Unit |
| Output transition time                  |                       |                | 5                   | _      | 80   | 200  |      |
| (low to high)                           | t <sub>TLH</sub>      | —              | 10                  | —      | 50   | 100  | ns   |
| (low to high)                           |                       |                | 15                  | —      | 40   | 80   |      |
| Output transition time                  |                       |                | 5                   | _      | 80   | 200  |      |
| Output transition time<br>(high to low) | t <sub>THL</sub>      | —              | 10                  | —      | 50   | 100  | ns   |
| (ilight to low)                         |                       |                | 15                  | _      | 40   | 80   |      |
| Propagation delay time                  | <b>+</b>              |                | 5                   |        | 150  | 320  | ns   |
| (CLOCK-Q)                               | <sup>t</sup> pLH<br>t | —              | 10                  | —      | 65   | 160  |      |
|   | t <sub>pHL</sub>      |                | 15                  | _      | 45   | 120  |      |
| Propagation delay time                  | <b>+</b>              |                | 5                   |        | 230  | 460  |      |
| (P/S -Q)                                | <sup>t</sup> pLH<br>t | —              | 10                  | —      | 90   | 180  | ns   |
| (10-0)                                  | t <sub>pHL</sub>      |                | 15                  | _      | 60   | 120  |      |
|   |                       |                | 5                   | 3.0    | 6.5  | _    | MHz  |
| Max clock frequency                     | f <sub>CL</sub>       | —              | 10                  | 6.0    | 18.0 |      |      |
|   |                       |                | 15                  | 8.5    | 24.0 |      |      |
|   | t <sub>W</sub>        | —              | 5                   |        | 80   | 180  |      |
| Min clock pulse width                   |                       |                | 10                  | —      | 30   | 80   | ns   |
|   |                       |                | 15                  | _      | 20   | 50   |      |
| Max clock rise time                     | ter                   |                | 5                   | 20.0   |      |      | μS   |
| Max clock fall time                     | t <sub>rCL</sub>      | _              | 10                  | 2.5    |      |      |      |
|   | t <sub>fCL</sub>      |                | 15                  | 1.0    | _    |      |      |
| Min set-up time                         |                       |                | 5                   | —      | 40   | 120  |      |
| (SI-CLOCK)                              | tsu                   | —              | 10                  | —      | 20   | 80   | ns   |
| (51-0200K)                              |                       |                | 15                  | _      | 15   | 60   |      |
| Min set-up time                         |                       |                | 5                   |        | 25   | 50   |      |
| (PI-P/S)                                | tsu                   |                | 10                  | —      | 15   | 30   | ns   |
| (1-173)                                 |                       |                | 15                  |        | 10   | 20   |      |
| Min hold time                           |                       |                | 5                   | _      | 35   | 70   |      |
| (SI-CLOCK), (PI-P/ $\overline{S}$ )     | t <sub>H</sub>        | —              | 10                  | —      | 20   | 40   | ns   |
| (OFOLOON), (FF70)                       |                       |                | 15                  | _      | 15   | 30   |      |
| Min pulse width                         |                       |                | 5                   | —      | 90   | 180  |      |
| $(P/\overline{S} - CONTROL)$            | twH                   | —              | 10                  | —      | 30   | 80   | ns   |
|   |                       |                | 15                  | _      | 10   | 50   |      |
| Min removal time                        |                       |                | 5                   |        | 45   | 280  |      |
| (P/S -CLOCK)                            | t <sub>rem</sub>      | —              | 10                  | —      | 20   | 140  | ns   |
|   |                       |                | 15                  |        | 15   | 100  |      |
| Input capacitance                       | C <sub>IN</sub>       |                |                     | —      | 5    | 7.5  | pF   |

# <u>TOSHIBA</u>

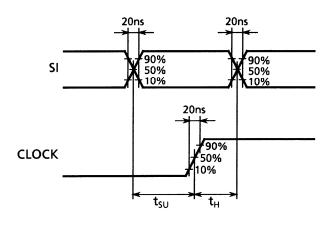
### Waveforms for Measurement of Dynamic Characteristics

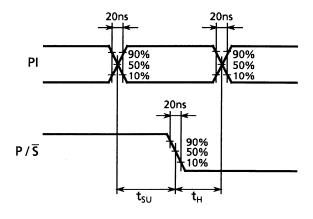
#### Waveform 1



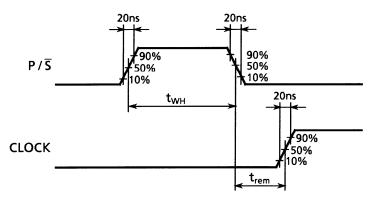
#### Waveform 2

Waveform 3





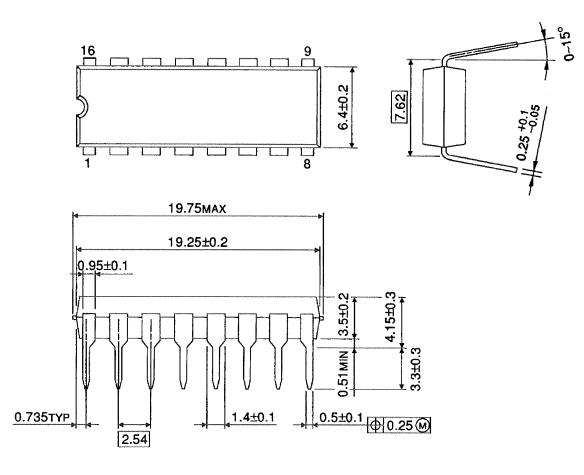
### Waveform 4



### **Package Dimensions**

DIP16-P-300-2.54A

Unit : mm



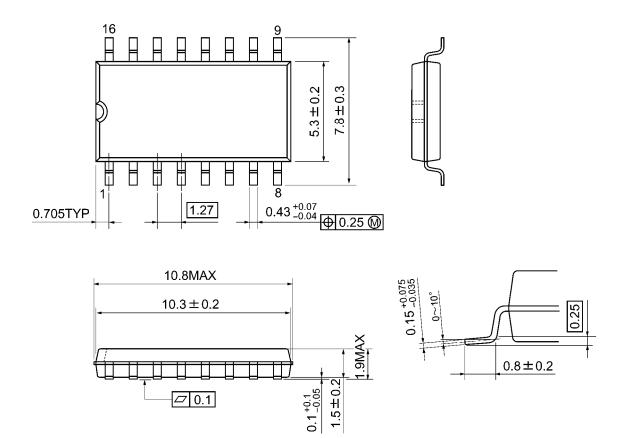
Weight: 1.00g (typ.)



### **Package Dimensions**

SOP16-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

### **RESTRICTIONS ON PRODUCT USE**

- Toshiba Corporation, and its subsidiaries and affiliates (collectively "TOSHIBA"), reserve the right to make changes to the information in this document, and related hardware, software and systems (collectively "Product") without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA's written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product's quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before customers use the Product, create designs including the Product, or incorporate the Product into their own applications, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application with which the Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications. TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.
- PRODUCT IS NEITHER INTENDED NOR WARRANTED FOR USE IN EQUIPMENTS OR SYSTEMS THAT REQUIRE EXTRAORDINARILY HIGH LEVELS OF QUALITY AND/OR RELIABILITY, AND/OR A MALFUNCTION OR FAILURE OF WHICH MAY CAUSE LOSS OF HUMAN LIFE, BODILY INJURY, SERIOUS PROPERTY DAMAGE AND/OR SERIOUS PUBLIC IMPACT ("UNINTENDED USE"). Except for specific applications as expressly stated in this document, Unintended Use includes, without limitation, equipment used in nuclear facilities, equipment used in the aerospace industry, medical equipment, equipment used for automobiles, trains, ships and other transportation, traffic signaling equipment, equipment used to control combustions or explosions, safety devices, elevators and escalators, devices related to electric power, and equipment used in finance-related fields. IF YOU USE PRODUCT FOR UNINTENDED USE, TOSHIBA ASSUMES NO LIABILITY FOR PRODUCT. For details, please contact your TOSHIBA sales representative.
- Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any
  applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE FOR PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (2) DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO SALE, USE OF PRODUCT, OR INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without
  limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile
  technology products (mass destruction weapons). Product and related software and technology may be controlled under the
  applicable export laws and regulations including, without limitation, the Japanese Foreign Exchange and Foreign Trade Law and the
  U.S. Export Administration Regulations. Export and re-export of Product or related software or technology are strictly prohibited
  except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. Please use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. TOSHIBA ASSUMES NO LIABILITY FOR DAMAGES OR LOSSES OCCURRING AS A RESULT OF NONCOMPLIANCE WITH APPLICABLE LAWS AND REGULATIONS.