TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC4520BP, TC4520BF

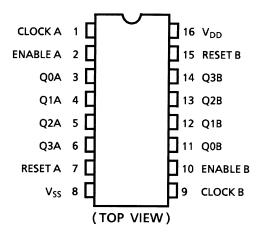
TC4520B Dual Binary Up Counter

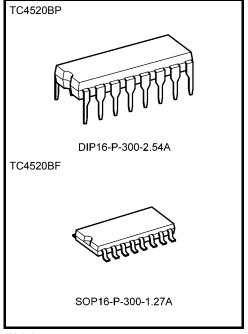
TC4520B is up counters of 4 bit binary.

Since both of TC4520B contain two independent circuits of counters with the same functions in one package, counting or frequency division of eight binary bits can be achived with one IC. The counters can be reset to "0" (Q0 to Q3 = "L") by giving "H" level signal to RESET input regardless of other inputs.

The counting condition is changed by the rising edge of CLOCK input if ENABLE = "H" or by the falling edge of ENABLE if CLOCK = "L".

Pin Assignment



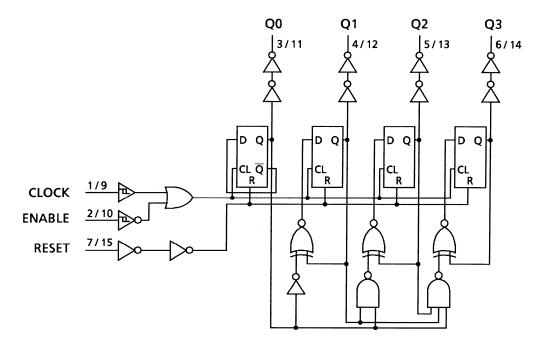


Weight

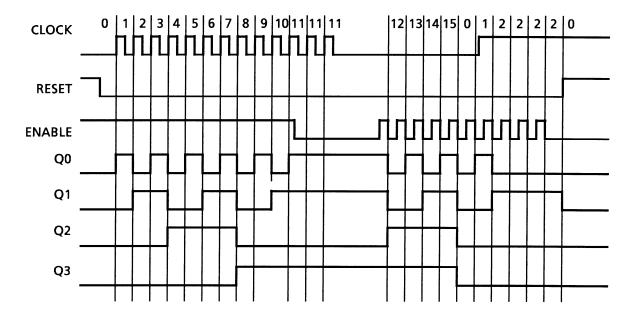
DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.)

Logic Diagram

1/2 TC4520B



Timing Chart



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2014-03-01

Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
DC supply voltage	V_{DD}	V_{SS} – 0.5 to V_{SS} + 20	V
Input voltage	V _{IN}	$V_{SS} - 0.5$ to $V_{DD} + 0.5$	٧
Output voltage	V _{OUT}	$V_{SS} - 0.5$ to $V_{DD} + 0.5$	٧
DC input current	I _{IN}	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T _{opr}	-40 to 85	°C
Storage temperature range	T _{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (V_{SS} = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V_{DD}	_	3	_	18	V
Input voltage	V _{IN}		0	_	V_{DD}	V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{DD} or V_{SS} .

Static Electrical Characteristics ($V_{SS} = 0 V$)

		Sym-	Test Condition		–40°C		25°C		85°C				
Charac	teristics	bol		V _{DD} (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit	
High-level output voltage		V _{OH}	$ I_{OUT} < 1 \mu A$ $V_{IN} = V_{SS}, V_{DD}$	5 10	4.95 9.95	_ _	4.95 9.95	5.00 10.00	_ _	4.95 9.95	_ _	٧	
		*IN *33, *DD	15	14.95	_	14.95	15.00	_	14.95	_			
.			V_{OL} $ I_{OUT} < 1 \mu A$ $V_{IN} = V_{SS}, V_{DD}$	5	_	0.05	_	0.00	0.05	_	0.05		
Low-level voltage	output	V_{OL}		10	_	0.05	_	0.00	0.05	_	0.05	V	
			VIN - VSS, VDD	15		0.05	_	0.00	0.05	_	0.05		
			V _{OH} = 4.6 V	5	-0.61		-0.51	-1.0	_	-0.42	_		
			V _{OH} = 2.5 V	5	-2.5	_	-2.1	-4.0	_	-1.7	_	mA	
Output hig	h current	I _{OH}	V _{OH} = 9.5 V	10	-1.5	_	-1.3	-2.2	_	-1.1	_		
			V _{OH} = 13.5 V	15	-4.0	_	-3.4	-9.0	_	-2.8	_		
			$V_{IN} = V_{SS}, V_{DD}$										
		V _{OL} = 0.4 V	5	0.61		0.51	1.2	_	0.42	_			
0			V _{OL} = 0.5 V	10	1.5	_	1.3	3.2	_	1.1	_	^	
Output low current	l _{OL}	V _{OL} = 1.5 V	15	4.0	_	3.4	12.0	_	2.8	_	mA		
		$V_{IN} = V_{SS}, V_{DD}$											
			V _{OUT} = 0.5 V, 4.5 V	5	3.5	_	3.5	2.75	_	3.5	_		
		.,	V _{OUT} = 1.0 V, 9.0 V	10	7.0	_	7.0	5.5	_	7.0	_	.,	
Input high	voltage	V _{IH}	V _{OUT} = 1.5 V, 13.5 V	15	11.0	_	11.0	8.25	_	11.0	_	V	
			I _{OUT} < 1 μA										
			V _{OUT} = 0.5 V, 4.5 V	5		1.5	_	2.25	1.5	_	1.5		
		.,	V _{OUT} = 1.0 V, 9.0 V	10	_	3.0	_	4.5	3.0	_	3.0	,	
Input low voltage	V _{IL}	V _{OUT} = 1.5 V, 13.5 V	15	_	4.0	_	6.75	4.0	_	4.0	V		
		I _{OUT} < 1 μA											
Input	"H" level	l _{IH}	V _{IH} = 18 V	18		0.1	_	10 ⁻⁵	0.1	_	1.0	^	
current	"L" level	I _{IL}	V _{IL} = 0 V	18	_	-0.1	_	-10 ⁻⁵	-0.1	_	-1.0	μΑ	
			$V_{IN} = V_{SS}, V_{DD}$ (Note)	5	_	5	_	0.005	5	_	150		
Quiescent current	supply	I _{DD}		10	_	10	_	0.010	10	_	300	μА	
- 3				15	_	20	_	0.015	20		600		

Note: All valid input combinations.

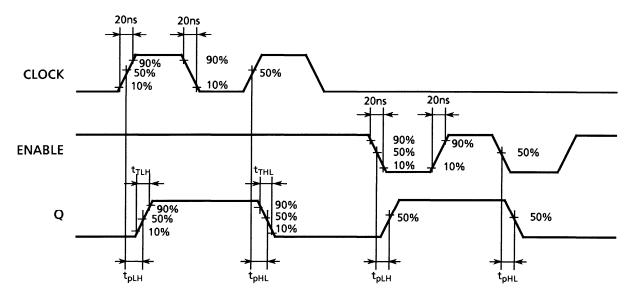
Dynamic Electrical Characteristics (Ta = 25°C, V_{SS} = 0 V, C_L = 50 pF)

Characteristics Output transition time (low to high) Output transition time	Symbol t _{TLH}	_	V _{DD} (V) 5 10	Min —	Тур. 70	Max	Unit
(low to high)	tTLH	_		_	70		
(low to high)	t _{TLH}	_	10		, ,	200	
			Ī	_	35	100	ns
Output transition time			15	_	30	80	
Output transition time			5	_	70	200	
(high to low)	t_{THL}	_	10	_	35	100	ns
(high to low)			15	_	30	80	
Decreasion delevitima			5	_	160	560	
Propagation delay time	t _{pLH}	_	10	_	75	230	ns
(CLOCK, ENABLE-Q)	t _{pHL}		15	_	60	160	
			5	_	110	560	
Propagation delay time	t _{pHL}	_	10	_	55	230	ns
(RESET-Q)	•		15	_	40	160	
	t _{CL}		5	1.5	6	_	
Max clock frequency		_	10	3	14	_	MHz
			15	4	18	_	
	t _{rCL}	_	5				
Max clock input rise/fall time			10	No limit			μS
			15				
			5				
Max input rise/fall time	t _r	_	10	No limit			μS
(ENABLE)	t _f		15				
	t _W		5	_	30	200	
Min clock pulse width		_	10	_	15	100	ns
·			15	_	10	70	
			5	_	35	250	
Min pulse width	t_W	_	10	_	20	110	ns
(ENABLE)			15	_	15	80	
			5	_	45	250	
Min pulse width	t _{WH}	_	10	_	20	110	ns
(RESET)	""		15	_	15	80	
	t _{rem}		5	_	_	0	
Min removal time		_	10	_	_	0	ns
(RESET-CLOCK, ENABLE)			15	_	_	0	
Input capacitance	C _{IN}	_	<u> </u>	_	5	7.5	pF

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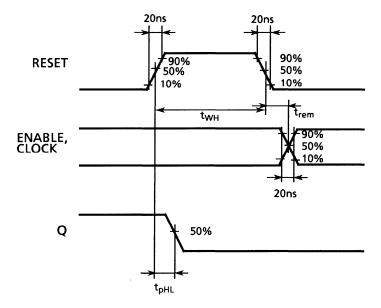
Waveforms for Measurement of Dynamic Characteristics

Waveform 1



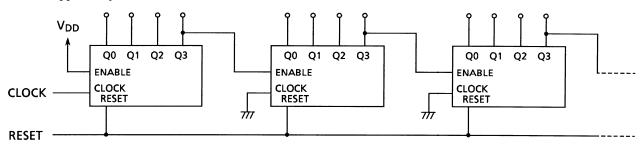
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Waveform 2

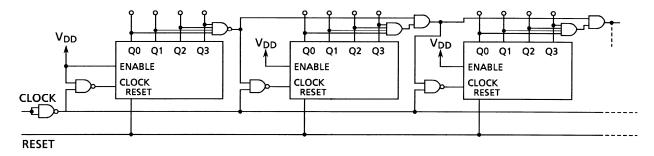


Application Circuit

Ripple carry counter



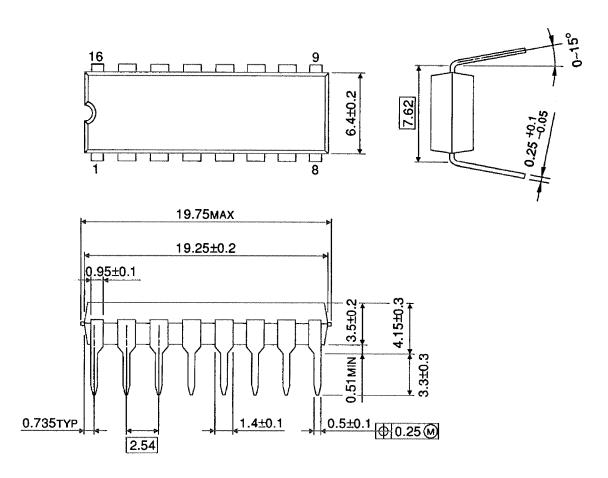
Ripple carry counter



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Package Dimensions

DIP16-P-300-2.54A Unit: mm

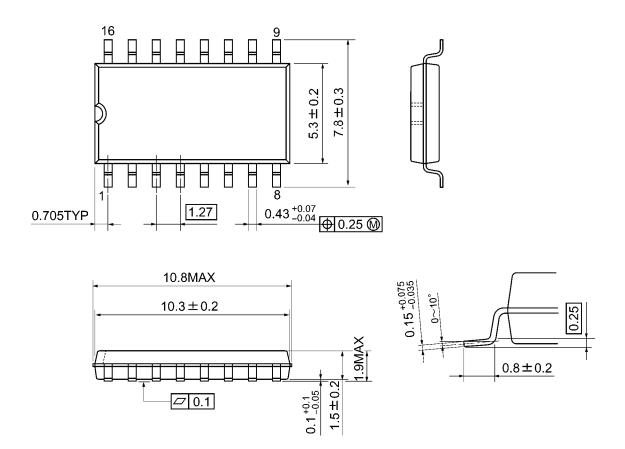


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Weight: 1.00 g (typ.)

Package Dimensions

SOP16-P-300-1.27A Unit: mm



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Weight: 0.18 g (typ.)

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