TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type ($L^2 - \pi - MOS V$)

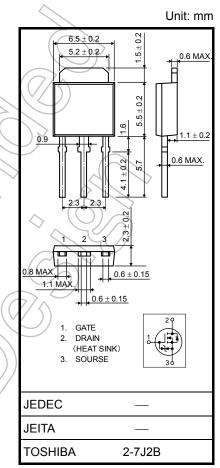
2SK4019

Chopper Regulators, DC-DC Converters and Motor Drive Applications

- 4-V gate drive
- Low drain-source ON-resistance: R_{DS (ON)} = 0.17 Ω (typ.)
- High forward transfer admittance: |Y_{fs}| = 4.5 S (typ.)
- Low leakage current: I_{DSS} = 100 μA (max) (V_{DS} = 100 V)
- Enhancement mode: V_{th} = 0.8 to 2.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Character	istic	Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	100	A
Drain-gate voltage (R _{GS} = 20 kΩ)		V _{DGR}	100	V
Gate-source voltage		V _{GSS}	±20	> v
Drain current	DC (Note 1)	I _D	5	А
	Pulse (Note 1)	I _{DP}	20	A
Drain power dissipatio	n (Tc = 25°C)	P _D <	20	W
Single-pulse avalanch	e energy (Note 2)	EAS	180	Em
Avalanche current		IAR	5	А
Repetitive avalanche e	energy (Note 3)	EAR	2	mJ
Channel temperature		Tch	150	2%
Storage temperature r	ange	∕7, _{stg}	-55 to 150	°C



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.)

may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristic	Symbol	Мах	Unit
Thermal resistance, channel to case	Rth (ch-c)	6.25	°C / W
Thermal resistance, channel to ambient	R _{th (ch−a)}	125	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 25 V, T_{ch} = 25°C (initial), L = 11.6 mH, R_G = 25 Ω , I_{AR} = 5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

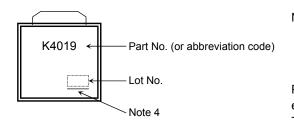
Electrical Characteristics (Ta = 25°C)

Chara	cteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	—	±10	μA
Drain cutoff curr	rent	IDSS	V _{DS} = 100 V, V _{GS} = 0 V	_	—	100	μA
Drain-source br voltage	reakdown	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	100	_	_	V
Gate threshold	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	0.8)?	2.0	V
Drain-source ON-resistance		D	V _{GS} = 4 V, I _D = 2.5 A		0.22	0.30	Ω
		R _{DS} (ON)	V _{GS} = 10 V, I _D = 2.5 A	\bigcirc	0.17	0.23	
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 2.5 A	2.0	4.5	_	S
Input capacitance Reverse transfer capacitance		Ciss	(\bigcirc)	_	500	_	
		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	80	1	pF
Output capacitance		C _{oss}		_	(190	\rightarrow	
- Switching time -	Rise time	tr	V _{GS} ^{10V} _{OV}		517		
	Turn-on time	t _{on}	$\begin{array}{c} 0.03 0.01 \mathbf{k} \\ 0 \mathbf$		25	_	ns
	Fall time	t _f		$\overline{\mathbb{Q}}$	50		113
	Turn-off time	t _{off}	$V_{DD} \approx 50V$ Duty $\leq 1\%$, t _w = 10 μ s) _	195	—	
Total gate charge (Gate-source plus gate-drain)		Qg	$\bigcirc > \bigcirc))$	_	22	—	
Gate-source charge		Qgs	$V_{DD} \approx 80 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}$	—	15	—	nC
Gate-drain ("Miller") charge		Qgd		_	7	—	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)		-	_	_	5	А
Pulse drain reverse current (Note 1)	IDRP	-		_	20	А
Forward voltage (diode)	VDSF	I _{DR} = 5 A, V _{GS} = 0 V	-	_	-1.7	V
Reverse recovery time	t _{rr}	D _R = 5 A, V _{GS} = 0 V, dI _{DR} / dt = 50 A / μs	-	160	_	ns
Reverse recovery charge	Qrr	μ	_	0.28	_	μC

Marking

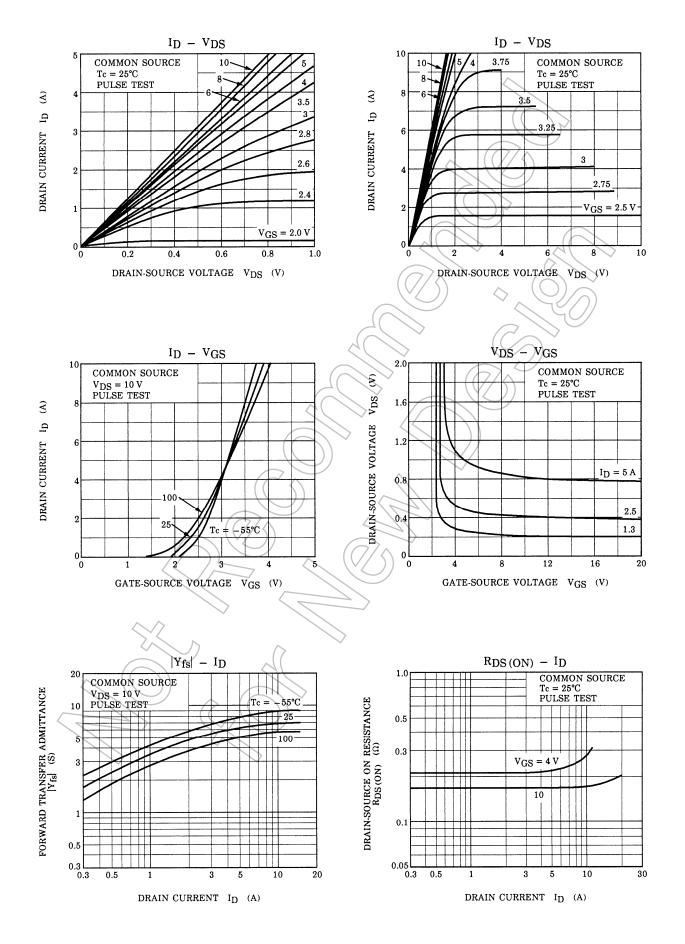


Note 4: A line under a Lot No. identifies the indication of product Labels.

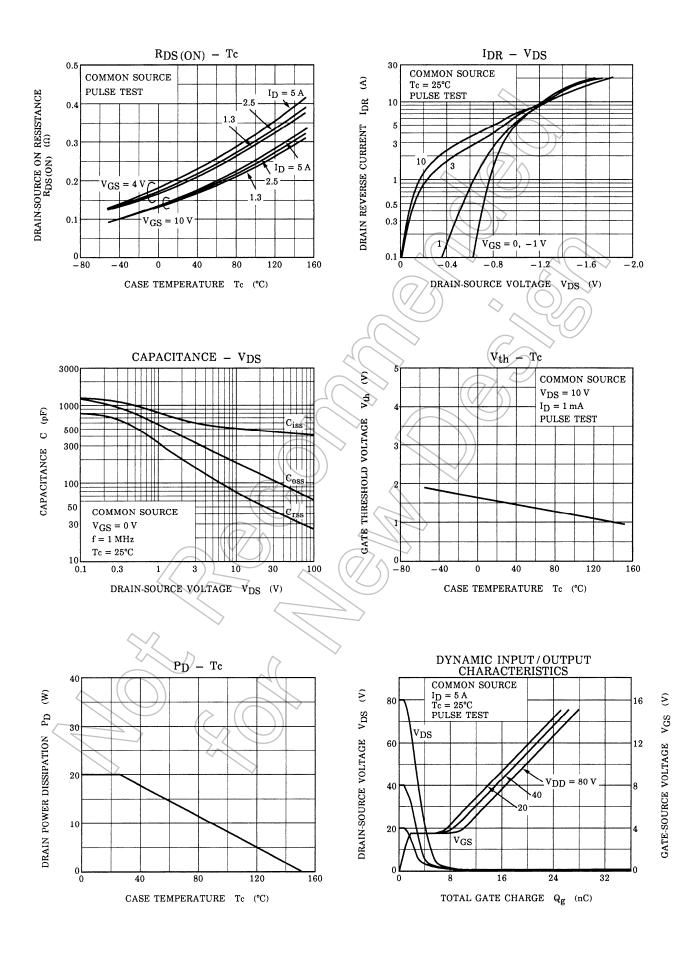
[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

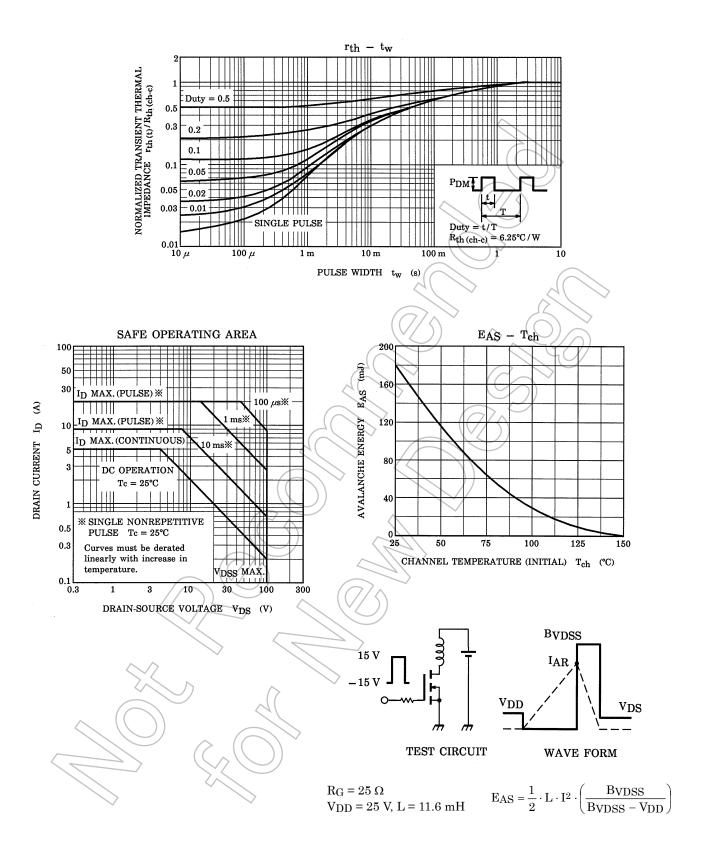
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