

MOSFETs Silicon N-channel MOS (U-MOSVIII-H)

SSM3K361R

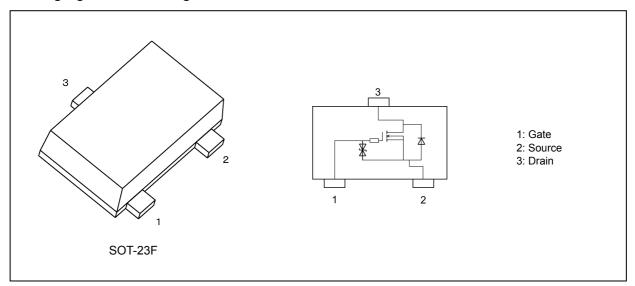
1. Applications

- Power Management Switches
- · DC-DC Converters

2. Features

- (1) AEC-Q101 qualified (Please see the orderable part number list)
- (2) 175 °C MOSFET
- (3) 4.5 V drive
- (4) Low drain-source on-resistance
 - $$\begin{split} : R_{DS(ON)} = 65 \text{ m}\Omega \text{ (typ.) (@V_{GS} = 4.5 V)} \\ R_{DS(ON)} = 51 \text{ m}\Omega \text{ (typ.) (@V_{GS} = 10 V)} \end{split}$$

3. Packaging and Pin Assignment



4. Orderable part number

Orderable part number	AEC-Q101		Note	
SSM3K361R,LF			General Use	
SSM3K361R,LXGF	YES	(Note 1)	Unintended Use	(Note 1)
SSM3K361R,LXHF	YES		Automotive Use	

Note 1: For more information, please contact our sales or use the inquiry form on our website.

Start of commercial production



5. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

Characteristics			Symbol	Rating	Unit
Drain-source voltage			V _{DSS}	100	V
Gate-source voltage			V_{GSS}	±20	
Drain current (DC)		(Note 1)	I_D	3.5	Α
Drain current (pulsed)		(Note 1), (Note 2)	I _{DP}	14	
Power dissipation		(Note 3)	P_{D}	1.2	W
Power dissipation	(t = 10 s)	(Note 3)	P_{D}	2.4	
Single-pulse avalanche energy		(Note 4)	E _{AS}	9.1	mJ
Avalanche current			I _{AR}	3.5	Α
Channel temperature		(Note 5)	T _{ch}	175	°C
Storage temperature		(Note 5)	T _{stg}	-55 to 175	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note 1: Ensure that the channel temperature does not exceed 175 °C.
- Note 2: Pulse width \leq 10 ms, Duty \leq 1 %
- Note 3: Device mounted on a 25.4 mm × 25.4 mm × 1.6 mm FR4 glass epoxy board (Cu pad: 645 mm²)
- Note 4: V_{DD} = 25 V, T_{ch} = 25 °C (Initial state), L = 1 mH, R_G = 25 Ω
- Note 5: The definitions of the absolute maximum channel and storage temperatures are qualified per AEC-Q101.
- Note: This transistor is sensitive to electrostatic discharge and should be handled with care.
- Note: The MOSFETs in this device are sensitive to electrostatic discharge. When handling this device, the worktables, operators, soldering irons and other objects should be protected against anti-static discharge.
- Note: The channel-to-ambient thermal resistance, R_{th(ch-a)}, and the drain power dissipation, P_D, vary according to the board material, board area, board thickness and pad area. When using this device, be sure to take heat dissipation fully into account.

6. Thermal Characteristics

Characteristics		Symbol	Max	Unit
Channel-to-ambient thermal resistance	(Note 1)	R _{th(ch-a)}	125	°C/W

Note 1: Device mounted on an 25.4 mm \times 25.4 mm \times 1.6 mm FR4 glass epoxy board (Cu pad: 645 mm²)



7. Electrical Characteristics

7.1. Static Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 16 \text{ V}$	_	_	±10	μА
Drain cut-off current		I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V		_	10	
Drain-source breakdown voltage		V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	100	_	_	V
Drain-source breakdown voltage	(Note 1)	V _{(BR)DSX}	I _D = 10 mA, V _{GS} = -20 V	80	_	_	
Gate threshold voltage	(Note 2)	V _{th}	V_{DS} = 10 V, I_{D} = 0.1 mA	1.5	_	2.5	٧
Drain-source on-resistance	(Note 3)	R _{DS(ON)}	I _D = 1 A, V _{GS} = 4.5 V	_	65	92	mΩ
			I _D = 2 A, V _{GS} = 10 V	_	51	69	

Note 1: If a reverse bias is applied between gate and source, this device enters V_{(BR)DSX} mode. Note that the drainsource breakdown voltage is lowered in this mode.

Note 2: Let V_{th} be the voltage applied between gate and source that causes the drain current (I_D) to below (0.1 mA for this device). Then, for normal switching operation, $V_{GS(ON)}$ must be higher than V_{th} , and $V_{GS(OFF)}$ must be lower than V_{th} . This relationship can be expressed as: $V_{GS(OFF)} < V_{th} < V_{GS(ON)}$. Take this into consideration when using the device.

Note 3: Pulse measurement.

7.2. Dynamic Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V},$	_	430	_	pF
Reverse transfer capacitance	C _{rss}	f = 1 MHz	_	22	_	
Output capacitance	C _{oss}		_	160	_	
Switching time (rise time)	t _r	$V_{DD} = 30 \text{ V}, I_D = 1.0 \text{ A},$	_	9	_	ns
Switching time (turn-on time)	t _{on}	V_{GS} = 0 to 4.5 V, R_{GS} = 50 Ω Duty \leq 1 %, Input: t_r , t_f < 5 ns,	_	21	_	
Switching time (fall time)	t _f	Common source,	_	7	_	
Switching time (turn-off time)	t _{off}	See Chapter 7.3	_	16	_	

7.3. Switching Time Test Circuit

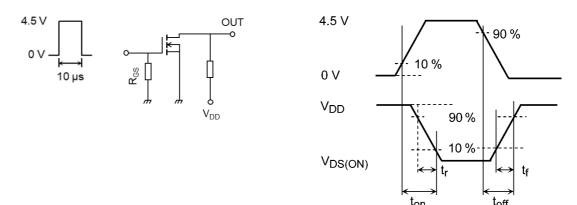


Fig. 7.3.1 Switching Time Test Circuit

Fig. 7.3.2 Input Waveform/Output Waveform

7.4. Gate Charge Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} = 50 \text{ V}, I_D = 2.0 \text{ A},$	_	3.2	_	nC
Gate-source charge 1	Q _{gs1}	$V_{GS} = 4.5 \text{ V}$	_	1.1	_	
Gate-drain charge	Q_{gd}		_	1.5		

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7.5. Source-Drain Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Diode forward voltage	(Note 1)	V_{DSF}	$I_{DR} = 3.5 \text{ A}, V_{GS} = 0 \text{ V}$		0.9	1.5	٧

Note 1: Pulse measurement.

8. Marking

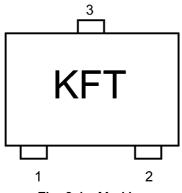


Fig. 8.1 Marking



9. Characteristics Curves (Note)

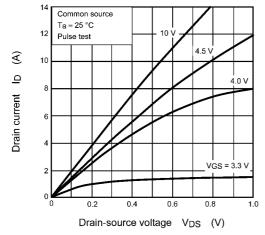


Fig. 9.1 I_D - V_{DS}

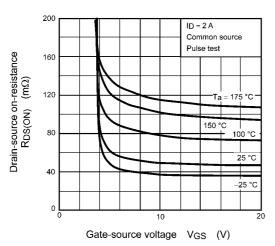


Fig. 9.3 R_{DS(ON)} - V_{GS}

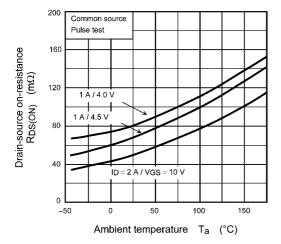


Fig. 9.5 R_{DS(ON)} - T_a

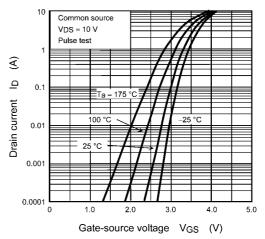


Fig. 9.2 ID - VGS

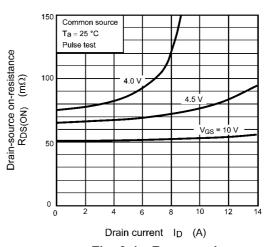


Fig. 9.4 R_{DS(ON)} - I_D

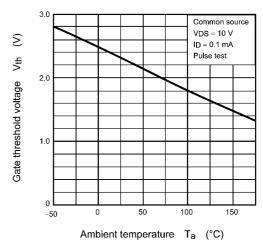


Fig. 9.6 V_{th} - T_a



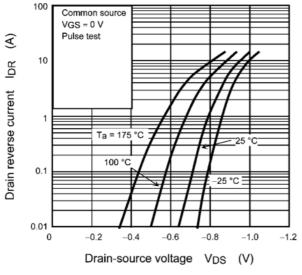


Fig. 9.7 IDR - VDS

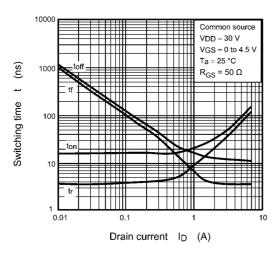


Fig. 9.9 t - I_D

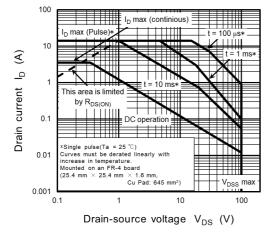


Fig. 9.11 Safe Operating Area

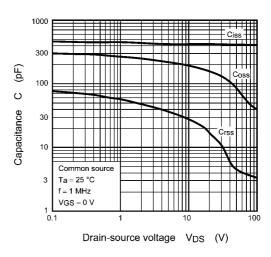


Fig. 9.8 C - V_{DS}

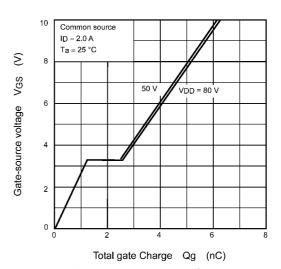


Fig. 9.10 Dynamic Input Characteristics

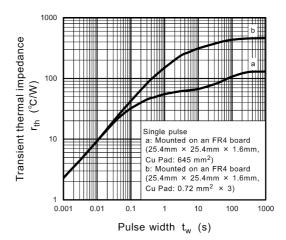
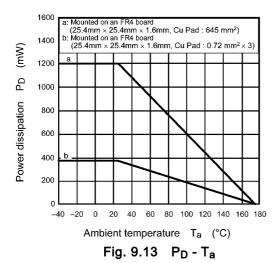


Fig. 9.12 r_{th} - t_w



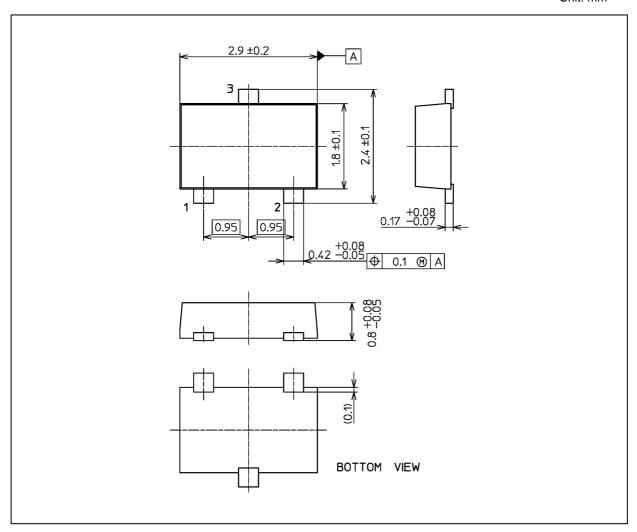


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

Unit: mm



Weight: 0.011 g (typ.)

	Package Name(s)
Nickname: SOT-23F	

Rev.12.0



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