

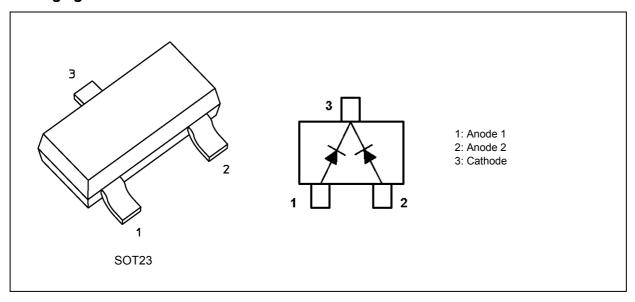
Switching Diodes Silicon Epitaxial Planar

TBAV70

1. Applications

· Ultra-High-Speed Switching

2. Packaging and Internal Circuit



3. Absolute Maximum Ratings (Note) (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Note	Rating	Unit
Peak reverse voltage	V_{RM}		85	V
Reverse voltage	V _R		80	
Average rectified current	I _O	(Note 1)	215	mA
Peak forward current	I _{FM}	(Note 1)	500	
Non-repetitive peak forward surge current	I _{FSM}	(Note 1), (Note 2)	2	Α
Power dissipation	P _D	(Note 3)	320	mW
Junction temperature	Tj		150	°C
Storage temperature	T _{stg}		-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Unit rating. Total rating = Unit rating \times 1.5

Note 2: Measured with a 10 ms pulse.

Note 3: Mounted on an FR4 board (25.4 mm \times 25.4 mm \times 1.6 mmt, Cu pad: 0.42 mm² \times 3)

Start of commercial production



4. Electrical Characteristics (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V _F (1)	I _F = 1 mA	_	_	0.715	V
	V _F (2)	I _F = 10 mA	_	_	0.855	
	V _F (3)	I _F = 50 mA	_	_	1.0	
	V _F (4)	I _F = 150 mA	_	_	1.25	
Reverse current	I _R (1)	V _R = 25 V	_	_	30	nA
	I _R (2)	V _R = 80 V	_	_	500	
	I _R (3)	V _R = 25 V, T _j = 150 °C	_	_	30	μА
	I _R (4)	V _R = 80 V, T _j = 150 °C	_	_	100	
Total capacitance	Ct	V _R = 0 V, f = 1 MHz	_	0.9	_	pF
Reverse recovery time	t _{rr}	I _F = 10 mA, See Fig. 4.1.	_	1.6	4.0	ns

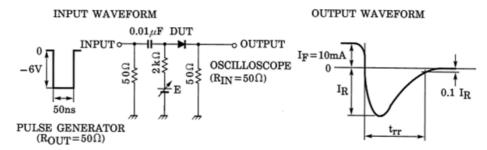


Fig. 4.1 Reverse recovery time (t_{rr}) Test circuit

5. Marking

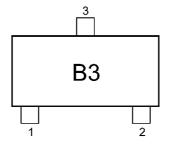
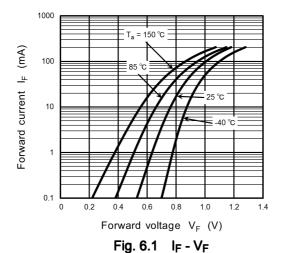


Fig. 5.1 Marking



6. Characteristics Curves (Note)



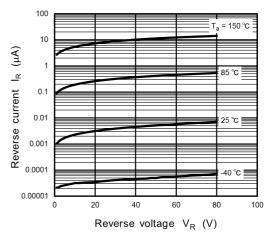
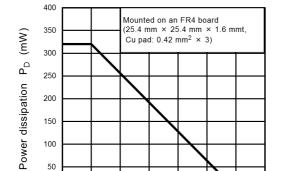


Fig. 6.2 I_R - V_R



0

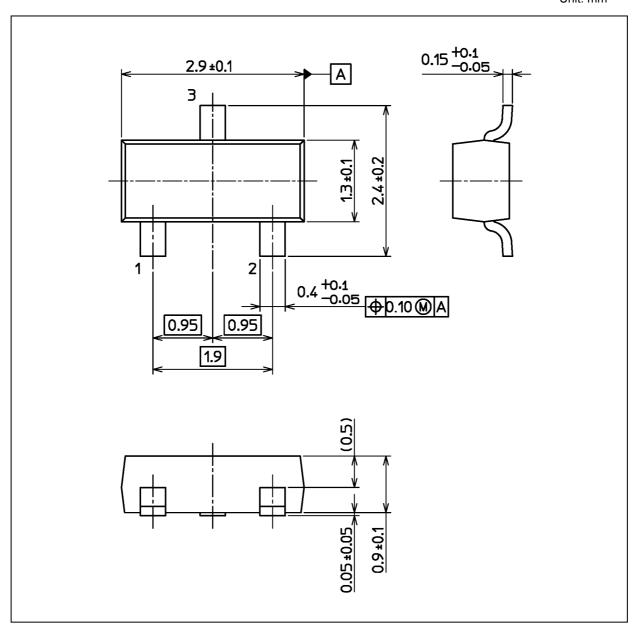
Ambient temperature T_a (°C) **Fig. 6.3** $P_D - T_a$

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

Unit: mm



Weight: 0.009 g (typ.)

	Package Name(s)
JEDEC: SOT-23	
Nickname: SOT23	



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