Overview

This application note explains the applications of the LDO regulator TCR5BM/8BM series for battery-powered mobile devices to power supplies for MCUs, including various features and characteristics of this product. Please refer to this document when designing the power supply for MCUs using TCR5BM/8BM Series.
# Table of Contents

1. INTRODUCTION ........................................................................................................................................... 3

2. REQUIRED CHARACTERISTICS FOR POWER SUPPLY FOR MCU ...................................................... 4

3. APPLICATION CIRCUIT ............................................................................................................................... 5
   3.1. Typical Connection Diagram .................................................................................................................. 5
   3.2. Parts list .................................................................................................................................................. 5

4. CIRCUIT AND BOARD DESIGN .................................................................................................................. 6
   4.1. Points to note in the design .................................................................................................................... 6
   4.2. Dropout voltage: $V_{DO}$ ....................................................................................................................... 8
   4.3. Load transient response: Delta $V_{OUT}$ ............................................................................................... 10
   4.4. Ripple rejection ratio: R.R. ..................................................................................................................... 12
   4.5. Other electrical characteristics .............................................................................................................. 14
   4.6. Other Functions .................................................................................................................................... 15

5. PRODUCT OVERVIEW ................................................................................................................................. 17
   5.1. TCR5BM/8BM series ............................................................................................................................ 17
   5.2. Appearance and terminal arrangement .................................................................................................. 18
   5.3. Internal circuit block diagram ............................................................................................................... 18
   5.4. Product List .......................................................................................................................................... 19
   5.5. Terminal Description ............................................................................................................................. 20

6. CONCLUSION ............................................................................................................................................... 20

RESTRICTIONS ON PRODUCT USE ............................................................................................................. 21
1. Introduction

Low-drop out regulator ICs (hereinafter “LDOs”) are widely used inside various electronic devices, in mobile devices such as smartphones and tablets they are particularly required to be smaller and lighter in size and save power.

By using an external bias pin separately from the input pin as the power supply line for the output drive, TCR5BM/8BM series achieves extremely low dropout characteristics of 100 mV (typical) for TCR5BM series and 170 mV (typical) for TCR8BM series, while ensuring stable operation at low voltages and high output currents.

Both series have a fixed output voltage lineup, ranging from 0.8 V to 3.6 V, and have a maximum output current of 500 mA (typical) for TCR5BM series and 800 mA (typical) for TCR8BM series, allowing you to use the optimum output voltage for power supplies for a wide range of applications. DFN5B packages (1.2 mm×1.2 mm×0.38 mm) are also used, contributing to smaller and thinner sets.

Please refer to the datasheet for details of TCR5BM/8BM series, such as the characteristics of the series. For details of the main characteristics of the LDO, refer to the link destination below.

Download TCR5BM Series Data Sheets: -> [Click Here]

Download TCR8BM Series Data Sheets: -> [Click Here]

For the power efficiencies of the 2-power supply type LDOs -> [Click Here]

For the load transient responses of LDOs -> [Click Here]

For ripple rejection ratio -> [Click Here]

For oscillations of LDOs -> [Click Here]

For optimization of thermal designs -> [Click Here]
2. Required Characteristics for Power Supply for MCU

As shown in Fig. 2.1, the power supplies generally supplied to the MCU include core power supplies, reference power supplies, and general-purpose power supplies, each of which requires different performances. This guide especially describes the applications of the LDO regulator TCR5BM/8BM series to core power supplies with low-voltage, high-current loads.

Currently, the mainstream voltage for MCU core power supplies is 1.0 to 1.2 V. However, this voltage tends to be lower, and is expected to reach around 0.8 to 1.2 V in the future.

A characteristic of the MCU core power supply is that the load current of several hundred mA or more instantaneously increases or decreases as the peak current when the CPU performs heavy processing as the current consumption of the core part. If the output voltage fluctuates greatly with respect to a sudden increase or decrease in the load current, a malfunction of the control logic, etc. may occur, causing malfunction of the MCU to cause a malfunction of the entire set. For this reason, load transient response characteristics are most important. In addition, to avoid logic malfunction due to noise, the core power supply must provide a stable low voltage and must also be capable of removing switching noise from PMIC and DC-DC converters, i.e. high ripple rejection ratios.

Since the MCU itself also generates heat, it is also necessary to reduce power consumption in order to minimize the impact on the surrounding area as much as possible.

The two-input LDO regulator TCR5BM/8BM series offers the performance of low-fluctuation load transient response, high ripple rejection ratio, and low power dissipation required for these MCU core power supplies in a small area. This section describes the specific circuit configuration.

Depending on the expected output current, select TCR5BM series if the output current is 500 mA or less, or select TCR8BM series if the output current is 500 mA or more.
3. Application circuit

3.1. Typical Connection Diagram

![Typical Connection Diagram](image)

Figure 3.1 Applied Circuits for TCR5BM/TCR8BM Series MCUs

3.2. Parts list

<table>
<thead>
<tr>
<th>Item</th>
<th>Part</th>
<th>Quantity</th>
<th>Manufacturer</th>
<th>Value</th>
<th>Part name</th>
<th>Description</th>
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<td>TOSHIBA</td>
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<td></td>
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<td></td>
<td>1 μF</td>
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<td>0.6×0.3 (0201)</td>
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<td>-</td>
<td>0.6×0.3 (0201)</td>
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<td>Ceramic 10 V ±20 %</td>
<td>-</td>
<td>1.6×0.8 (0603)</td>
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</table>

* In the circuit diagram and bill of materials, the xx part of the product name is a number indicating the output voltage.
  e.g. TCR5BM12A/TCR8BM12A when \( V_{OUT} = 1.2 \) V
4. Circuit and Board Design

4.1. Points to note in the design

This section describes the points to note in designing circuits and boards when configuring MCUs' core power supplies using TCR5BM/8BM series.

- About VBIAS inputs
  
  VBIAS pin is the power supply for the circuits that drive the output elements. Therefore, input a voltage that is 2.5 V or higher and higher than VOUT voltage is +1.4 V, and use as stable a power supply as possible for the external voltage. Noise, etc., may adversely affect the output voltage through the drive circuit of this LDO. Also, set the power supply startup sequences in order of VBIAS -> VIN -> CONTROL.

- External Capacitors
  
  Ceramic capacitors can be used with this LDO, but depending on the type, they may have very large temperature characteristics. When selecting a capacitor, consider the operating environment thoroughly. We also recommend ceramic capacitors with ESR of 1.0 Ω or less. For stable operation, connect a capacitor of 1 μF or more to the VIN pin, 0.1 μF or more to VBIAS pin, and 2.2 μF or more to VOUT pin.

- Mounting on a Printed Circuit Board
  
  Although the LDO is designed with sufficient consideration for oscillation such as built-in phase compensation capacitance, oscillation may occur due to the influence of wiring resistance and L component depending on the pattern of the printed circuit board and external conditions such as the operating environment.
  
  As a general precaution, route the wiring of VIN and GND so as not to form a loop, and make the wiring area as large as possible to reduce the wiring resistance. Pay special attention to the routing of the path through which I/O current flows so that VIN and GND inside the LDO are not affected by the common impedance. Be aware that VBIAS wiring is also susceptible to noises if it is routed too long.
  
  Generally, the influence of routing of wires can be solved by optimizing the value and position of the capacitor while checking the output waveform on the actual device. However, even if there is no influence, use a capacitor with a value larger than that shown in the previous section. When the output capacitor is apart from VOUT pin of the LDO, the output capacitor is easily affected by the wiring resistor and L-component. For more stable operation of the power supply, mount the output capacitor as close to VOUT pin as possible, and separate the wiring connected to the output current from the wiring flowing the output current as close to VOUT pin as possible (if possible, at the root of the pin), so that the wiring resistance does not affect as a common impedance.
  
  Figure 4.1 shows an image of routing of the wiring pattern.
Figure 4.1 Routing Image of Wiring Pattern

- **Allowable loss**
  Design the printed circuit board pattern to have as much margin as possible for heat generation during maximum loss (power consumption) in LDO assumed in actual use conditions. Consider the worst case of each parameter such as ambient temperature, input voltage and output current in the actual operating environment, and design with a derating of approximately 1.2 to 1.5 times the assumed maximum loss (power consumption).

- **Overcurrent protection circuit and overheat protection circuit**
  This LDO has built-in foldback type overcurrent protection circuit and overheat protection circuit, but these do not guarantee that the operation of the device is always kept within the maximum rating. Depending on the conditions of use, deterioration and reliability of the product may be affected. Nor does it guarantee that LDO will not destroy in any case. If the output and GND terminals of this LDO are in the imperfect short mode, the LDO may be damaged.

  When using this LDO, please refer to the above and the absolute maximum ratings stated in our "Semiconductor Reliability Handbook" and data sheets, etc., and pay attention not to exceed the absolute maximum ratings in any case considering the appropriate derating.

  In addition, it is recommended that adequate safety measures such as fail-safe be implemented in the set.
4.2. Dropout voltage: $V_{DO}$

In the data sheet, it is specified as the minimum value of the difference between the input voltage and the output voltage required to output a predetermined voltage. It may also be referred to as the minimum voltage difference between input and output. Lower values consume less power.

In TCR5BM/8BM series, the power supply route to the gate drive circuits of the output elements is separated from the input terminals (VINs), and independent bias terminals (VBIAS) are provided to achieve low dropout voltage.

The following curves shows the dropout voltage characteristics of TCR5BM/8BM series products suitable for the MCU-core power supply. Please refer to this curves when applying the MCU-core power supply.

![Dropout Voltage Curves](image-url)

Figure 4.2 Dropout Voltage Characteristics of TCR5BM Series ($V_{BIAS}=3.3\, \text{V}$, $V_{OUT}=0.8$ to $1.2\, \text{V}$)

* The maximum condition includes the full...
Figure 4.3 TCR8BM Series Dropout Voltage Characteristics (V_{BIAS}=3.3 V, V_{OUT}=0.8 to 1.2 V)

* The maximum condition includes the full
4.3. Load transient response: Delta $V_{\text{out}}$

Indicates the peak value of the fluctuation instantaneously appearing in the output voltage for the step change in the load current.

Since the resistance of the load connected to the output of the LDO is not constant, the output current also fluctuates accordingly. However, TCR5BM/8BM series has excellent response performance against sudden current changes and performs stable constant voltage operation. The following are examples of load-transient response waveforms for TCR5BM/8BM series products with $V_{\text{out}}$ values suitable for the MCU-core power supply. Refer to this table when applying the MCU-core power supply.

![Load Transient Response Waveforms](image_url)

Figure 4.4 Load Transient Responses of TCR5BM Series
($V_{\text{in}}=1.35\,\text{V},\, V_{\text{bias}}=3.3\,\text{V},\, I_{\text{out}}=1\,\text{mA-500\,mA-1\,mA},\, C_{\text{out}}=22\,\mu\text{F}$)
Figure 4.5 Load Transient Responses of TCR8BM Series

(V_{IN}=V_{OUT}+0.3 \text{ V}, V_{BIAS}=3.3 \text{ V}, I_{OUT}=1 \text{ mA-800 mA-1 mA}, C_{OUT}=22 \mu\text{F})
4.4. Ripple rejection ratio: R.R.

Also known as the PSRR (Power Supply Rejection Ratio), this is the ratio of the ripple voltage component included in the input voltage to the ripple voltage component appearing at the output voltage. A larger value indicates a smaller ripple component that appears in the output, but in some cases the denominator and numerator are interchanged and displayed as a minus, in which case the smaller one is better. In determining the influence of power supply noise on the load side, this characteristic is also very important for LDOs with low-voltage output. The following shows the frequency characteristics of the ripple rejection ratio of TCR5BM/8BM series products whose $V_{OUT}$ values are suitable for the core power supplies of MCUs. This section describes the characteristics when $C_{OUT}$ of outputs is 2.2 $\mu$F and 22 $\mu$F as a reference for determining external constants.

![Figure 4.6 Ripple Rejection Ratio Frequency Characteristics of TCR5BM Series](image-url)

$V_{OUT}=0.8 \text{ V}$

$V_{OUT}=0.9 \text{ V}$

$V_{OUT}=1.0 \text{ V}$

$V_{OUT}=1.05 \text{ V}$

$V_{OUT}=1.1 \text{ V}$

$V_{OUT}=1.2 \text{ V}$

*Figure 4.6 Ripple Rejection Ratio Frequency Characteristics of TCR5BM Series*  
$(V_{IN}=1.35 \text{ V}, V_{BIAS}=3.3 \text{ V}, I_{OUT}=10 \text{ mA})$
Figure 4.7 Ripple Rejection Ratio Frequency Characteristics of TCR8BM Series
($V_{IN}=V_{OUT}+0.3\ V$, $V_{BIAS}=3.3\ V$, $I_{OUT}=10\ mA$)
4.5. Other electrical characteristics

In addition to the three items described above, the data sheet also contains various electrical characteristics. These are also briefly described below. Data sheets contain values at Ta (ambient temperature) = 25 °C, but some items contain values within the range of Ta = -40 to 85 °C.

- **Output-voltage accuracy: \( V_{\text{OUT}} \)**
  The output voltage is distributed at a constant width with the set value as the center due to factors such as variations in the internal circuit elements. \( V_{\text{OUT}} \) shows the width, indicating maximum and minimum. TCR5BM/8BM series is lined up according to the output voltage value. If the output voltage is 1.8 V or less, the series is displayed as a ratio to the output voltage. If the output voltage is greater than 1.8 V, \( V_{\text{OUT}} \) is displayed as a voltage value.

- **Input stability: Reg • line**
  The amount of change of \( V_{\text{OUT}} \) with respect to \( V_{\text{IN}} \) when \( I_{\text{OUT}} \) is kept constant.

- **Load stability: Reg • load**
  The amount of change of \( V_{\text{OUT}} \) with respect to \( I_{\text{OUT}} \) when \( V_{\text{IN}} \) is kept constant.

- **Bias current: \( I_{\text{BIAS(ON)}}, I_{\text{IN(ON)}} \)**
  This value indicates the current that flows to the VIN pin or VBIAS pin when the LDO is operated with no load connected to the output and no output current applied. The smaller the value, the better the efficiency will be. Control pull-down current (below) is not included.

- **Standby current: \( I_{\text{BIAS(OFF)}}, I_{\text{IN(OFF)}} \)**
  This value indicates the current flowing to the VIN pin or VBIAS pin when the LDO operation is stopped by the control pin. This current is the standby current in the set, which causes the battery to drain. The smaller the current is the desirable item.

- **Control pull-down current: \( I_{\text{CT}} \)**
  Current flowing into the control terminal when the control terminal is set to HIGH.

- **Undervoltage protection threshold voltage: \( V_{\text{UVLO}} \)**
  Threshold value for the input voltage that performs undervoltage protection operation.

- **Output-voltage thermal coefficient: \( T_{\text{CVO}} \)**
  The temperature coefficient of the output voltage within the operating temperature range (-40 °C to 85 °C). Displays the change in output voltage when the temperature changes by 1 °C in ppm with respect to the output voltage at room temperature (25 °C).
Output-Noise Voltage: $V_{NO}$
This value indicates the magnitude of the noise component appearing in the output voltage.

Control Voltages: $V_{CT(ON)}$, $V_{CT(OFF)}$
Indicates the range of the applied voltage to the control terminals that turn LDO ON or OFF. This indicates that the LDO is ON within the control voltage (ON) range and OFF within the control voltage (OFF) range. Note that this does not indicate the distributions of ON/OFF switching thresholds. Be sure to set the voltage of HIGH signal applied to the control terminal and the voltage of the LOW signal to be within this voltage. The threshold is distributed between the control voltage (ON) and the control voltage (OFF).

Limit current: $I_{CL}$
Indicates the value of the output current at which the overcurrent protection operates. Wiring in which the output current flows should have a width that will not cause any problem even if this current flows at least.

Output Discharge On Resistance: $R_{SD}$
Indicates the resistance value that discharges the charge stored in the output capacitor when LDO is set to OFF.

4.6. Other Functions
The following functions are also built into TCR5BM/8BM series.

Overcurrent protection function
If the output current exceeds the limit current due to an abnormality or short-circuit in the load, the output voltage begins to drop and the output current also drops. This protects the unit from power consumption in the event of an abnormality. Circuits with such protective characteristics are called foldback types. It is also referred to as a "f-shaped characteristic".

Overheat protection function
If a large current continues to flow due to semi-short-circuit of the load, etc., and the temperature of the LDO exceeds the set value, the LDO is placed in standby and the output is shut off to prevent damage.

Inrush current suppression function
When the output is turned on, the charging current of the output capacitor flows. However, if this current is too large, the overcurrent protection circuit may malfunction and rise may be disabled, or overshoot may occur in the output voltage. To prevent this, this function limits the current flowing through the output capacitor.
• Low-voltage malfunction prevention function
  This function stops the operation of the LDO to prevent malfunction when the input voltage drops below the operating range.

• Auto Discharge Function
  This function discharges the remaining charge of the output capacitor when the output is turned off by control of the control terminal.
5. Product Overview

5.1. TCR5BM/8BM series

TCR5BM/8BM series are single-output LDO regulators of COMS technology with ultra-low dropouts, fast load transients, high ripple rejection ratio, inrush current suppression circuits, and control terminals. By adopting a function to provide an external bias voltage separately from the power input, the dropout voltage is 100 mV (typical) (1.1 V output, IOUT= 500 mA, VBIAS= 3.3 V) for TCR5BM series and 170 mV (typical) (1.1 V output, IOUT= 800 mA, VBIAS = 3.3 V) for TCR8BM series.

The output voltage can be selected from 0.8 V to 3.6 V with the voltage fixed type. The maximum output current of TCR5BM series is 500mA and that of TCR8BM series is 800mA. TCR5BM series and up to 800mA in TCR8BM series, and the overcurrent protection circuit, overheat protection circuit and auto discharge function are provided.

All products use an ultra-compact packaged DFN5B (1.2 mm x 2 mm x 0.38 mm) and small ceramic capacitors are applicable for inputs and outputs, making them ideal for applications requiring high-density mounting, such as portable devices.

Features

- Low dropout voltage
  - TCR5BM series VDO = 100 mV (typical) @ 1.1 V out, VBIAS= 3.3 V, IOUT= 500 mA
  - TCR8BM series VDO = 170 mV (typical) @ 1.1 V out, VBIAS= 3.3 V, IOUT= 800 mA
- Low standby current
  - TCR5BM/8BM series IBIAS(OFF) = 1 μA (max.)
- Low bias current
  - TCR5BM series IBIAS(ON) = 19 μA (typical) @ VBIAS = 5.3 V, IOUT= 0 mA
  - TCR8BM series IBIAS(ON) = 20 μA (typical) @ VBIAS = 5.5 V, IOUT= 0 mA
- Wide Output Voltage Lineup: (VOUT= 0.8 to 3.6 V)
- Built-in overcurrent protection circuit
- Built-in overheat protection circuit
- Inrush current suppression circuit built-in
- Built-in low-voltage malfunction prevention circuit
- Built-in auto discharge function
- Pull-down connections are provided between CONTROL and GND terminals.
- Ultra-small-outline package: DFN5B (1.2 mm x 1.2 mm x 0.38 mm)
5.2. Appearance and terminal arrangement

![Bottom View Illustration](image)

Figure 5.1 Appearance and Pin Layout of TCR5BM/8BM Series

5.3. Internal circuit block diagram

![Internal Circuit Block Diagram](image)

Figure 5.2 TCR5BM/8BM Series Internal Circuit Blocks

* The center electrode is GND.
### 5.4. Product List

#### TCR5BM series

Table 5.1 List of TCR5BM series products and marking

<table>
<thead>
<tr>
<th>Product name</th>
<th>$V_{OUT}$ (V) (Typical)</th>
<th>Marking</th>
<th>Product name</th>
<th>$V_{OUT}$ (V) (Typical)</th>
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#### TCR8BM series

Table 5.2 List of TCR8BM series products and marking

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<tr>
<th>Product name</th>
<th>$V_{OUT}$ (V) (Typical)</th>
<th>Marking</th>
<th>Product name</th>
<th>$V_{OUT}$ (V) (Typical)</th>
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5.5. Terminal Description

Table 5.3 TCR5BM/8BM Series Pin Descriptions

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<tr>
<th>Pin number</th>
<th>Pin name</th>
<th>Function</th>
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| 1          | VOUT    | Output terminal  
It is recommended to use a capacitor of 2.2 μF or more (ESR=1Ω or less) for stable operation. |
| 2          | VBIAS   | Bias power input pin  
The maximum input voltage of this pin is 6 V. It is recommended to use a capacitor of 0.1 μF or more (ESR=1 Ω or less) for stable operation. |
| 3          | CONTROL | Output ON/OFF control terminal  
When this pin is set to "High", the output is turned on, and when it is set to "Low", the output is turned off. This pin is pulled down internally. If it is OPEN, the output is turned off. |
| 4          | VIN     | Power input terminal  
The maximum input voltage of this pin is 6 V. For stable operation, it is recommended to use a capacitor of 1 μF or more (ESR=1 Ω or less). |
| 5          | GND     | Ground terminal |

6. Conclusion

The Toshiba LDO regulator uses a two-power circuit configuration, enabling high-power efficiency and low-loss, high-current, low-voltage output. In addition, high-speed load transient response at high current required for stable operation of the subsequent ICs, and high ripple compression over a wide frequency range allow the ICs to be used as the optimum power supply ICs for the core power supplies of MCUs. Refer to the data sheet for details of each characteristic.

Low-dropout LDO products with two high-performance power supplies

- 500mA LDO: Download TCR5BM datasheet from here -> Click Here
- 800mA LDO: Download TCR8BM datasheet from here -> Click Here
- 1.3A LDO: Download TCR13AGADJ datasheet from here -> Click Here
- 1.5A LDO: Download TCR15AG datasheet from here -> Click Here

For more information on using the LDO, see "Low-Drop-Out (LDO) Regulator IC Application Notes".
Low-Drop-Out (LDO) Regulator IC Application Note from here -> Click Here

Other LDO product lineup is from here -> Click Here
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