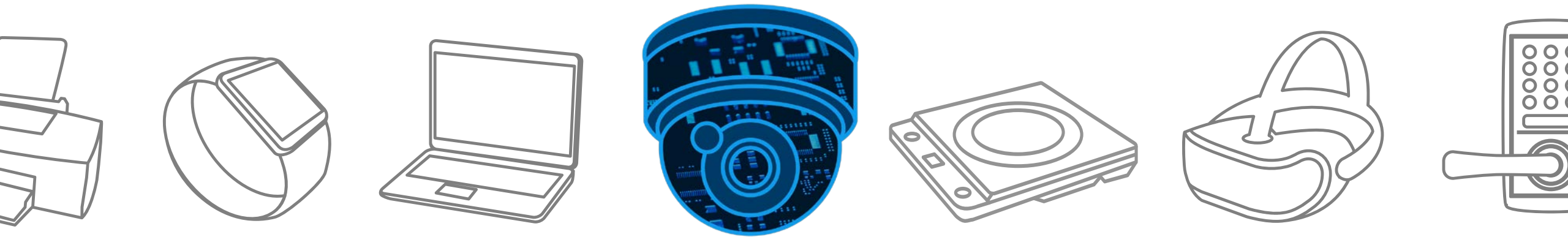
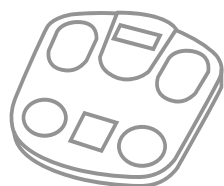
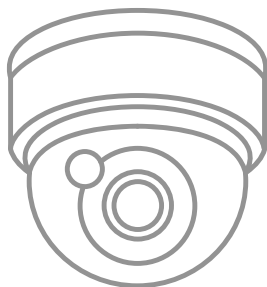
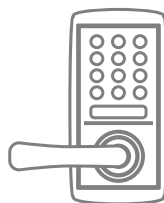


Surveillance Camera

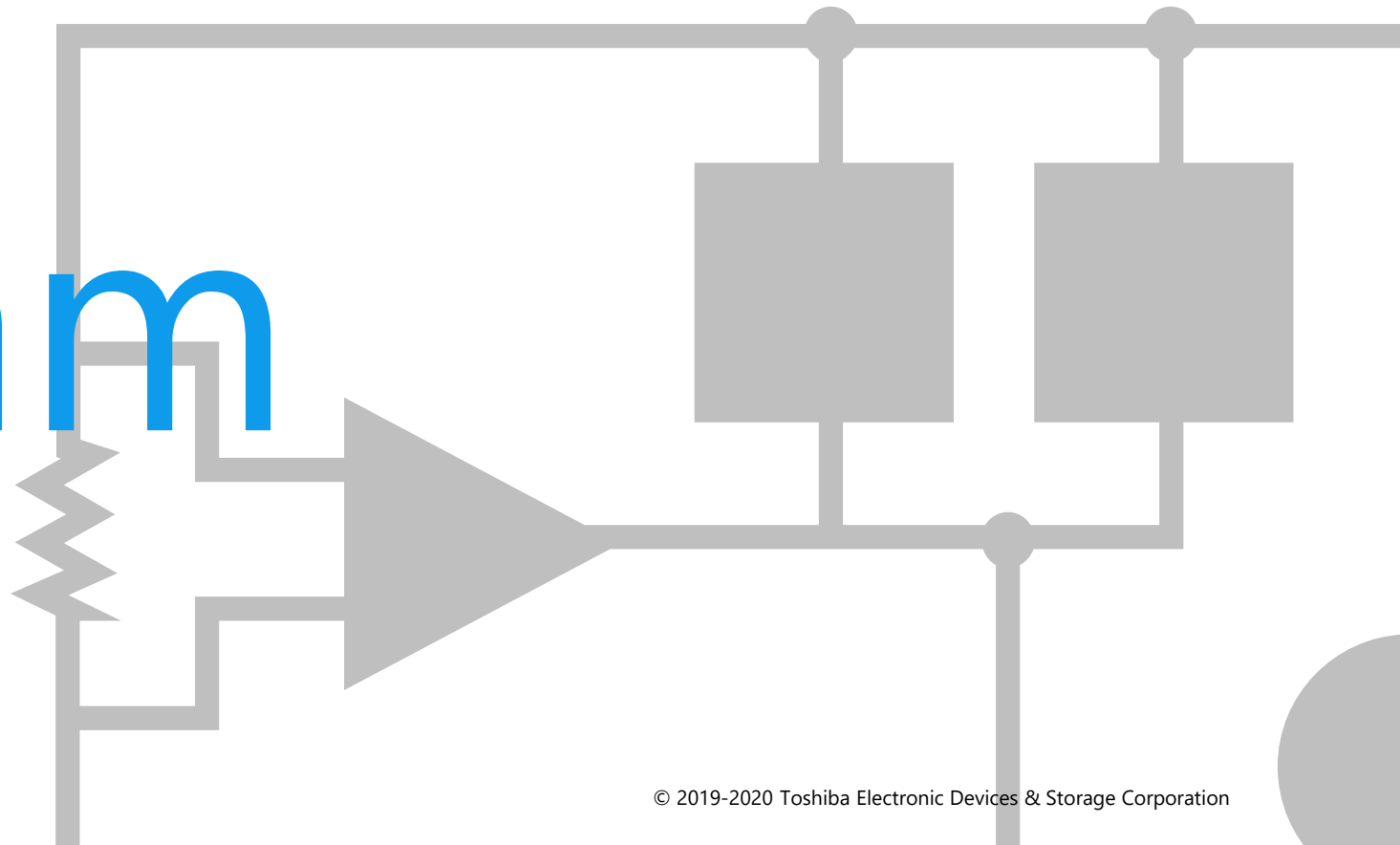
Solution Proposal by Toshiba



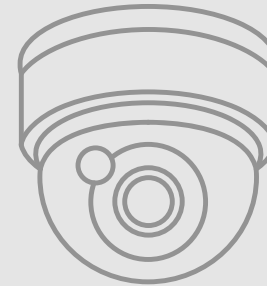
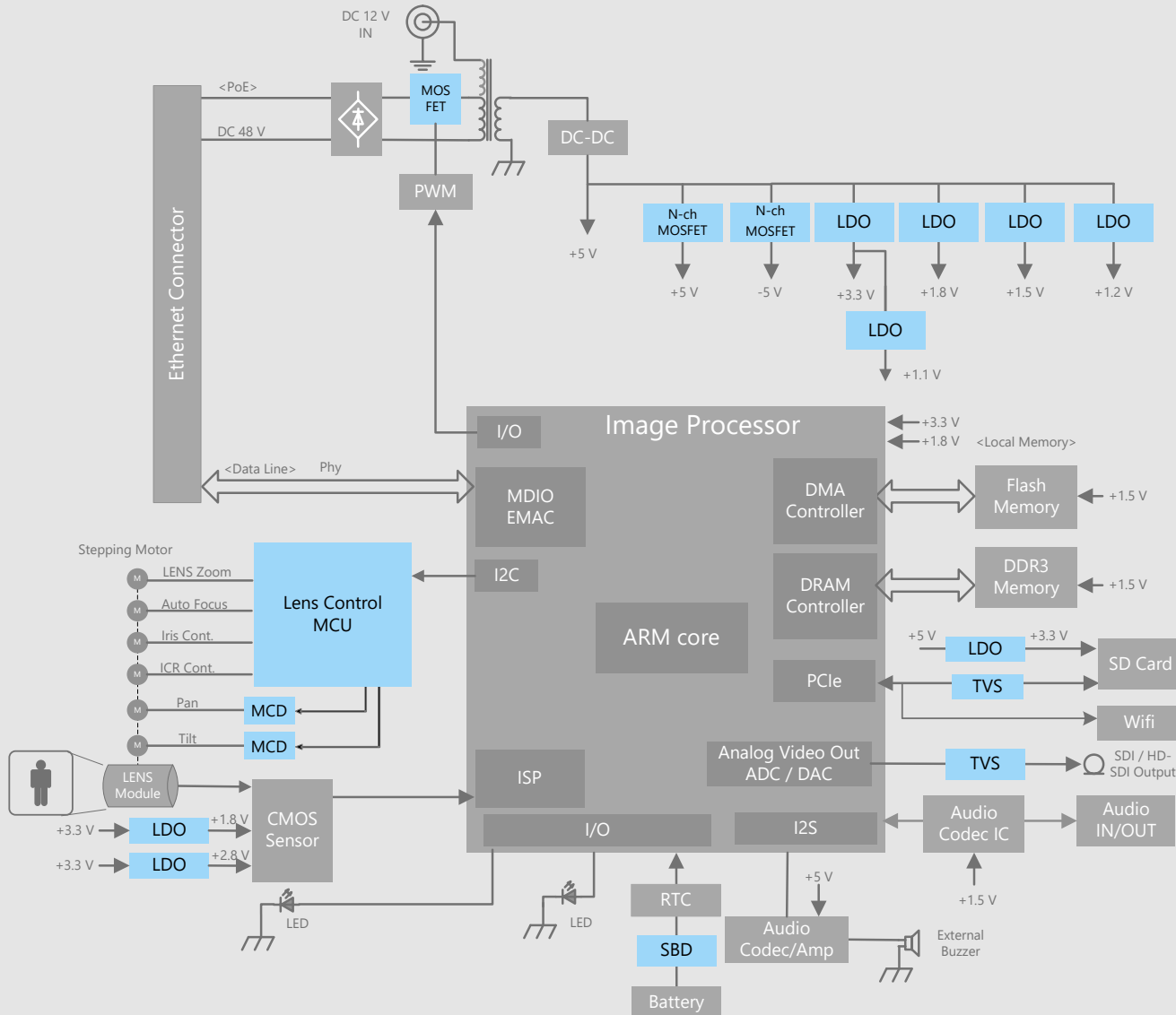


Toshiba Electronic Devices & Storage Corporation provides comprehensive device solutions to customers developing new products by applying its thorough understanding of the systems acquired through the analysis of basic product designs.

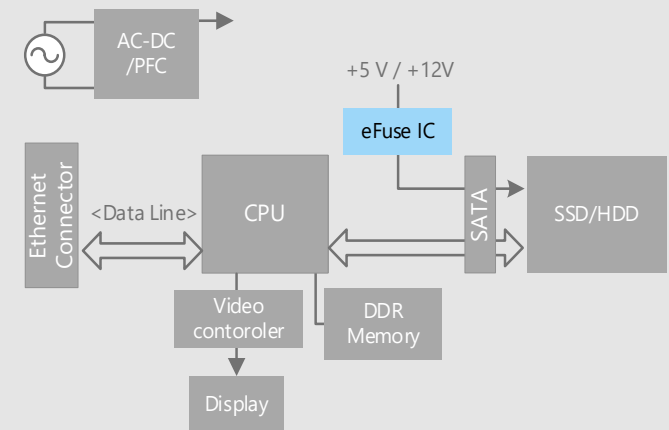
Block Diagram



Surveillance Camera Overall block diagram

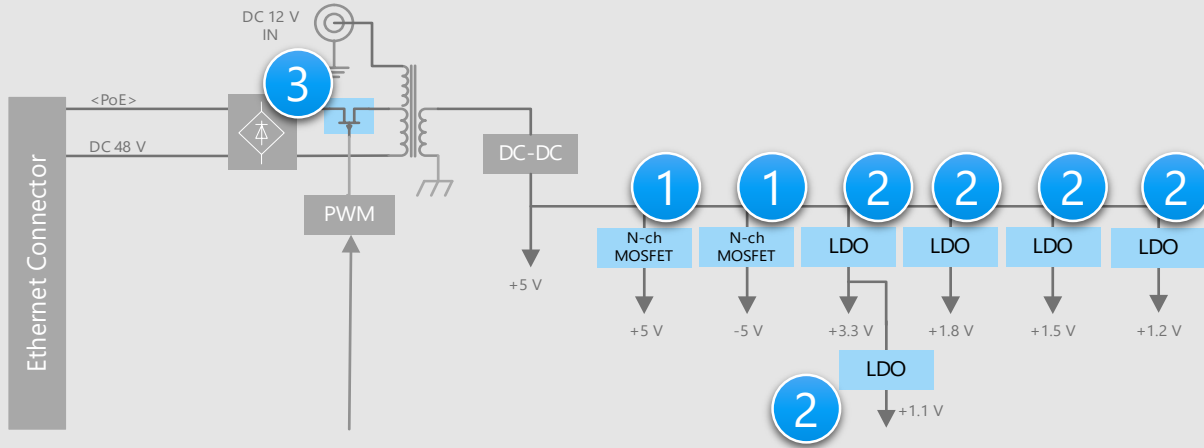


Recorder unit



Surveillance Camera Detail of power supply circuit (1)

Power supply



Power supply circuit of storage



※ Click the number in the circuit diagram to jump to the detailed description page

Criteria for device selection

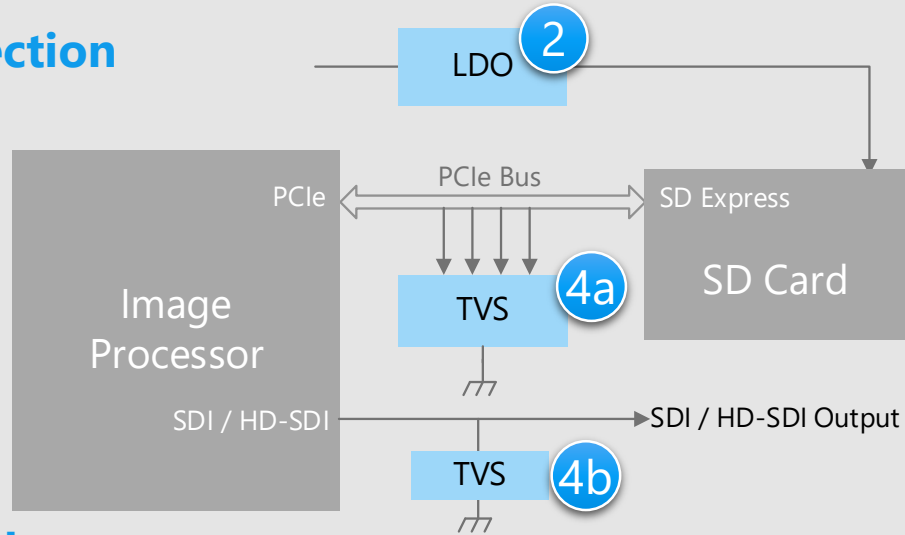
- V_{DSS} voltage is a key factor in selecting a MOSFET. Use at voltages exceeding V_{DSS} may cause MOSFET breakdown.
- The on-resistance ($R_{DS(ON)}$) of a MOSFET with high V_{DSS} is generally large.

Proposals from Toshiba

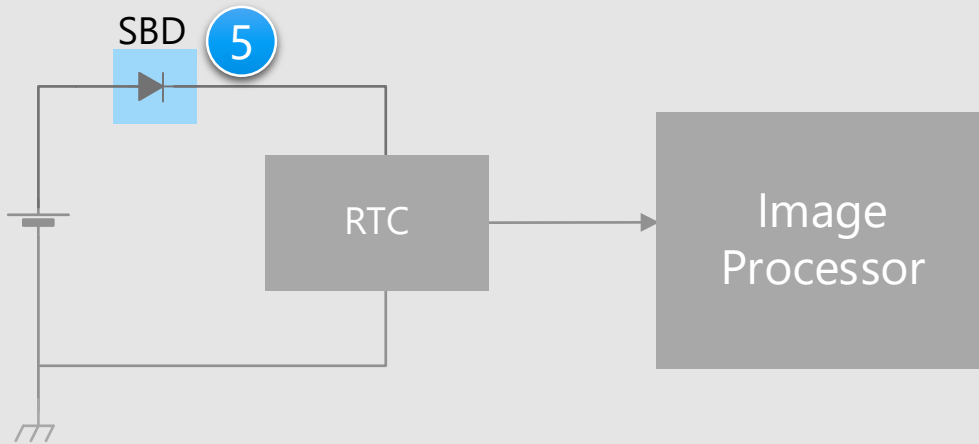
- **Realize a set with low power consumption by low on-resistance** ①
U-MOS series MOSFET (Trench type)
- **Realize a compact surface-mounted power supply resistant to noise** ②
LDO regulator
- **Optimal for high-efficiency power supply switching** ③
DTMOS IV MOSFET (Super junction type)
- **Robust protection function** ⑧
eFuse IC

Surveillance Camera Detail of power supply circuit (2)

Interface section



Power supply



※ Click the number in the circuit diagram to jump to the detailed description page

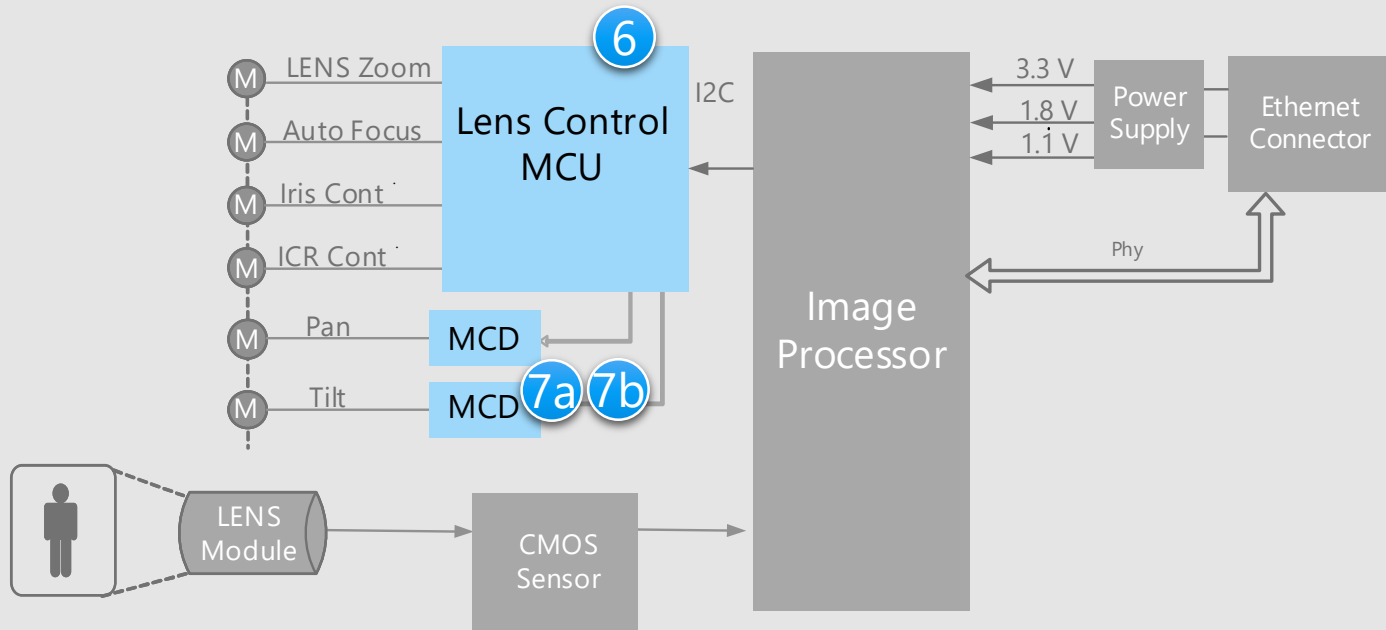
Criteria for device selection

- The PSRR of LDO is important for SD memory card I/F.
- TVS diode protects high-speed lines from external ESD.
- Low V_F and low I_R are required for SBD.

Proposals from Toshiba

- **Realize a compact surface-mounted power supply resistant to noise**
LDO regulator 2
- **Absorbing static electricity (ESD) from external terminals to prevent device breakdown and circuit malfunction**
TVS diode 4
- **High speed, low loss, compact surface mounting**
Schottky barrier diode (SBD) 5

Surveillance Camera Detail of camera motion section



Criteria for device selection

- Two or more motors can be driven at the same time by using lens control specialized MCU.
- The board area can be reduced by using small size package products.

Proposals from Toshiba

-Low power consumption Lens motor control MCU

MCU

6

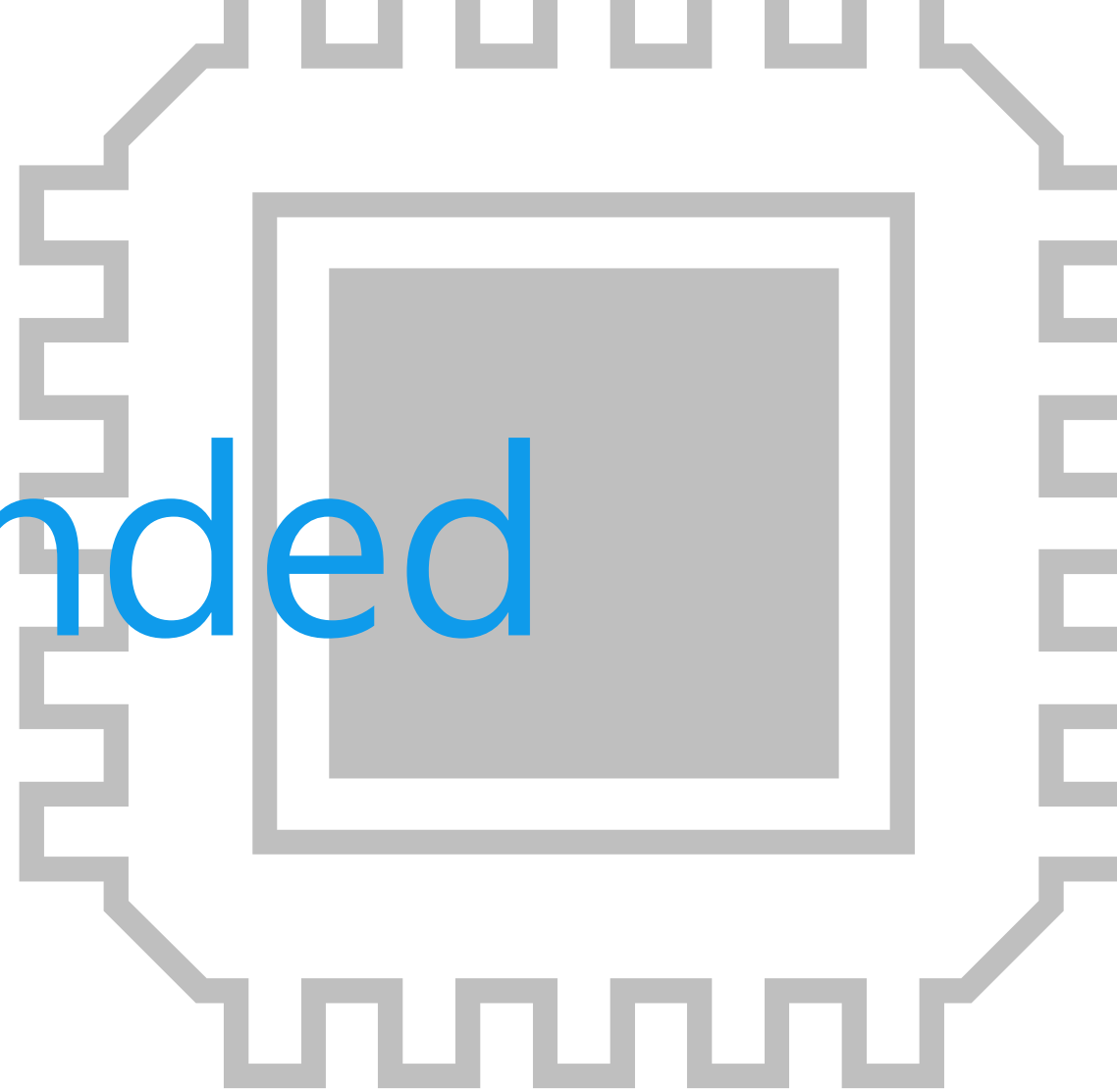
-Low ON register high efficient stepping motor control

Motor control driver

7a 7b

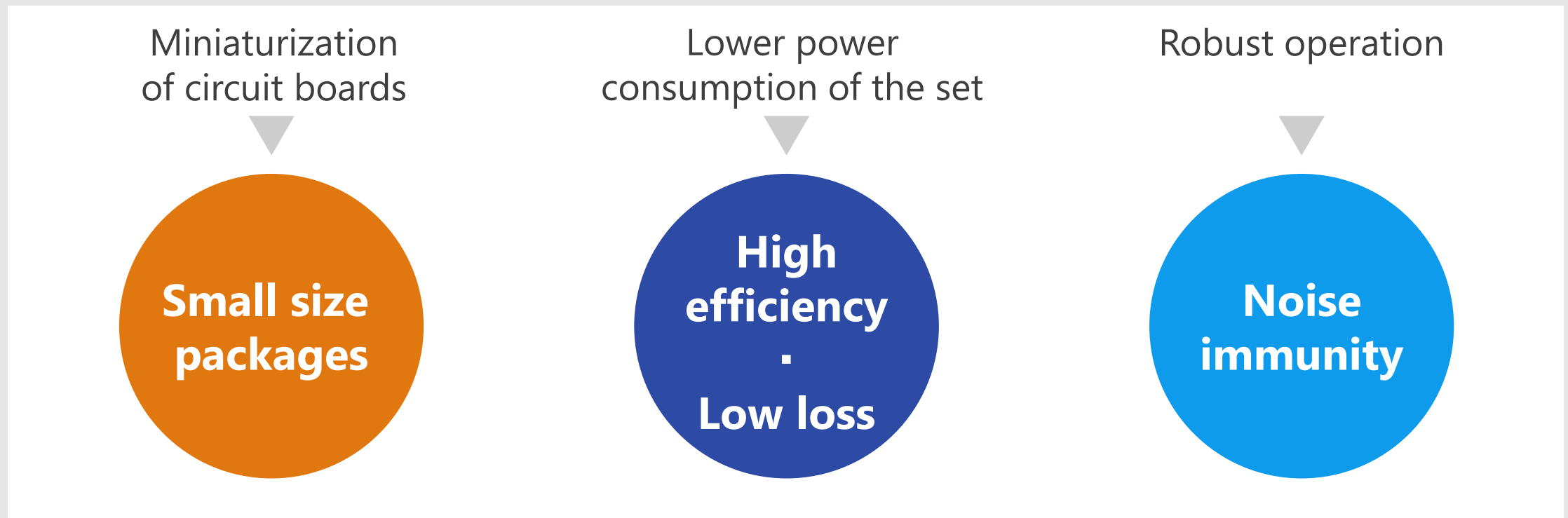
※ Click the number in the circuit diagram to jump to the detailed description page

Recommended Devices



Device solutions to address customer needs

As described above, in the design of surveillance cameras, “**Miniaturization of circuit boards**”, “**Low power consumption of set**” and “**Robust operation**” are important factors. Toshiba’s proposals are based on these three solution perspectives.



Device solutions to address customer needs

Small size packages

High efficiency
·
Low loss

Noise immunity

①	Small-signal MOSFET	●	●	
②	Small surface mount LDO regulator	●	●	●
③	Power MOSFET	●	●	●
④	TVS diode	●	●	●
⑤	Schottky barrier diode (SBD)	●	●	
⑥	LENS motor control MCU	●	●	
⑦	Motor control driver	●	●	
⑧	Electronic Fuse eFuse IC	●	●	

1 Small-signal MOSFET

SSM3K376R

Small size packages

High efficiency
Low loss

Noise immunity

Value provided

Suitable for power management switches and greatly contributes to miniaturization.

1 Low voltage drive

It drives at $V_{GS} = 1.8\text{ V}$.

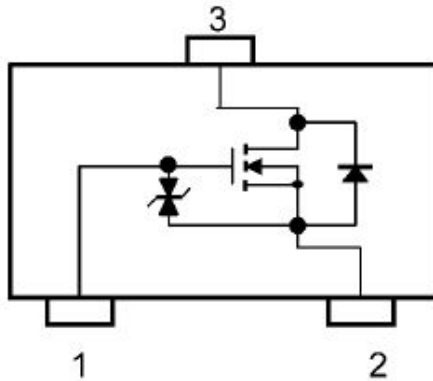
2 Low on-resistance

By reducing on-resistance between the source and drain, heat generation and power consumption can be kept low.

3 Compact package


Sealed in SOT-23F type packages.

SSM3K376R
Internal connection diagram



AEC-Q101 conformance
Please contact our sales representative for details.

Line up

Part number	SSM3K376R
Package	SOT-23F 
Polarity	N-ch
V_{DSS} [V]	30
I_D [A]	4
P_D [W]	1
$R_{DS(ON)}$ (Max) [$m\Omega$] @ $V_{GS} = 4.5\text{ V}$	56

[◆Return to Block Diagram TOP](#)

Value provided

Equipped overcurrent protection, high-speed load transient response and auto discharge functions.

1 Low dropout voltage

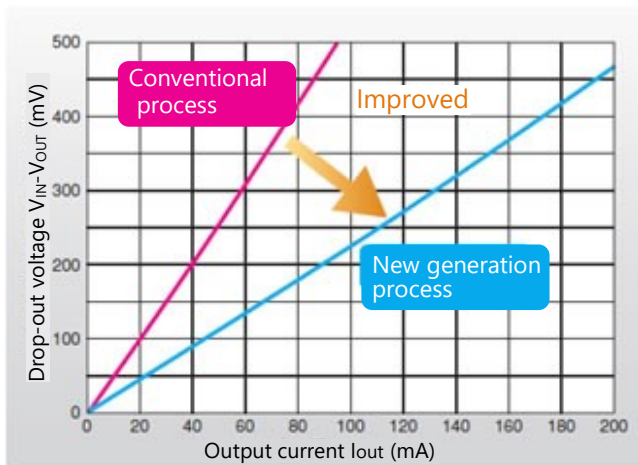
The drop-out characteristics is significantly improved by adopting newly developed new-generation process.

2 High ripple rejection


High ripple rejection R.R reject ripple effectively.

3 Can be used with ceramic capacitors

Improved drop-out characteristics enables to use ceramic capacitors for stabilization of operation.



Line up

Part number	TCR2EF series
Package	SMV 
V_{IN} (Max) [V]	6.0
I_{OUT} (Max) [mA]	200
Output voltage range [V]	1.0 to 5.0

[Return to Block Diagram TOP](#)

Value provided

Suitable for switching regulators and greatly contributes to miniaturization.

1 Fast switching speed

t_{on} (Typ.) = 14 [ns]
 t_{off} (Typ.) = 19 [ns]

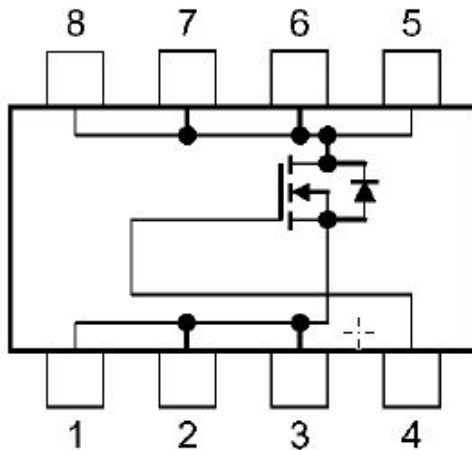
2 Low on-resistance

By reducing on-resistance between the source and drain, heat generation and power consumption can be kept low.


3 Enhancement

Enhancement MOSFET for easy handle

TPH5900CNH
Internal connection diagram



Line up

Part number	TPH5900CNH
Package	SOP Advance 
Polarity	N-ch
V_{DSS} [V]	150
I_D [A]	9
P_D [W]	42
$R_{DS(ON)}$ (Max) [m Ω] @ $V_{GS} = 10$ V	59

[Return to Block Diagram TOP](#)

Value provided

Absorbs static electricity (ESD) from external terminals, prevents circuit malfunction and protects devices.

1 Improved ESD pulse absorption

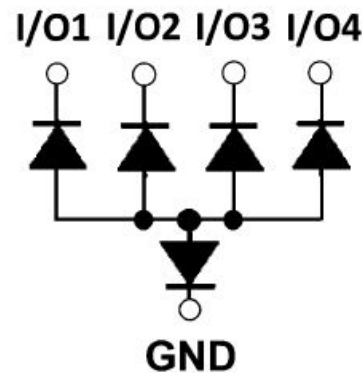
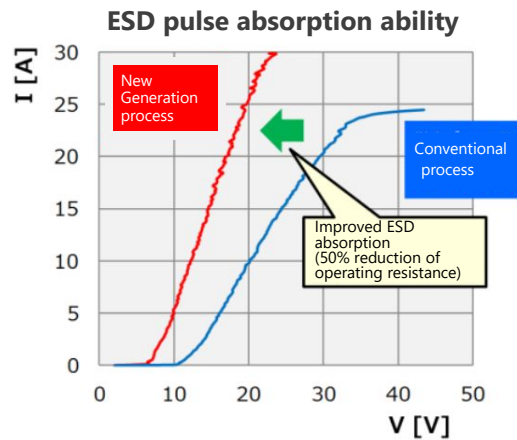
Improved ESD absorption compared to conventional products. (50 % reduction in operating resistance)
Both low operating resistance and low capacitance are realized and ensures high signal protection performance and signal quality.

2 Suppress ESD energy by low clamp voltage



Secure protection of connected circuits/devices was realized by using proprietary technology.

3 Optimal for high-density mounting

Four circuits on one chip contributes to saving of circuit board space.



Line up

Part number	DF10G5M4N	DF10G6M4N
Package	DFN10 	DFN10 
V_{ESD} [kV]	±20	±20
V_{RWM} (Max) [V]	3.6	5.5
C_T (Typ.) [pF]	0.2	0.2
R_{DYN} (Typ.) [Ω]	0.5	0.5

(NOTE) : This product is designed for ESD protection purpose and cannot be used for purposes other than ESD protection (including but not limited to voltage regulation applications).

[Return to Block Diagram TOP](#)

Value provided

Absorbs static electricity (ESD) from external terminals, prevents circuit malfunction and protects devices.

1 Improved ESD pulse absorption

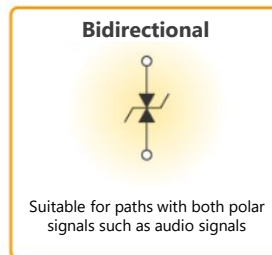
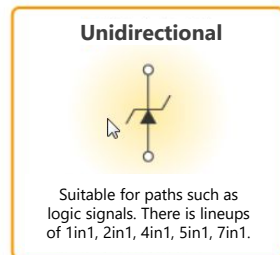
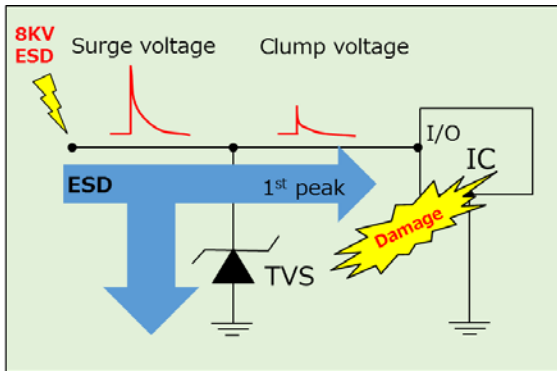
Both low operating resistance and low capacitance can realize and ensure high signal protection performance and signal quality.

2 Suppress ESD energy by low clamp voltage


Secure protection of connected circuits/devices was realized by using proprietary technology.

3 Optimal for high-density mounting

A variety of compact packages are available.



Line up

Part number	DF2B5M4ASL	DF2B6M4ASL
Package	SL2 	
V_{ESD} (Max) [kV]	±16	±15
V_{RWM} (Max) [V]	3.6	5.5
C_T (Typ.) [pF]	0.15	0.15
R_{dyn} (Typ.) [Ω]	0.7	0.7

(NOTE) : This product is designed for ESD protection purpose and cannot be used for purposes other than ESD protection (including but not limited to voltage regulation applications).

[◆Return to Block Diagram TOP](#)

5 Schottky barrier diode (SBD)

CUS10F30 / CTS05F40

Small size packages

High efficiency
Low loss

Noise immunity

Value provided

Applied to various applications which requires high speed and low loss, and greatly contributes to miniaturization.

1 Fast switching

Suitable for fast switching applications.

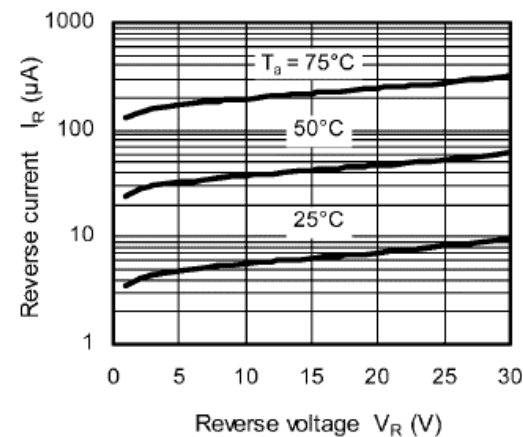
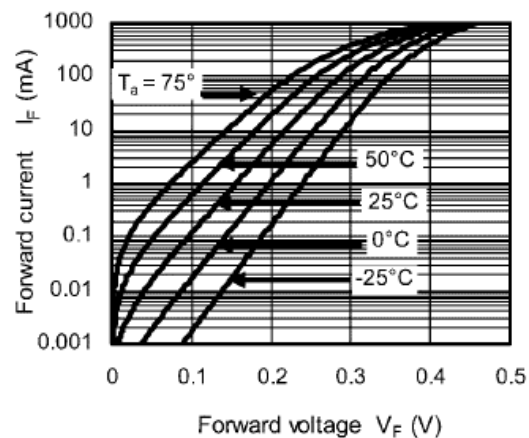
2 Be resistant to reverse voltage

Reverse voltage V_R can be applied up to 40 V.



3 Compact

Sealed in USC/CTS2B type packages.

CUS10F30 Characteristics



Line up

Part number	CUS10F30	CTS05F40
Package	USC 	CST2 
I_O (Max) [A]	1.0	0.5
V_R (Max) [V]	30	40
V_F (Typ.) [V] @ $I_F = 0.1$ A	0.43	0.74
I_R (Max) [μ A] @ $V_R = 10$ V	50	15

[Return to Block Diagram TOP](#)

Value provided

LENS control specialized MCU at small package and low power consumption

1 All in one solution for LENS control

- Silent & high speed multi-channel motor control
- Minimizing PCB area and number of components

2 Low power consumption

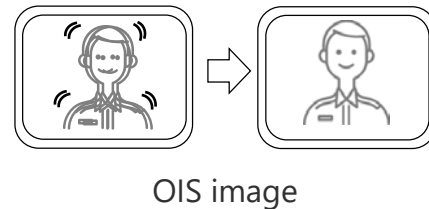
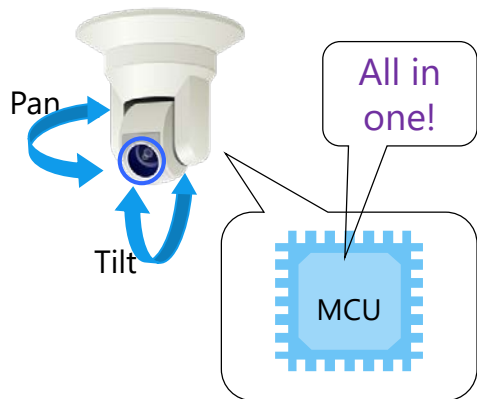
- Built-in Arm® Cortex® M3 and PSC co-processor
- Operation frequency reduction by distributed processing

3 High quality OIS control

- Packaging USC/CTS2B type. OIS supports over 20 Hz.
- User friendly sample software and evaluation board

*PSC: Programmable Servo Controller

*OIS: Optical Image Stabilizer



Line up

Part number	TMPM342FYXBG	TMPM343FXXBG
Package	VF8GA142	VBGA162
Package size [mm]	7 x 7 0.5 pitch	
CPU	Arm® Cortex®-M3, Max operation f 40 MHz	Arm® Cortex®-M3, Max operation f 50 MHz
Memory	Flash ROM 256 KB SRAM 32 KB	Flash ROM 512 KB / 1 MB SRAM 48 KB+32 KB / 64 KB+32 KB
Features	7bit resolution micro step function, PSC (342: 1 unit, 343: 4 unit) , 2-phase pulse counter (342: 2 ch, 343: 3 ch) , H-SW driver(342: 7.5 ch, 343: 8 ch) , µstep unit (342: 2 unit, 343: 3 unit)	

[◆Return to Block Diagram TOP](#)

Value provided

Possible to drive 2ch of bipolar type stepping motor

1 4 products line up

There are 2 control types, clock input and phase signal input as the I/F. Also there are two package type, SSOP and QFN, respectively.

2 Abnormality detection

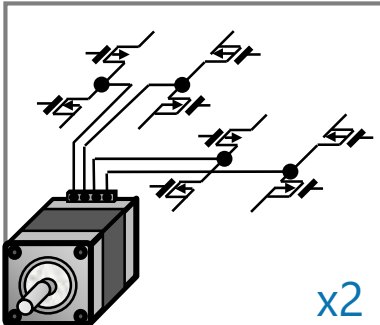
Various abnormality detection such as over limit current detection, over heat detection and power on reset contribute safety motor control.

3 3 selectable operation mode

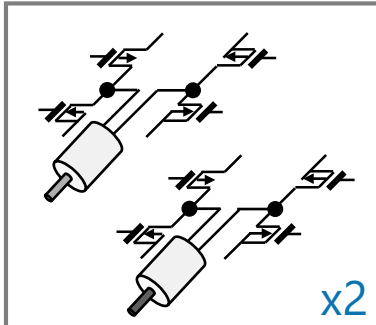
3 selectable H bridge combination according to motor type and required current as follows: 1) 2 steppers drive 2) 4 brushed drive 3) large current 2 brushed drive

3 drive mode

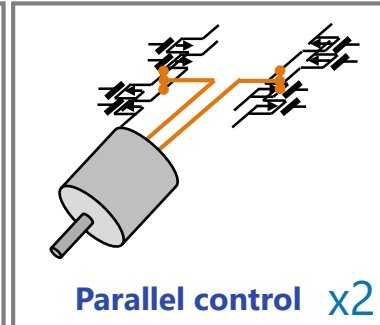
1) 2 steppers



2) 4 brushed



3) Large current 2 brushed



Line up

Part number	TC78S121FNG / FTG, TC78S122FNG / FTG
Package	HTSSOP48 / QFN48
Package size [mm]	12.5 x 8.1 x 1.2
Maximum ratings	40 V / 2.0 A
Low on resistor output (sum of 2 Tr)	0.6 Ω
Features	<ul style="list-style-type: none"> - Over limit current detection, over heat detection and power on reset - 2 line up supports clock input for stepping motor control and phase input - Single power supply without 5 V input

[◆Return to Block Diagram TOP](#)

Value provided

Selectable drive mode of 2ch DC brushed motor or 1ch stepping motor

1 Small package

The QFN16 package contributes to reduce foot print areas.

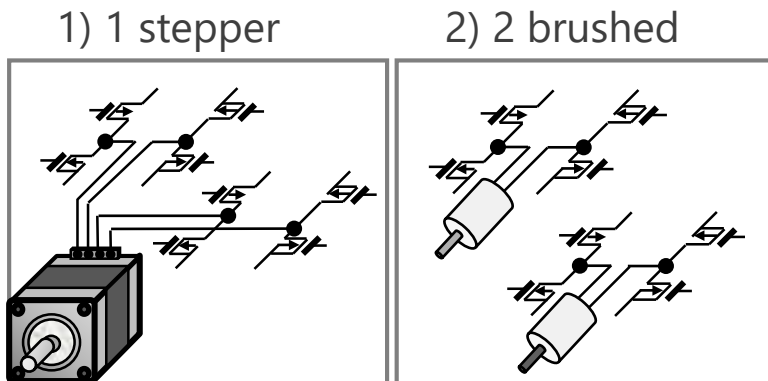
2 Protection and abnormality detection

Thorough current preventing function and various abnormality detection such as over current detection, over heat detection and low voltage detection contribute safety motor control.

3 2 drive mode

3 selectable H bridge combination according to motor type and required current as follows: 1) 2 steppers drive 2) 4 brushed drive 3) large current 2 brushed drive

2 drive mode



Line up

Part number	TC78H653FTG
Package	QFN16
Package size [mm]	3.0 x 3.0 x 0.9
Maximum ratings	8 V / 4.0 A
Low on register output (Total Tr)	0.22 Ω
Features	<ul style="list-style-type: none"> • Thorough current preventing function • Over current detection, over heat detection and low voltage detection

[◆Return to Block Diagram TOP](#)

Value provided

eFuse IC (electronic fuses) can protect circuits from abnormal conditions such as overcurrent and overvoltage repeatedly.

1 Repeated use

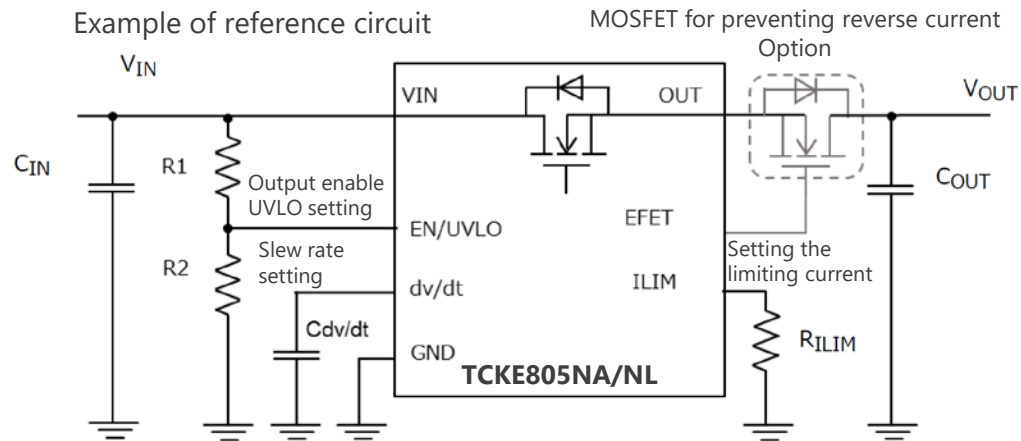
When excessive current flows through the eFuse IC, the internal detection circuit operates and turns off the internal MOS. It is not destroyed by a single overcurrent and can be used repeatedly.

2 High-speed short-circuit protection


The cut-off time at the time of output short-circuit is 150ns (Typ.), and the output current is cut-off at high speed at the time of short-circuit detection.

3 Rich protection functions

In addition to short-circuit protection, the circuit is protected by overcurrent clamp (OCC), overvoltage clamp (OVC), Thermal shut down (TSD), inrush current suppression, Reverse current protection (optional), and other functions



Line up

Part number	TCKE800NA/NL*	TCKE805NA/NL	TCKE812NA/NL*
Package	WSON10B 3.0x3.0x0.7mm 		
V_{IN} [V]	4.4 to 18		
R_{ON} (Typ.) [m Ω]	28		
Return function	NA: Automatic return, NL: Latch type (external signal control)		
V_{OVC} (Typ.) [V]	-	6.04	15.0

* Under development

[Return to Block Diagram TOP](#)

If you are interested in these products and have questions or comments about any of them, please do not hesitate to contact us below:

Contact address: <https://toshiba.semicon-storage.com/ap-en/contact.html>



Terms of use

This terms of use is made between Toshiba Electronic Devices and Storage Corporation ("We") and customers who use documents and data that are consulted to design electronics applications on which our semiconductor devices are mounted ("this Reference Design"). Customers shall comply with this terms of use. Please note that it is assumed that customers agree to any and all this terms of use if customers download this Reference Design. We may, at its sole and exclusive discretion, change, alter, modify, add, and/or remove any part of this terms of use at any time without any prior notice. We may terminate this terms of use at any time and for any reason. Upon termination of this terms of use, customers shall destroy this Reference Design. In the event of any breach thereof by customers, customers shall destroy this Reference Design, and furnish us a written confirmation to prove such destruction.

1. Restrictions on usage

- 1.This Reference Design is provided solely as reference data for designing electronics applications. Customers shall not use this Reference Design for any other purpose, including without limitation, verification of reliability.
- 2.This Reference Design is for customer's own use and not for sale, lease or other transfer.
- 3.Customers shall not use this Reference Design for evaluation in high or low temperature, high humidity, or high electromagnetic environments.
- 4.This Reference Design shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable laws or regulations.

2. Limitations

- 1.We reserve the right to make changes to this Reference Design without notice.
- 2.This Reference Design should be treated as a reference only. We are not responsible for any incorrect or incomplete data and information.
- 3.Semiconductor devices can malfunction or fail. When designing electronics applications by referring to this Reference Design, customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of semiconductor devices could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Customers must also refer to and comply with the latest versions of all relevant our information, including without limitation, specifications, data sheets and application notes for semiconductor devices, as well as the precautions and conditions set forth in the "Semiconductor Reliability Handbook".
- 4.When designing electronics applications by referring to this Reference Design, customers must evaluate the whole system adequately. Customers are solely responsible for all aspects of their own product design or applications. WE ASSUME NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.
- 5.No responsibility is assumed by us for any infringement of patents or any other intellectual property rights of third parties that may result from the use of this Reference Design. No license to any intellectual property right is granted by this terms of use, whether express or implied, by estoppel or otherwise.
- 6.THIS REFERENCE DESIGN IS PROVIDED "AS IS". WE (a) ASSUME NO LIABILITY WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (b) DISCLAIM ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO THIS REFERENCE DESIGN, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.

3. Export Control

Customers shall not use or otherwise make available this Reference Design for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). This Reference Design may be controlled under the applicable export laws and regulations including, without limitation, the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export Administration Regulations. Export and re-export of this Reference Design are strictly prohibited except in compliance with all applicable export laws and regulations.

4. Governing Laws

This terms of use shall be governed and construed by laws of Japan.

RESTRICTIONS ON PRODUCT USE

- Toshiba Electronic Devices & Storage Corporation, and its subsidiaries and affiliates (collectively "TOSHIBA"), reserve the right to make changes to the information in this document, and related hardware, software and systems (collectively "Product") without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA's written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product's quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which Minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before customers use the Product, create designs including the Product, or incorporate the Product into their own applications, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application with which the Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications. TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.
- PRODUCT IS NEITHER INTENDED NOR WARRANTED FOR USE IN EQUIPMENTS OR SYSTEMS THAT REQUIRE EXTRAORDINARILY HIGH LEVELS OF QUALITY AND/OR RELIABILITY, AND/OR A MALFUNCTION OR FAILURE OF WHICH MAY CAUSE LOSS OF HUMAN LIFE, BODILY INJURY, SERIOUS PROPERTY DAMAGE AND/OR SERIOUS PUBLIC IMPACT ("UNINTENDED USE"). Except for specific applications as expressly stated in this document, Unintended Use includes, without limitation, equipment used in nuclear facilities, equipment used in the aerospace industry, medical equipment, equipment used for automobiles, trains, ships and other transportation, traffic signaling equipment, equipment used to control combustions or explosions, safety devices, elevators and escalators, devices related to electric power, and equipment used in finance-related fields. IF YOU USE PRODUCT FOR UNINTENDED USE, TOSHIBA ASSUMES NO LIABILITY FOR PRODUCT. For details, please contact your TOSHIBA sales representative.
- Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE FOR PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (2) DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO SALE, USE OF PRODUCT, OR INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.
- GaAs (Gallium Arsenide) is used in Product. GaAs is harmful to humans if consumed or absorbed, whether in the form of dust or vapor. Handle with care and do not break, cut, crush, grind, dissolve chemically or otherwise expose GaAs in Product.
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). Product and related software and technology may be controlled under the applicable export laws and regulations including, without limitation, the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export Administration Regulations. Export and re-export of Product or related software or technology are strictly prohibited except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. Please use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. TOSHIBA ASSUMES NO LIABILITY FOR DAMAGES OR LOSSES OCCURRING AS A RESULT OF NONCOMPLIANCE WITH APPLICABLE LAWS AND REGULATIONS.

TOSHIBA

* Arm and Cortex are registered trademarks of Arm Limited (or its subsidiaries) in the US and/or elsewhere.
* Other company names, product names, and service names may be trademarks of their respective companies.