

# CRC\_MEMEORY\_RAM

## 1. Operation Outline

The continuous data specified by MEMORY\_SIZE from MEMORY\_TOP is input to CRC .  
The CRC result is stored in crc\_data.

## 2. Each Setting

<u>LED</u>	: LED0 (PortJ0)
	: LED2 (PortJ2)

## 3. Basic Operation

After start-up, the CRC receives the continuous data in the specified region of the memory to calculate its CRC value.  
The CRC result is checked. When it is correct, the LED0 lights.  
If it is not correct, the LED2 lights.

## 4. Note

MEMORY\_TOP and MEMORY\_SIZE should relate to the unused region for program in the RAM.