

SIO_8bit_TRANS

1. Operation Outline

The 1-byte test data is transmitted by a command input (via UART) from the terminal software of a PC.

2. Each Setting

<u>SIO</u>	: SO1 (Port90) : SCLK1 (Port92)	
<u>UART</u>	: TXD2 (Port93) : RXD2 (Port94)	
<u>Transfer selection edge</u>	: default = Data reception at the rising edge, data transmission at the falling edge main.c: Changing the value of "#define SIO_SEL_EDGE" allows changing the transfer selection edge.	
<u>Serial clock selection [Hz]</u>	: default = fcgck/2^9 main.c: Changing the value of "#define SIO_SCLK" allows changing the serial clock.	
<u>Transfer format (MSB/LSB) selection</u>	: default = LSB first main.c: Changing the value of "#define SIO_TRANS_FORMAT" allows changing the transfer format (MSB/LSB).	
<u>Transfer mode selection</u>	: 8-bit transmission mode	
<u>Command list</u>	: write	Test data are transmitted.
<u>Serial port setting</u>	Baud rate : 115200 (bps) Data : 8 (bit) Parity : None Stop : 1 (bit) Flow control : None	

3. Basic Operation

When the "write" command is input on the terminal software, the 1-byte test data is output by 20 ms period.
(Test data : The values (0x00-0xFF) counted up by 1)

Display example of terminal software

```
SIO Sample Program
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Command> write
```

4. Note

None.