

FLASH_MCU_RAM

1. Operation Outline

The control program is transferred to RAM region in MCU mode, and the write and read operations of test data are executed.

2. Each Setting

SW : SW0 (Port12)

UART : TXD2 (Port93)
: RXD2 (Port94)

Serial port setting Baud rate : 115200 (bps)
Data : 8 (bit)
Parity : None
Stop : 1 (bit)
Flow control : None

3. Basic Operation

Terminal soft starts (1).

When SW0 is pushed down, the program starts (2). The program is transferred to the RAM region (3).

The sector erase of code region (0xE000 to 0xEFFF) is executed (4).

After that, the data of 0x3F are written to 0xE500 (5).

The data is read from 0xE500, and stored to the RAM (6).

After the execution, "Finished" is displayed (7).

Example of terminal software display

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Flash MCU mode(executing a control program in RAM) Sample      (2)
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Transfer a control program to the RAM.                (3)
Sector Erase: 0xE000-0xEFFF                            (4)
Byte Program: 0xE500                                    (5)
Read Data: (Data)0x3F, (RAM Addr)0x****                (6)
```

4. Note

None.