

FLASH_SPROM_UART

1. Operation Outline

The write and read controls of test data from the external control board are executed with UART communication.

2. Each Setting

SW : SW0 (Port12)

UART : TXD2 (Port93)
: RXD2 (Port94)

Serial port setting

Baud rate	: 115200 (bps)
Data	: 8 (bit)
Parity	: None
Stop	: 1 (bit)
Flow control	: None

3. Basic Operation

The target board connects to the external control board with UART, and starts in the serial PROM mode.

When the SW0 on the external control board is pushed down, the following process can be executed.

When the setting command of Flash memory security is received, the Security Program is released.

The erase command of Flash memory is received, the Sector Erase of 0xE000-0xEFFF in the Code region is executed.

When the write command of Flash memory is received, 64-byte data are written to 0xE100 (the write data are all 0x1F).

When the read command of Flash memory is received, the data are read from 0xE100, and stored to the RAM.

4. Note

None.