

# Flash\_Data

## 1. Operation Outline

The data in data flash are re-written by using the Dual Mode.

## 2. Board setting

Connect the terminal on the evaluation board as follows.

CN5 1-2            CN4 29-30  
         3-4

## 3. Each Setting

Switch                                : PV1        S7

UART                                : UT0RXD(PE2)  
                                      : UT0TXDA(PE3)

### Serial port setting

Baud rate                : 115200(bps)  
Data                      : 8(bit)  
Parity                    : None  
Stop                      : 1(bit)  
Flow control             : None

## 3. Basic Operation

1. When starting a program, the startup message is displayed on the terminal emulator.

```
*****  
FLASH DATA Sample start  
*****  
Please press the S7
```

2. When pushing the S7, the following processes are executed.

Area erasing: 0x30000000-0x30007FFF

Area4 Erased.

The test data 0x00-0xFF is wrote to the Page0 and (0x30000000-0x300000FF) and Page1 area(0x30000100-0x300001FF).

Rewriting.

Page0 area(0x30000000-0x300000FF) erasing.

Page0 Erased.

Block0 area(0x30000000-0x30000FFF) erasing.

Block0 Erased.

When the completion of erasing, "Finished." is displayed on the terminal emulator.

Finished.

#### 4. Note

Multiple push-downs of a switch are not supported.