

## I2C Master Slave

### 1.Operation Summary

You can confirm slave and master of I2C operation, when you input command with TeraTerm on PC.

### 2.Board setting

It will use 2 evaluation boards. Connect the terminal on the evaluation board as follows

Evaluation board 1

CN5 1-2 3-4

CN5 6 - evaluation board2:CN5 6

CN5 8 - evaluation board2:CN5 8

CN4 29 - CN5 6

CN4 31 - CN5 8

Evaluation board2

CN5 1-2 3-4

CN5 6 - evaluation board1:CN5 6

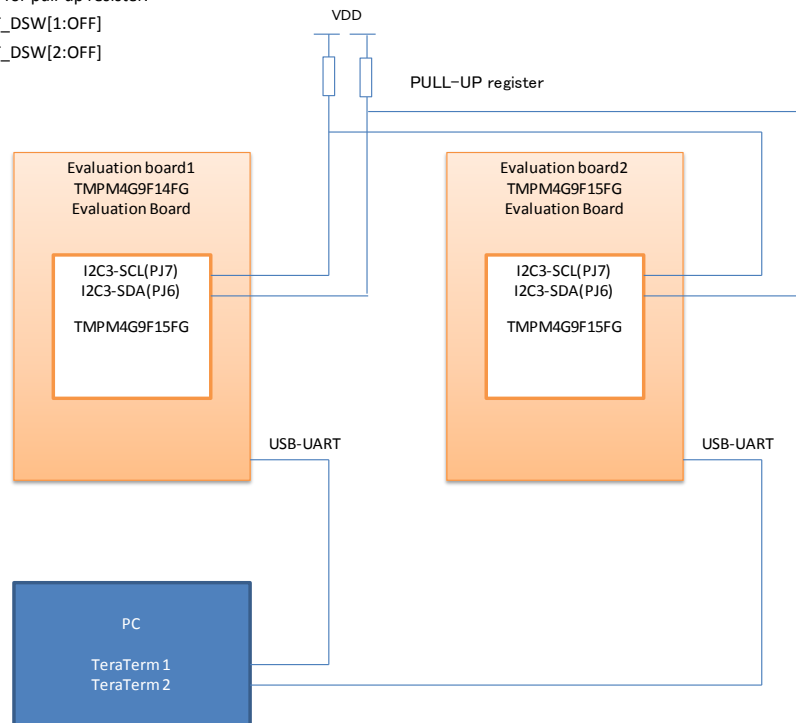
CN5 8 - evaluation board1:CN5 8

[PORT DipSW]

it use for pull-up resistor.

PORT\_DSW[1:OFF]

PORT\_DSW[2:OFF]



### 3. Basic Operation

The "command >" prompt will be displayed in the Tera Term. Then, input a command in the following command format.

Then CPU operate as I2C master or I2C slave.

it switch from master to slave by command, when CPU operate as master. You can input command when the CPU in the slave mode.

#### • command format :

"command[\_parameter]"    ※ About command see the command list.  
the parameter is difference depending the command.

#### • UART Setting

|              |              |
|--------------|--------------|
| TXD          | :PE3         |
| RXD          | :PE2         |
| Baud Rate    | :115200(bps) |
| Data         | :8(bit)      |
| Parity       | : None       |
| Stop Bit     | :1(bit)      |
| Flow Control | : None       |

### 4. Output Example

I2C Master side

```
I2C TEST - I2C0
-----
| I2C master mode |
-----
command > write
master
sa B0
tx{0} 00
tx{1} 01
tx{2} 02
tx{3} 03

command > read
master
sa B0
tx{0} 00
tx{1} 01
rx{0} 80
rx{1} 81

command >
```

I2C Slave side

```
I2C TEST - I2C0
-----
| I2C master mode |
-----
command > slave

-----
| I2C slave mode |
-----
slave
sa B0
rx{0} 00
rx{1} 01
rx{2} 02
rx{3} 03

slave
sa B0
rx{0} 00
rx{1} 01
tx{0} 80
tx{1} 81

slave
```