

# ADC

## 1. Operation Outline

2-channel output voltages of the potentiometer are measured by the ADC. The result is displayed on Tera Term.

## 2. Each Setting

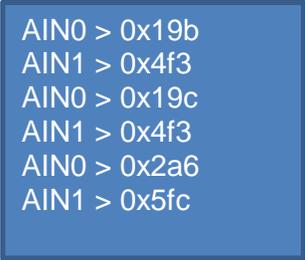
UART : UT1TXDA (PortU5)  
: UT1RXD (PortU6)

Serial port setting Baud rate : 115200 (bps)  
Data : 8 bit  
Parity : None  
Stop : 1 bit  
Flow control : None

ADC : AINA01 (PortM6)  
: AINA00 (PortM7)

## 3. Basic Operation

2-channel output voltages of the potentiometer are measured by the ADC every 5 seconds. The result is displayed on Tera Term.  
The display of the result is as follows;



```
AIN0 > 0x19b  
AIN1 > 0x4f3  
AIN0 > 0x19c  
AIN1 > 0x4f3  
AIN0 > 0x2a6  
AIN1 > 0x5fc
```

## 4. Note

Nothing.