Switching Diodes Silicon Epitaxial Planar

1SS307E

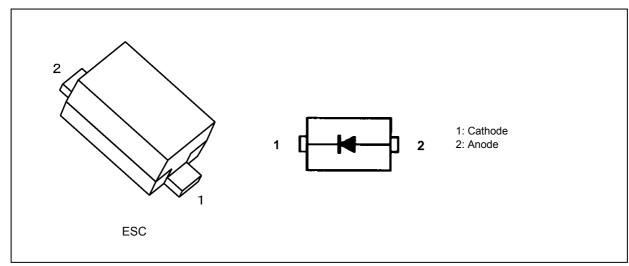
1. Applications

General-Purpose Rectifiers

2. Features

- (1) Very low reverse current. : $I_R = 10 \text{ nA} \text{ (max)}$
- (2) AEC-Q101 qualified (Note 1)

Note 1: For detail information, please contact to our sales.



3. Packaging and Internal Circuit

4. Absolute Maximum Ratings (Note) (Unless otherwise specified, T_a = 25 °C)

| Characteristics | Symbol | Note | Rating | Unit |
|---|------------------|----------|------------|------|
| Peak reverse voltage | V _{RM} | | 85 | V |
| Reverse voltage | V _R | | 80 | |
| Peak forward current | I _{FM} | | 300 | mA |
| Average rectified current | Ι _Ο | | 100 | |
| Power dissipation | PD | (Note 1) | 150 | mW |
| Non-repetitive peak forward surge current | I _{FSM} | (Note 2) | 1 | А |
| Junction temperature | Tj | | 150 | °C |
| Storage temperature | T _{stg} | | -55 to 150 | °C |

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Mounted on a glass epoxy circuit board of 20 mm \times 20 mm, Pad dimension of 4 mm \times 4 mm.

Note 2: Measured with a 10 ms pulse.

5. Electrical Characteristics (Unless otherwise specified, Ta = 25 °C)

| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|-------------------|----------------|---------------------------------|-----|------|-----|------|
| Forward voltage | V _F | I _F = 100 mA | _ | 0.9 | 1.3 | V |
| Reverse current | I _R | V _R = 80 V | _ | _ | 10 | nA |
| Total capacitance | Ct | V _R = 0 V, f = 1 MHz | | 2.0 | 6.0 | pF |

6. Marking

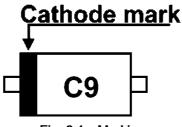


Fig. 6.1 Marking

7. Land Pattern Dimensions (for reference only)

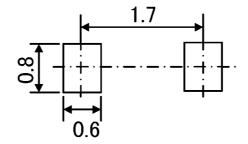


Fig. 7.1 ESC (Unit: mm)

- 1000 10 T_a = 150 °C 100 Forward current I_F (mA) (NA) 1 . 125 °C Reverse current I_R 10 = 150 °C 0.1 125 °C 1 0.01 75 ℃ 75 ℃ 0.1 25 °C 0.001 0.01 25 °C -25 °C 0.001 0.0001 0.8 1.2 0 10 20 30 40 50 60 70 80 0 0.2 0.4 0.6 1 Reverse voltage V_R (V) Forward voltage V_F (V) Fig. 8.1 I_F - V_F Fig. 8.2 I_R - V_R 10 250 f = 1 MHz Mounted on a glass epoxy circuit T_a = 25 °C board of 20 mm × 20 mm, (PF) (Mm) Pad dimension of 4 mm × 4 mm. 200 Total capacitance Ct Power dissipation P_D 150 1 100 50 0.1 10 30 40 50 60 70 80 0 20 0 Reverse voltage V_R (V) 0 25 50 75 100 125 150 Ambient temperature T_a (°C) Fig. 8.3 Ct - VR Fig. 8.4 P_D - T_a
- 8. Characteristics Curves (Note)

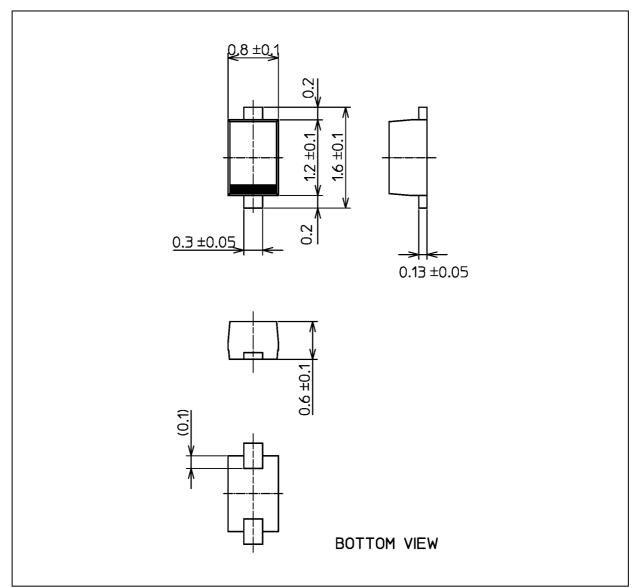
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



1SS307E

Package Dimensions

Unit: mm



Weight: 1.4 mg (typ.)

| Package Name(s) | | | |
|-----------------|--|--|--|
| TOSHIBA: 1-1G1S | | | |
| Nickname: ESC | | | |

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