Unit: mm

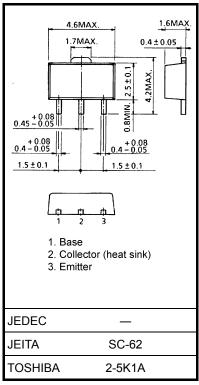
TOSHIBA Transistor Silicon PNP Epitaxial Type

2SA2059

High-Speed Switching Applications DC-DC Converter Applications Strobe Applications

- High DC current gain: h_{FE} = 200 to 500 (I_C = -0.5 A)
- Low collector-emitter saturation voltage: V_{CE (sat)} = -0.19 V (max)
- High-speed switching: t_f = 40 ns (typ.)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		V _{CBO}	-20	V	
Collector-emitter voltage		V _{CEO}	-20	V	
Emitter-base voltage		V _{EBO}	-7	V	
Collector current	DC	Ι _C	-3.0	А	
	Pulse	I _{CP}	-5.0		
Base current		Ι _Β	-0.3	А	
Collector power dissipation	DC	PC	1.0	w	
	t = 10 s	(Note)	2.5		
Junction temperature		Тj	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	



Weight: 0.05 g (typ.)

Note 1: Mounted on an FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm²)

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Absolute Maximum Ratings (Ta = 25°C)

Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I _{CBO}	$V_{CB} = -20 V, I_E = 0$	_	_	-100	nA
Emitter cut-off current		I _{EBO}	$V_{EB} = -7 V, I_C = 0$	_	_	-100	nA
Collector-emitter breakdown voltage		V (BR) CEO	I _C = −10 mA, I _B = 0	-20	_	_	V
DC current gain		h _{FE} (1)	V _{CE} = -2 V, I _C = -0.5 A	200	_	500	
		h _{FE} (2)	V _{CE} = -2 V, I _C = -1.6 A	100	_	_	
Collector-emitter saturation voltage		V _{CE (sat)}	I _C = −1.6 A, I _B = −53 mA	_	_	-0.19	V
Base-emitter saturation voltage		V _{BE (sat)}	I _C = −1.6 A, I _B = −53 mA	_	_	-1.10	V
Collector output capacitance		C _{ob}	V _{CB} = −10 V, I _E = 0, f = 1 MHz	_	28	_	pF
Switching time	Rise time	tr	See Figure 1 circuit diagram.	_	70	_	ns
	Storage time	t _{stg}	$V_{cc} \approx -12$ V, $R_L = 7.5$ Ω	_	150	_	
	Fall time	t _f	I _{B1} = 53.3 mA ,I _{B2} = 53.3 mA	_	40	—	

I_{B1} I_{B1} I_{B2} I_{B2} I_{B1} I_{B2} I

Marking

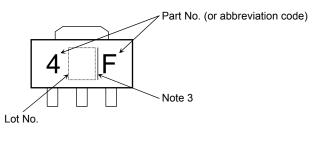
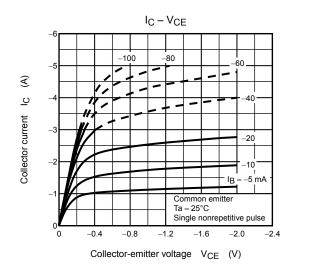


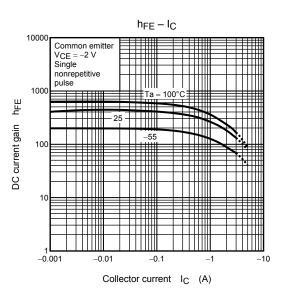
Figure 1 Switching Time Test Circuit & Timing Chart

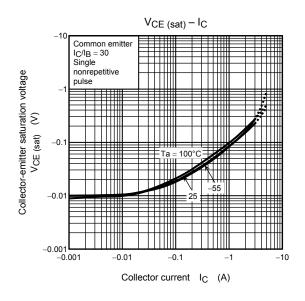
Note 3: A line to the right of a Lot No. identifies the indication of product Labels. Without a line: [[Pb]]/INCLUDES > MCV With a line: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

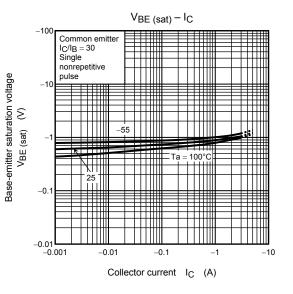
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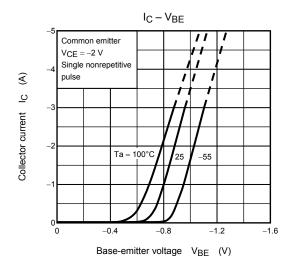
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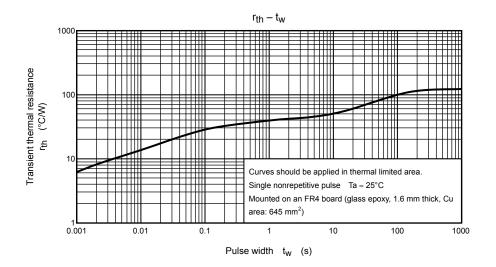












Safe Operating Area -10 ++++++++ 10 μs **•** IC max (pulsed) ♦ 10 ms♦ 1 ms♦ 100 μs♦ -C max (continuous E 100 ms♦ <u>ں</u> 10 s♦* DC operation * Collector current (Ta = 25°C) Constant in the second se -0.1 T _____ VCEO max with increase in temperature. -0.01 -10 -100 Collector-emitter voltage V_{CE} (V)

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