

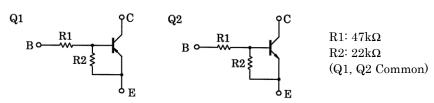
TOSHIBA Transistor Silicon NPN/PNP Epitaxial Type (PCT Process) (Transistor with Built-in Bias Resistor)

# **RN4609**

Switching, Inverter Circuit,
Interface Circuit and Driver Circuit

- Including two devices in SM6 (super mini type with 6 leads)
- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process and iniaturize equipment.

#### **Equivalent Circuit and Bias Resistor Values**

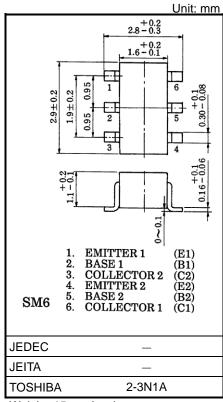


### Q1 Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	Vсво	-50	V
Collector-emitter voltage	VCEO	-50	V
Emitter-base voltage	VEBO	<b>-15</b>	V
Collector current	Ic	-100	mA

#### **Q2 Absolute Maximum Ratings (Ta = 25°C)**

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	50	V
Collector-emitter voltage	VCEO	50	V
Emitter-base voltage	V <sub>EBO</sub>	15	٧
Collector current	Ic	100	mA



Weight: 15 mg (typ.)



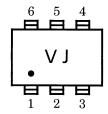
## Q1, Q2 Common Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector power dissipation	Pc *	300	mW
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	−55 to 150	°C

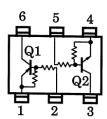
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Marking



## **Equivalent Circuit (Top View)**



<sup>\*</sup> Total rating



## Q1 Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector out off ourrent	Ісво	_	$V_{CB} = -50 \text{ V}, I_E = 0 \text{ mA}$	_	_	-100	nA
Collector cut-off current	ICEO	_	$V_{CE} = -50 \text{ V}, I_B = 0 \text{ mA}$	_	_	-500	IIA
Emitter cut-off current	I <sub>EBO</sub>	_	$V_{EB} = -15 \text{ V}, I_{C} = 0 \text{ mA}$	-0.167	_	-0.311	mA
DC current gain	hFE	_	V <sub>CE</sub> = −5 V, I <sub>C</sub> = −10 mA	70	_	_	_
Collector-emitter saturation voltage	VCE (sat)	_	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$	_	-0.1	-0.3	V
Input voltage (ON)	VI (ON)	_	VCE = −0.2 V, IC = −5 mA	-2.2	_	-5.8	V
Input voltage (OFF)	VI (OFF)	_	V <sub>CE</sub> = −5 V, I <sub>C</sub> = −0.1 mA	-1.5	_	-2.6	V
Transition frequency	f⊤	_	$V_{CE} = -10 \text{ V}, I_{C} = -5 \text{ mA}$	_	200	_	MHz
Collector output capacitance	C <sub>ob</sub>	_	V <sub>CB</sub> = -10 V, I <sub>E</sub> = 0 mA, f = 1 MHz	_	3	6	pF

## Q2 Electrical Characteristics (Ta = 25°C)

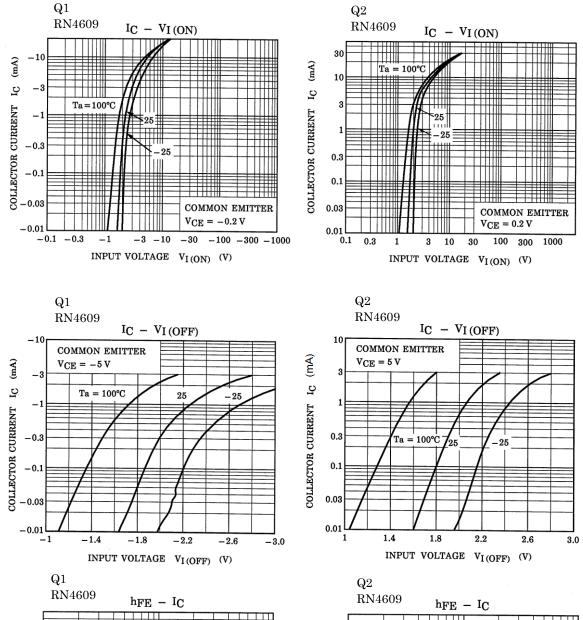
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	_	V <sub>CB</sub> = 50 V, I <sub>E</sub> = 0 mA	_	_	100	nA
	ICEO	_	VCE = 50 V, IB = 0 mA	_	_	500	
Emitter cut-off current	IEBO	_	V <sub>EB</sub> = 15 V, I <sub>C</sub> = 0 mA	0.167	_	0.311	mA
DC current gain	hFE	_	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 10 mA	70	_	_	_
Collector-emitter saturation voltage	VCE (sat)	_	I <sub>C</sub> = 5 mA, I <sub>B</sub> = 0.25 mA	_	0.1	0.3	V
Input voltage (ON)	VI (ON)	_	VCE = 0.2 V, IC = 5 mA	2.2	_	5.8	V
Input voltage (OFF)	VI (OFF)	_	VCE = 5 V, IC = 0.1 mA	1.5	_	2.6	V
Transition frequency	fŢ	_	VCE = 10 V, IC = 5 mA	_	250	_	MHz
Collector output capacitance	C <sub>ob</sub>	_	$V_{CB} = 10 \text{ V}, I_E = 0 \text{ mA},$ $f = 1  MHz$	_	3	6	pF

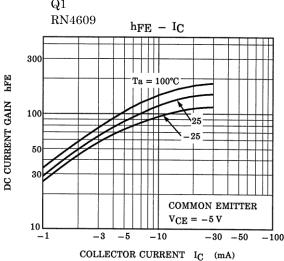
## Q1, Q2 Common Electrical Characteristics (Ta = 25°C)

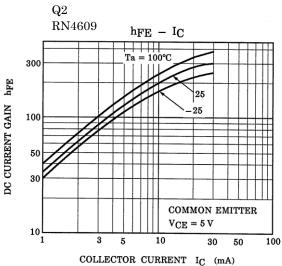
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input resistance	R1	_	-	32.9	47	61.1	kΩ
Resistance ratio	R1/R2	-		1.92	2.14	2.35	_



#### Q1,Q2 characteristics curves







The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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