

Bipolar Transistors Silicon NPN/PNP Epitaxial Type (PCT Process)(Bias Resistor built-in Transistor)

## **RN4985**

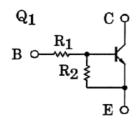
#### 1. Applications

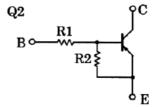
- · Switching
- · Inverter Circuits
- · Interfacing
- · Driver Circuits

#### 2. Features

- (1) AEC-Q101 qualified (Please see the orderable part number list)
- (2) Including two devices in US6 (ultra super mini type with 6 leads)
- (3) The integrated bias resistor reduces the number of external parts required, making it possible to reduce system size and assembly time.

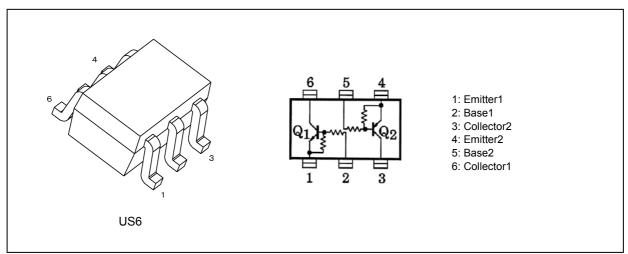
#### 3. Equivalent Circuit





R1: 2.2 kΩ R2: 47 kΩ (Q1, Q2 Common)

#### 4. Packaging and Pin Assignment



#### 5. Orderable part number

Orderable part number	AEC-Q101 Note				
RN4985,LF	— General Use				
RN4985,LXGF	YES	(Note 1)	Unintended Use (Note 1)		
RN4985,LXHF	YES		Automotive Use		

1

Note 1: For more information, please contact our sales or use the inquiry form on our website.

Start of commercial production



#### 6. Q1 Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	50	V
Collector-emitter voltage	V <sub>CEO</sub>	50	
Emitter-base voltage	V <sub>EBO</sub>	5	
Collector current	I <sub>C</sub>	100	mA

#### 7. Q2 Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	-50	V
Collector-emitter voltage	V <sub>CEO</sub>	-50	
Emitter-base voltage	V <sub>EBO</sub>	-5	
Collector current	I <sub>C</sub>	-100	mA

# 8. Q1, Q2 Common Absolute Maximum Ratings (Note) (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics	Symbol	Rating	Unit	
Collector power dissipation	(Note 1)	P <sub>C</sub>	200	mW
Junction temperature		T <sub>j</sub>	150	°C
Storage temperature		T <sub>stg</sub>	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Total rating

#### 9. Q1 Electrical Characteristics (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	V <sub>CB</sub> = 50 V, I <sub>E</sub> = 0 mA	_	_	100	nA
Collector cut-off current	I <sub>CEO</sub>	V <sub>CE</sub> = 50 V, I <sub>B</sub> = 0 mA	_	_	500	
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> = 5 V, I <sub>C</sub> = 0 mA	0.078	_	0.145	mA
DC current gain	h <sub>FE</sub>	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 10 mA	80	_	_	_
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> = 5 mA, I <sub>B</sub> = 0.25 mA	_	0.1	0.3	٧
Input voltage (ON)	V <sub>I(ON)</sub>	V <sub>CE</sub> = 0.2 V, I <sub>C</sub> = 5 mA	0.6	_	1.1	
Input voltage (off)	V <sub>I(off)</sub>	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 0.1 mA	0.5	_	0.8	
Transition frequency	f <sub>T</sub>	V <sub>CE</sub> = 10 V, I <sub>C</sub> = 5 mA	_	250	_	MHz
Collector output capacitance	C <sub>ob</sub>	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0 mA, f = 1 MHz	_	3	6	pF



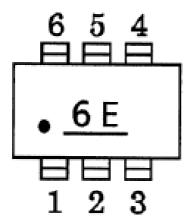
## 10. Q2 Electrical Characteristics (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = -50 \text{ V}, I_{E} = 0 \text{ mA}$	_	_	-100	nA
Collector cut-off current	I <sub>CEO</sub>	$V_{CE} = -50 \text{ V}, I_{B} = 0 \text{ mA}$	_	_	-500	
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB} = -5 \text{ V}, I_{C} = 0 \text{ mA}$	-0.078	_	-0.145	mA
DC current gain	h <sub>FE</sub>	$V_{CE} = -5 \text{ V}, I_{C} = -10 \text{ mA}$	80	_	_	_
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$	_	-0.1	-0.3	٧
Input voltage (ON)	$V_{I(ON)}$	$V_{CE} = -0.2 \text{ V}, I_{C} = -5 \text{ mA}$	-0.6	_	-1.1	
Input voltage (off)	$V_{I(off)}$	$V_{CE} = -5 \text{ V}, I_{C} = -0.1 \text{ mA}$	-0.5	_	-0.8	
Transition frequency	f <sub>T</sub>	$V_{CE}$ = -10 V, $I_{C}$ = -5 mA	_	200	_	MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = -10 \text{ V}, I_E = 0 \text{ mA}, f = 1 \text{ MHz}$	_	3	6	pF

## 11. Q1, Q2 Common Electrical Characteristics (Unless otherwise specified, Ta = 25 °C)

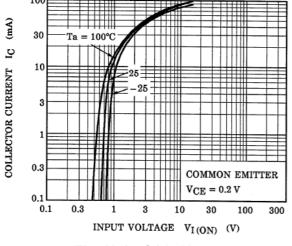
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input resistance	R <sub>1</sub>	-	1.54	2.2	2.86	kΩ
Resistor ratio	R1/R2	-	0.0421	0.0468	0.0515	_

## 12. Marking





#### 13. Characteristics Curves (Note)



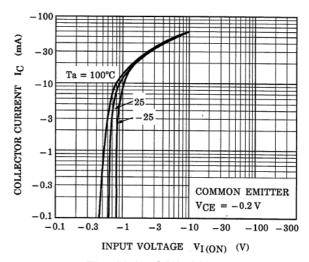
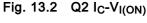
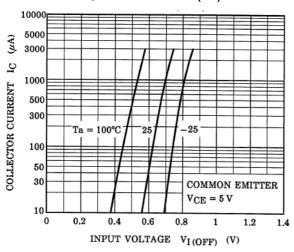


Fig. 13.1 Q1 I<sub>C</sub>-V<sub>I(ON)</sub>





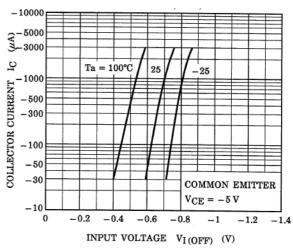
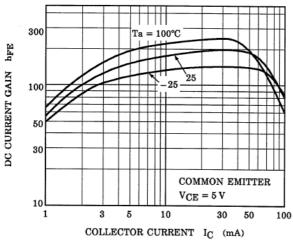


Fig. 13.3 Q1 I<sub>C</sub>-V<sub>I(OFF)</sub>

Fig. 13.4 Q2 I<sub>C</sub>-V<sub>I(OFF)</sub>



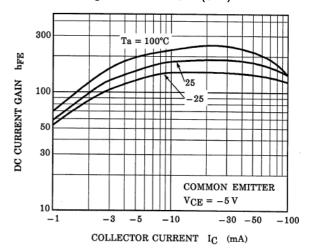
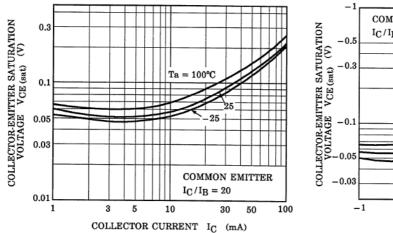


Fig. 13.5 Q1 h<sub>FE</sub>-I<sub>C</sub>

Fig. 13.6 Q2 h<sub>FE</sub>-I<sub>C</sub>





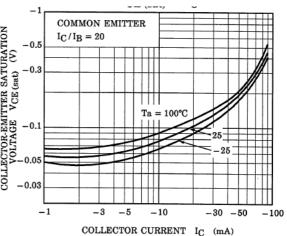


Fig. 13.7 Q1 V<sub>CE(sat)</sub>-I<sub>C</sub>

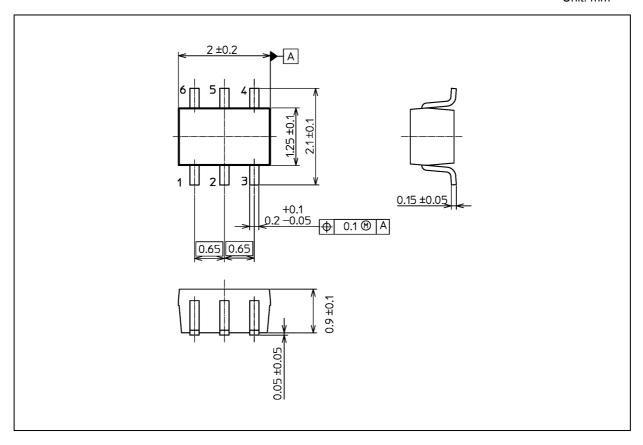
Fig. 13.8 Q2 V<sub>CE(sat)</sub>-I<sub>C</sub>

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



### **Package Dimensions**

Unit: mm



Weight: 6.8 mg (typ.)

	Package Name(s)
TOSHIBA: 1-2T1S	
Nickname: US6	



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