Unit: mm

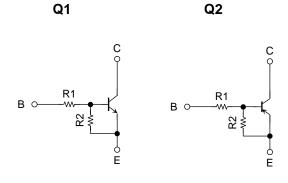
TOSHIBA Transistor Silicon NPN/PNP Epitaxial Type (PCT Process) (Transistor with Built-in Bias Resistor)

# **RN49A2**

Switching, Inverter Circuit, Interface Circuit and Driver Circuit

- Two devices are incorporated into an Ultra-Super-Mini (6-pin) package.
- Incorporating a bias resistor into a transistor reduces the parts count. Reducing the parts count enables the manufacture of ever more compact equipment and lowers assembly cost.

#### **Equivalent Circuit and Bias Resistor Values**



2.1±0.1  $1.25 \pm 0.1$ 0.65 2.0±0.2 1 0.2 - 0.05 1.3±0.1 59 2 0.15±0.05 0~0.1 EMITTER 1 1. (E1) 2. BASE 1 (B1) 3. **COLLECTOR 2** (C2) 4. EMITTER 2 (E2) 5. BASE 2 (B2) US6 6. COLLECTOR 1 (C1) JEDEC JEITA TOSHIBA 2-2J1A

Weight: 0.006g (typ.)

Q1

R1: 47 kΩ, R2: 47 kΩ

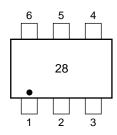
Q2

R1: 2.2 k\Omega, R2: 47 k\Omega

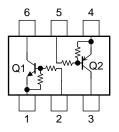
Q1: RN1104F equivalent

Q2: RN2105F equivalent

#### Marking



Equivalent Circuit (top view)



Start of commercial production 1999-10

#### Absolute Maximum Ratings (Ta = 25°C) (Q1)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	50	V
Collector-emitter voltage	VCEO	50	V
Emitter-base voltage	V <sub>EBO</sub>	10	V
Collector current	IC	100	mA

#### Absolute Maximum Ratings (Ta = 25°C) (Q2)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	-50	V
Collector-emitter voltage	VCEO	-50	V
Emitter-base voltage	VEBO	-5	V
Collector current	IC	-100	mA

#### Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Rating	Unit
Collector power dissipation	P <sub>C</sub> (Note 1)	200	mW
Junction temperature	Тј	150	°C
Storage temperature range	T <sub>stg</sub>	-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Total rating

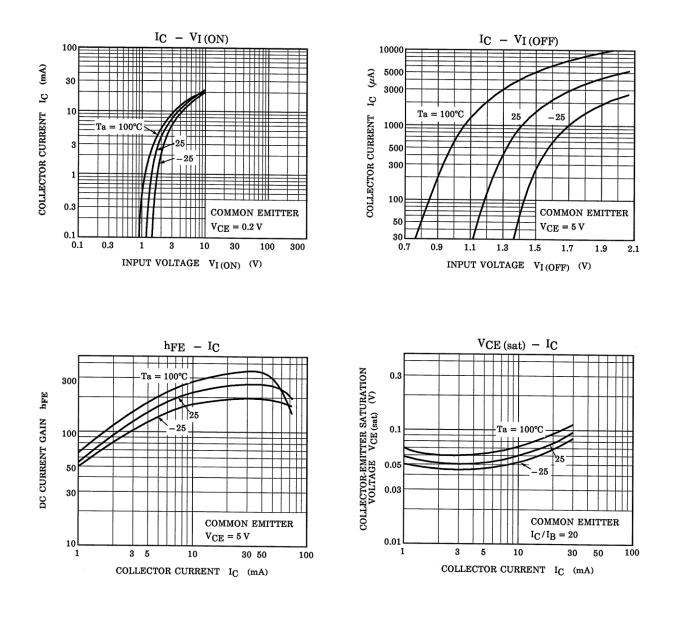
## Electrical Characteristics (Ta = 25°C) (Q1)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	ICBO	$V_{CB} = 50 \text{ V}, \text{ I}_{E} = 0 \text{ mA}$			100	nA
	ICEO	$V_{CE} = 50 \text{ V}, \text{ I}_{B} = 0 \text{ mA}$	_	_	500	
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB} = 10 \text{ V}, \text{ I}_{C} = 0 \text{ mA}$	0.082	—	0.15	mA
DC current gain	hFE	$V_{CE} = 5 V$ , $I_C = 10 mA$	80	_		
Collector-emitter saturation voltage	VCE (sat)	IC = 5 mA, IB = 0.25 mA	_	0.1	0.3	V
Input voltage (ON)	VI (ON)	$V_{CE}=0.2~V,~I_C=5~mA$	1.5	—	5.0	V
Input voltage (OFF)	VI (OFF)	$V_{CE} = 5 \text{ V}, \text{ I}_{C} = 0.1 \text{ mA}$	1.0	_	1.5	V
Transition frequency	f⊤	VCE = 10 V, IC = 5 mA		250		MHz
Collector output capacitance	Cob	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0 mA, f = 1 MHz		3		pF
Input resistance	R1	—	32.9	47	61.1	kΩ
Resistance ratio	R1/R2	—	0.9	1.0	1.1	

## Electrical Characteristics (Ta = 25°C) (Q2)

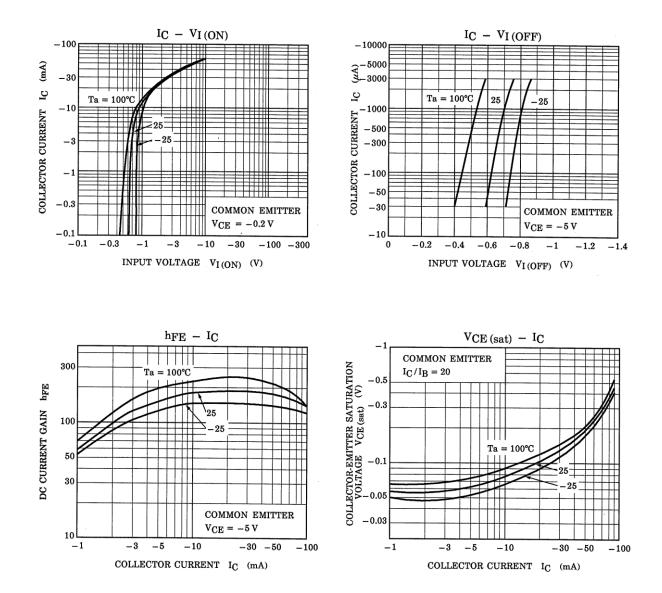
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	ICBO	$V_{CB} = -50 \text{ V}, \text{ I}_{E} = 0 \text{ mA}$		_	-100	nA
	ICEO	$V_{CE} = -50 \text{ V}, \text{ I}_{B} = 0 \text{ mA}$	_	—	-500	ПА
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB} = -5 V$ , $I_C = 0 mA$	-0.078	—	-0.145	mA
DC current gain	hFE	$V_{CE} = -5 \text{ V}, \text{ I}_{C} = -10 \text{ mA}$	80	—	—	—
Collector-emitter saturation voltage	V <sub>CE (sat)</sub>	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$	_	-0.1	-0.3	V
Input voltage (ON)	V <sub>I (ON)</sub>	$V_{CE} = -0.2 \text{ V}, \text{ I}_{C} = -5 \text{ mA}$	-0.6		-1.1	V
Input voltage (OFF)	VI (OFF)	$V_{CE} = -5 \text{ V}, \text{ I}_{C} = -0.1 \text{ mA}$	-0.5		-0.8	V
Transition frequency	fT	$V_{CE} = -10 \text{ V}, \text{ I}_{C} = -5 \text{ mA}$		200	_	MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = -10 \text{ V}, I_E = 0 \text{ mA}, f = 1 \text{ MHz}$		3	_	pF
Input resistance	R1	—	1.54	2.2	2.86	kΩ
Resistance ratio	R1/R2	—	0.0421	0. 0468	0.0 515	—

#### Characteristics Curves Q1



# TOSHIBA

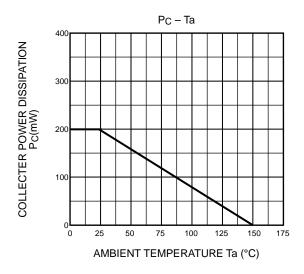
# Characteristics Curves Q2



The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

# TOSHIBA

#### Characteristics Curves Q1,Q2 Common



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