

CMOS Digital Integrated Circuits Silicon Monolithic

TC74AC04FT

1. Functional Description

· Hex Inverter

2. General

The TC74AC04FT is an advanced high speed CMOS INVERTER fabricated with silicon gate and double-layer metal wiring C2MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

The internal circuit is composed of 3 stages including buffer output, which provide high noise immunity and stable output.

All inputs are equipped with protection circuits against satic discharge or transient excess voltage.

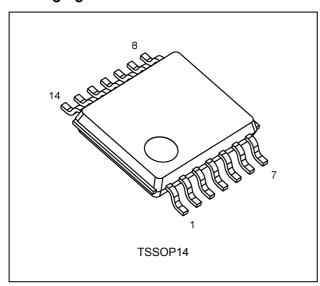
3. Features

- (1) Wide operating temperature range: $T_{opr} = -40$ to 125 °C (Note 1)
- (2) High speed: $t_{pd} = 3.2 \text{ ns (typ.)}$ at $V_{CC} = 5.0 \text{ V}$
- (3) Low power dissipation: I_{CC} = 4.0 μA (max) at T_a = 25 $^{\circ}C$
- (4) High noise immunity: $V_{NIH} = V_{NIL} = 28 \% V_{CC}$ (min)
- (5) Output current: $|I_{OH}|/I_{OL} = 24 \text{ mA (min)} (V_{CC} = 4.5 \text{ V})$
- (6) Balanced propagation delays: t_{PLH} ≈ t_{PHL}
- (7) Wide operating voltage range: $V_{CC(opr)} = 2.0$ to 5.5 V
- (8) Pin and function compatible with 74F04

Note 1: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.

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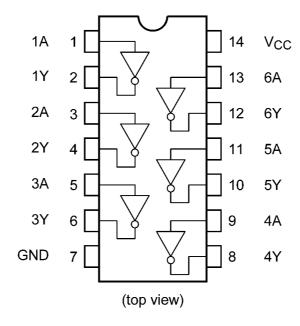
4. Packaging



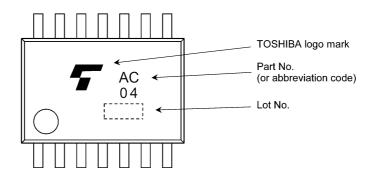
Start of commercial production



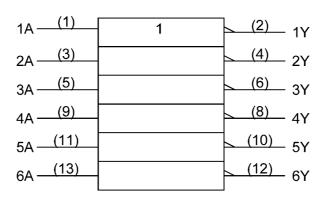
5. Pin Assignment



6. Marking



7. IEC Logic Symbol



Rev.1.0



8. Truth Table

А	Y
L	Н
Н	L

9. Absolute Maximum Ratings (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CC}		-0.5 to 7.0	V
Input voltage	V _{IN}		-0.5 to V _{CC} + 0.5	V
Output voltage	V _{OUT}		-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}		±20	mA
Output diode current	lok		±50	mA
Output current	l _{out}		±50	mA
V _{CC} /ground current	I _{CC}		±150	mA
Power dissipation	P _D	(Note 1)	180	mW
Storage temperature	T _{stg}		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: 180 mW in the range of T_a = -40 to 85 °C. From T_a = 85 to 125 °C a derating factor of -3.25 mW/°C shall be applied until 50 mW.

10. Operating Ranges (Note)

Characteristics	Symbol	Note	Test Condition	Rating	Unit
Supply voltage	V _{CC}			2.0 to 5.5	V
Input voltage	V _{IN}			0 to V _{CC}	V
Output voltage	V _{OUT}			0 to V _{CC}	V
Operating temperature	T _{opr}	(Note 1)		-40 to 125	°C
Input rise and fall times	dt/dv		V_{CC} = 3.3 \pm 0.3 V	0 to 100	ns/V
			V_{CC} = $5.0 \pm 0.5 \text{ V}$	0 to 20	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Note 1: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.

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11. Electrical Characteristics

11.1. DC Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Тур.	Max	Unit
High-level input voltage	V _{IH}	_		2.0	1.50	_	_	V
				3.0	2.10	_	_	V
				5.5	3.85	_	_	V
Low-level input voltage	V _{IL}	_		2.0	_	_	0.50	V
				3.0			0.90	V
				5.5			1.65	V
High-level output voltage	V _{OH}	V _{IN} = V _{IL}	I _{OH} = -50 μA	2.0	1.9	2.0	_	V
				3.0	2.9	3.0	_	
				4.5	4.4	4.5	_	
			I _{OH} = -4 mA	3.0	2.58	_	_	
			I _{OH} = -24 mA	4.5	3.94	_	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH}	I _{OL} = 50 μA	2.0	_	0.0	0.1	V
				3.0	_	0.0	0.1	
				4.5	_	0.0	0.1	
			I _{OL} = 12 mA	3.0	_	_	0.36	
			I _{OL} = 24 mA	4.5			0.36	
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		5.5		1	±0.1	μА
Quiescent supply current	I _{CC}	$V_{IN} = V_{CC}$ or GND		5.5	_	_	4.0	μА

11.2. DC Characteristics (Unless otherwise specified, Ta = -40 to 85 °C)

Characteristics	Symbol	Test Condition		Note	V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	_			2.0	1.50	_	V
					3.0	2.10	_	
					5.5	3.85		
Low-level input voltage	V _{IL}	_			2.0		0.50	V
					3.0	_	0.90	
					5.5	_	1.65	
High-level output voltage	V _{OH}	V _{IN} = V _{IL}	I _{OH} = -50 μA		2.0	1.9	_	V
					3.0	2.9	_	
					4.5	4.4	_	
			I _{OH} = -4 mA		3.0	2.48	_	
			I _{OH} = -24 mA		4.5	3.80	_	
			I _{OH} = -75 mA	(Note 1)	5.5	3.85	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH}	I _{OL} = 50 μA		2.0	_	0.1	V
					3.0	_	0.1	
					4.5	_	0.1	
			I _{OL} = 12 mA		3.0	_	0.44	
			I _{OL} = 24 mA		4.5	_	0.44	
			I _{OL} = 75 mA	(Note 1)	5.5		1.65	
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND			5.5	_	±1.0	μΑ
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND			5.5		40.0	μΑ

Note 1: This spec indicates the capability of driving 50 Ω transmission lines. One output should be tested within a 10 ms maximum duration.



11.3. DC Characteristics (Note) (Unless otherwise specified, Ta = -40 to 125 °C)

Characteristics	Symbol	Test Condition	n	Note	V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	_			2.0	1.50	_	V
					3.0	2.10	_	
					5.5	3.85	_	
Low-level input voltage	V _{IL}	_			2.0	_	0.50	V
					3.0		0.90	
					5.5		1.65	
High-level output voltage	V _{OH}	V _{IN} = V _{IL}	I _{OH} = -50 μA		2.0	1.9	_	V
					3.0	2.9	_	
					4.5	4.4	_	
			I _{OH} = -4 mA		3.0	2.48	_	
			I _{OH} = -24 mA		4.5	3.70	_	
			I _{OH} = -50 mA	(Note 1)	5.5	3.85	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH}	I _{OL} = 50 μA		2.0	_	0.1	V
					3.0		0.1	
					4.5	_	0.1	
			I _{OL} = 12 mA		3.0	_	0.44	
			I _{OL} = 24 mA		4.5		0.50	
			I _{OL} = 50 mA	(Note 1)	5.5	_	1.65	
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND			5.5		±1.0	μА
Quiescent supply current	Icc	$V_{IN} = V_{CC}$ or GND			5.5	_	80.0	μΑ

Note 1: This spec indicates the capability of driving 50 Ω transmission lines. One output should be tested within a 10 ms maximum duration.

Note: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.

11.4. AC Characteristics (Unless otherwise specified, $T_a = 25$ °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	Min	Тур.	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}		C _L = 50 pF	3.3 ± 0.3	_	5.7	9.8	ns
			$R_L = 500 \Omega$	5.0 ± 0.5	_	4.3	6.6	
Input capacitance	C _{IN}		_		_	5	10	pF
Power dissipation capacitance	C _{PD}	(Note 1)	_		_	20	_	pF

Note 1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation. $I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{|N} + I_{CC}/6 \text{ (per gate)}$

11.5. AC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}	C _L = 50 pF	3.3 ± 0.3	1.0	11.2	ns
		$R_L = 500 \Omega$	5.0 ± 0.5	1.0	7.5	
Input capacitance	C _{IN}	_	·	_	10	pF



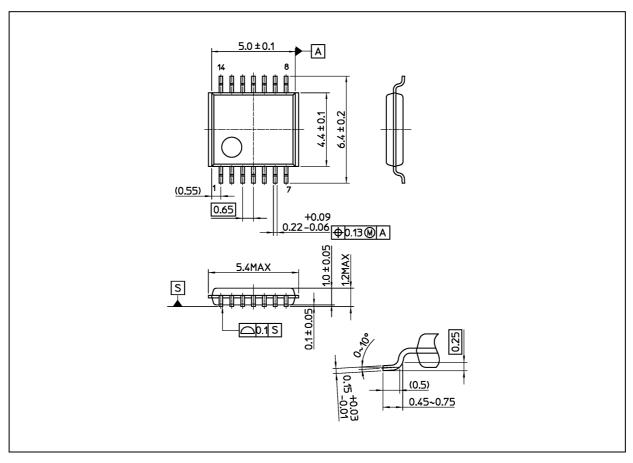
11.6. AC Characteristics (Note) (Unless otherwise specified, $T_a = -40$ to 125 °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}	C _L = 50 pF	3.3 ± 0.3	1.0	12.2	ns
		$R_L = 500 \Omega$	5.0 ± 0.5	1.0	8.1	
Input capacitance	C _{IN}	_			10	pF

Note: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.

Package Dimensions

Unit: mm



Weight: 0.06 g (typ.)

	Package Name(s)
Nickname: TSSOP14	



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