CMOS Digital Integrated Circuits Silicon Monolithic

TC74VCX245FT

1. Functional Description

• Low-Voltage Octal Bus Transceiver with 3.6-V Tolerant Inputs and Outputs

2. General

The TC74VCX245FT is a high performance CMOS octal bus transceiver which is guaranteed to operate from 1.2 V to 3.6 V. Designed for use in 1.5 V, 1.8 V, 2.5 V or 3.3 V systems, it achieves high speed operation while maintaining the CMOS low power dissipation.

It is also designed with over voltage tolerant inputs and outputs up to $3.6\ \mathrm{V}.$

The direction of data transmission is determined by the level of the DIR inputs. The \overline{OE} inputs can be used to disable the device so that the busses are effectively isolated.

All inputs are equipped with protection circuits against static discharge.

3. Features (Note)

- (1) Wide operating temperature range: T_{opr} = -40 to 125 °C (Note 1)
- (2) Low-voltage operation: V_{CC} = 1.2 to 3.6 V
- (3) High-speed operation: $t_{pd} = 3.5 \text{ ns} (\text{max}) (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$

$$\begin{split} t_{pd} &= 4.2 \text{ ns (max)} \ (V_{CC} = 2.3 \text{ to } 2.7 \text{ V}) \\ t_{pd} &= 8.4 \text{ ns (max)} \ (V_{CC} = 1.65 \text{ to } 1.95 \text{ V}) \\ t_{pd} &= 16.8 \text{ ns (max)} \ (V_{CC} = 1.4 \text{ to } 1.6 \text{ V}) \end{split}$$

- t_{pd} = 42.0 ns (max) (V_{CC} = 1.2 V)
- (4) Output current: $I_{OH}/I_{OL} = \pm 24 \text{ mA} \text{ (min)} (V_{CC} = 3.0 \text{ V})$
 - I_{OH}/I_{OL} = ±18 mA (min) (V_{CC} = 2.3 V)

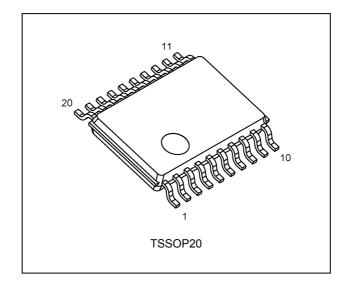
 $I_{OH}/I_{OL} = \pm 6 \text{ mA} \text{ (min)} (V_{CC} = 1.65 \text{ V})$

$$I_{OH}/I_{OL}$$
 = ±2 mA (min) (V_{CC} = 1.4 V)

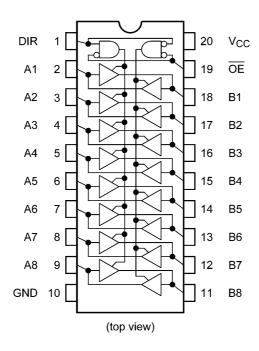
- (5) Bidirectional interface between 2.5 V and 3.3 V signals
- (6) 3.6 V tolerant function and power-down protection provided on all inputs and outputs.
- Note: Do not apply a signal to any bus pins when it is in the output mode. Damage may result. All floating (high impedance) bus pins must have their input levels fixed by means of pull-up or pull-down resistors.
- Note 1: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.

Rev.3.0

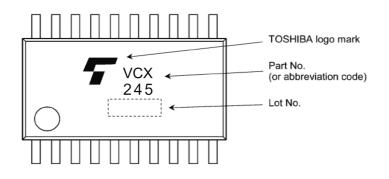
4. Packaging



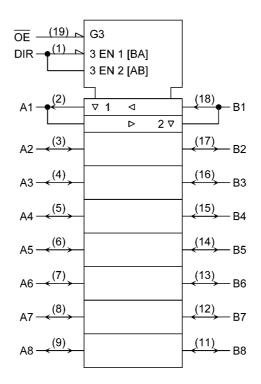
5. Pin Assignment



6. Marking



7. IEC Logic Symbol



8. Truth Table

Input OE	Input DIR	Outputs	Function A-Bus	Function B-Bus
L	L	A = B	Output	Input
L	Н	B = A	Input	Output
н	Х	Z	Z	Z

X: Don't care

Z: High impedance

9. Absolute Maximum Ratings (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CC}		-0.5 to 4.6	V
Input voltage(DIR/OE)	V _{IN}		-0.5 to 4.6	V
Bus I/O voltage	V _{I/O}	(Note 1)	-0.5 to 4.6	V
		(Note 2)	-0.5 to V _{CC} + 0.5	
Input diode current	I _{IK}		-50	mA
Output diode current	I _{ОК}	(Note 3)	±50	mA
Output current	I _{OUT}		±50	mA
Power dissipation	PD	(Note 4)	180	mW
V _{CC} /ground current	I _{CC} /I _{GND}		±100	mA
Storage temperature	T _{stg}		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Output in OFF state.

Note 2: High (H) or Low (L) state. $I_{\mbox{OUT}}$ absolute maximum rating must be observed.

Note 3: V_{OUT} < GND, V_{OUT} > V_{CC}

Note 4: 180 mW in the range of $T_a = -40$ to 85 °C. From $T_a = 85$ to 125 °C a derating factor of -3.25 mW/°C shall be applied until 50 mW.

10. Operating Ranges (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CC}		1.2 to 3.6	V
Input voltage(DIR/OE)	V _{IN}		-0.3 to 3.6	V
Bus I/O voltage	V _{I/O}	(Note 1)	0 to 3.6	V
		(Note 2)	0 to V _{CC}	
Output current	I _{OH} ,I _{OL}	(Note 3)	±24	mA
		(Note 4)	±18	
		(Note 5)	±6	
		(Note 6)	±2	
Operating temperature	T _{opr}	(Note 7)	-40 to 125	°C
Input rise and fall times	dt/dv	(Note 8)	0 to 10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs and bus inputs must be tied to either V_{CC} or GND. Please connect both bus inputs and the bus outputs with V_{CC} or GND when the I/O of the bus terminal changes by the function. In this case, please note that the output is not short-circuited.

Note 1: Output in OFF state.

Note 2: High (H) or Low (L) state.

Note 3: V_{CC} = 3.0 to 3.6 V

Note 4: V_{CC} = 2.3 to 2.7 V

Note 5: V_{CC} = 1.65 to 1.95 V

Note 6: V_{CC} = 1.4 to 1.6 V

Note 7: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.

Note 8: V_{IN} = 0.8 to 2.0 V , V_{CC} = 3.0 V

11. Electrical Characteristics

11.1. DC Characteristics (Unless otherwise specified, $T_a = -40$ to 85°C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
High-level input voltage	VIH	—		1.2 to 1.4	$V_{CC} \times 0.8$	_	V
				1.4 to 1.65	$V_{CC} imes 0.65$	_	
				1.65 to 2.3	$V_{CC} imes 0.65$	_	
				2.3 to 2.7	1.6	_	
				2.7 to 3.6	2.0	_	
Low-level input voltage	VIL	—		1.2 to 1.4	—	$V_{CC} \times 0.05$	V
				1.4 to 1.65	—	$V_{CC} \times 0.05$	
				1.65 to 2.3	—	$V_{CC} \times 0.2$	
				2.3 to 2.7	_	0.7	
				2.7 to 3.6	_	0.8	
High-level output voltage	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -100 μA	1.2	V _{CC} - 0.1	_	V
				1.4 to 1.65	V _{CC} - 0.2	_	
				1.65 to 3.6	V _{CC} - 0.2	_	
			I _{OH} = -2 mA	1.4	1.05	_	
			I _{OH} = -6 mA	1.65	1.25	_	
				2.3	2.0		
			I _{OH} = -12 mA	2.3	1.8	_	
				2.7	2.2	—	
			I _{OH} = -18 mA	2.3	1.7	_	
				3.0	2.4	_	
			I _{OH} = -24 mA	3.0	2.2	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 100 μA	1.2	—	0.05	V
				1.4 to 1.65	—	0.05	
				1.65 to 3.6	—	0.2	
			I _{OL} = 2 mA	1.4	—	0.35	
			I _{OL} = 6 mA	1.65	—	0.3	
			I _{OL} = 12 mA	2.3	—	0.4	
				2.7	—	0.4	
			I _{OL} = 18 mA	2.3	—	0.6	
				3.0	—	0.4	
			I _{OL} = 24 mA	3.0	—	0.55	
Input leakage current	I _{IN}	V _{IN} = 0 to 3.6 V	•	1.2 to 3.6	—	±5.0	μA
3-state output OFF-state leakage current	I _{OZ}	V _{IN} = V _{IH} or V _{IL} V _{OUT} = 0 to 3.6 V		1.2 to 3.6	—	±10.0	μA
Power-OFF leakage current	I _{OFF}	V_{IN}/V_{OUT} = 0 to 3.6 V		0	_	10.0	μA
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		1.2 to 3.6	_	20.0	μA
		$V_{CC} \le (V_{IN}/V_{OUT}) \le 3.6 \text{ V}$		1.2 to 3.6	—	±20.0	
	Δl _{CC}	V _{IH} = V _{CC} - 0.6 V (per input)		2.7 to 3.6	_	750	μA

11.2. DC Characteristics (Note) (Unless otherwise specified, $T_a = -40$ to 125 °C)

Characteristics	Symbol	Test Condition	n	V _{CC} (V)	Min	Max	Unit
High-level input voltage	VIH	—		1.2 to 1.4	$V_{CC} imes 0.8$	_	V
				1.4 to 1.65	$V_{CC} imes 0.65$	_	
				1.65 to 2.3	$V_{CC} imes 0.65$	_	
				2.3 to 2.7	1.6	_	
				2.7 to 3.6	2.0	_	
Low-level input voltage	VIL	—		1.2 to 1.4	_	$V_{CC} \times 0.05$	V
				1.4 to 1.65	_	$V_{CC} \times 0.05$	
				1.65 to 2.3	_	$V_{CC} \times 0.2$	
				2.3 to 2.7	_	0.7	
				2.7 to 3.6	_	0.8	
High-level output voltage	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -100 μA	1.2	V _{CC} - 0.1	_	V
				1.4 to 1.65	V _{CC} - 0.2	_	
				1.65 to 3.6	V _{CC} - 0.2	_	
			I _{OH} = -2 mA	1.4	1.05	_	
			I _{OH} = -6 mA	1.65	1.25	_	
				2.3	2.0	_	
			I _{OH} = -12 mA	2.3	1.8	_	
				2.7	2.2	_	
			I _{OH} = -18 mA	2.3	1.6	_	
				3.0	2.4	_	
			I _{OH} = -24 mA	3.0	2.2	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 100 μA	1.2	_	0.05	V
				1.4 to 1.65	_	0.05	
				1.65 to 3.6	_	0.2	
			I _{OL} = 2 mA	1.4	_	0.35	
			I _{OL} = 6 mA	1.65	_	0.3	
			I _{OL} = 12 mA	2.3	_	0.4	
				2.7	_	0.4	
			I _{OL} = 18 mA	2.3	_	0.8	
				3.0	_	0.4	
			I _{OL} = 24 mA	3.0	_	0.55	
Input leakage current	I _{IN}	V _{IN} = 0 to 3.6 V		1.2 to 3.6	_	±20.0	μA
3-state output OFF-state leakage current	I _{OZ}	V _{IN} = V _{IH} or V _{IL} V _{OUT} = 0 to 3.6 V		1.2 to 3.6	_	±40.0	μA
Power-OFF leakage current	I _{OFF}	V_{IN}/V_{OUT} = 0 to 3.6 V		0	—	40.0	μA
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		1.2 to 3.6	—	80.0	μA
		$V_{CC} \le (V_{IN}/V_{OUT}) \le 3.6 \text{ V}$		1.2 to 3.6	_	±80.0	
	Δl _{CC}	V _{IH} = V _{CC} - 0.6 V (per input)		2.7 to 3.6	_	1.5	mA

Note: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.

11.3. AC Characteristics (Unless otherwise specified, $T_a = -40$ to 85°C)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}		See 11.7 AC Test Circuit,	1.2	1.5	42.0	ns
			Table 11.7.1, Fig. 11.8.1, Table 11.8.1	1.5 ± 0.1	1.0	16.8	
				$\textbf{1.8} \pm \textbf{0.15}$	1.5	8.4	
				2.5 ± 0.2	0.8	4.2	
				$\textbf{3.3}\pm\textbf{0.3}$	0.6	3.5	
3-state output enable time	t _{PZL} ,t _{PZH}		See 11.7 AC Test Circuit,	1.2	1.5	49.0	ns
			Table 11.7.1, Fig. 11.8.2, Table 11.8.1	1.5 ± 0.1	1.0	19.6	
				1.8 ± 0.15	1.5	9.8	
				2.5 ± 0.2	0.8	5.6	
				3.3 ± 0.3	0.6	4.5	
3-state output disable time	t _{PLZ} ,t _{PHZ}		See 11.7 AC Test Circuit,	1.2	1.5	36.0	ns
			Table 11.7.1, Fig. 11.8.2, Table 11.8.1	1.5 ± 0.1	1.0	14.4	
				1.8 ± 0.15	1.5	7.2	
				2.5 ± 0.2	0.8	4.0	
				3.3 ± 0.3	0.6	3.6	
Output skew	t _{osLH} ,t _{osHL}	(Note 1)	_	1.2	_	1.5	ns
				1.5 ± 0.1		1.5	
				1.8 ± 0.15	_	0.5	
				2.5 ± 0.2	-	0.5	
				$\textbf{3.3}\pm\textbf{0.3}$		0.5	

Note 1: Parameter guaranteed by design. ($t_{osLH} = |t_{PLH}m-t_{PLH}n|$, $t_{osHL} = |t_{PHL}m-t_{PHL}n|$)

11.4. AC Characteristics (Note) (Unless otherwise specified, T_a = -40 to 125 °C)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}		See 11.7 AC Test Circuit,	1.2	1.5	55.0	ns
			Table 11.7.1, Fig. 11.8.1, Table 11.8.1	1.5 ± 0.1	1.0	21.4	
				1.8 ± 0.15	1.5	10.0	
				2.5 ± 0.2	0.8	5.0	
				$\textbf{3.3}\pm\textbf{0.3}$	0.6	4.2	
3-state output enable time	t _{PZL} ,t _{PZH}		See 11.7 AC Test Circuit,	1.2	1.5	60.0	ns
			Table 11.7.1, Fig. 11.8.2, Table 11.8.1	1.5 ± 0.1	1.0	23.2	
				1.8 ± 0.15	1.5	11.6	
				2.5 ± 0.2	0.8	6.5	
				$\textbf{3.3}\pm\textbf{0.3}$	0.6	5.4	
3-state output disable time	t _{PLZ} ,t _{PHZ}		See 11.7 AC Test Circuit,	1.2	1.5	45.0	ns
			Table 11.7.1, Fig. 11.8.2, Table 11.8.1	1.5 ± 0.1	1.0	17.9	
				1.8 ± 0.15	1.5	9.0	
				2.5 ± 0.2	0.8	5.0	
				3.3 ± 0.3	0.6	4.5	
Output skew	t _{osLH} ,t _{osHL}	(Note 1)	_	1.2	_	2.0	ns
				1.5 ± 0.1	_	2.0	
				1.8 ± 0.15	_	1.0	
				2.5 ± 0.2	_	1.0	
				3.3 ± 0.3		1.0	

Note: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.

Note 1: Parameter guaranteed by design. ($t_{osLH} = |t_{PLH}m-t_{PLH}n|, t_{osHL} = |t_{PHL}m-t_{PHL}n|$)

11.5. Dynamic Switching Characteristics (Note) (Unless otherwise specified, T_a = 25℃, Input: t_r = t_f = 2.0 ns, C_L = 30 pF)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic V _{OL}	V _{OLP}	V _{IH} = 1.8 V, V _{IL} = 0 V	1.8	0.25	V
		V _{IH} = 2.5 V, V _{IL} = 0 V	2.5	0.6	
		V _{IH} = 3.3 V, V _{IL} = 0 V	3.3	0.8]
Quiet output minimum dynamic V _{OL}	V _{OLV}	V _{IH} = 1.8 V, V _{IL} = 0 V	1.8	-0.25	V
		V _{IH} = 2.5 V, V _{IL} = 0 V	2.5	-0.6	
		V _{IH} = 3.3 V, V _{IL} = 0 V	3.3	-0.8]
Quiet output minimum dynamic V _{OH}	V _{OHV}	V _{IH} = 1.8 V, V _{IL} = 0 V	1.8	1.5	V
		V _{IH} = 2.5 V, V _{IL} = 0 V	2.5	1.9	
		V _{IH} = 3.3 V, V _{IL} = 0 V	3.3	2.2	

Note: Parameter guaranteed by design.

11.6. Capacitive Characteristics (Unless otherwise specified, $T_a = 25^{\circ}C$)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}		—	1.8, 2.5, 3.3	6	pF
Bus I/O capacitance	C _{I/O}		—	1.8, 2.5, 3.3	7	pF
Power dissipation capacitance	C _{PD}	(Note 1)	f _{IN} = 10 MHz	1.8, 2.5, 3.3	20	pF

Note 1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.

 $I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{IN} + I_{CC}/8$ (per 1 gate)

11.7. AC Test Circuit

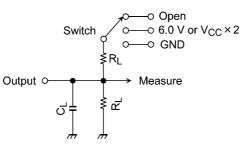


Table 11.7.1 Parameter for AC Test Circuit

Parameter	Switch	Test Condition
t _{PLH} , t _{PHL}	OPEN	—
t _{PLZ} , t _{PZL}	6.0 V	V_{CC} = 3.3 \pm 0.3 V
	$V_{CC} \times 2$	V_{CC} = 2.5 \pm 0.2 V
		V_{CC} = 1.8 \pm 0.15 V
		V_{CC} = 1.5 \pm 0.1 V
		V _{CC} = 1.2 V
t _{PHZ} , t _{PZH}	GND	_

11.8. AC Waveform

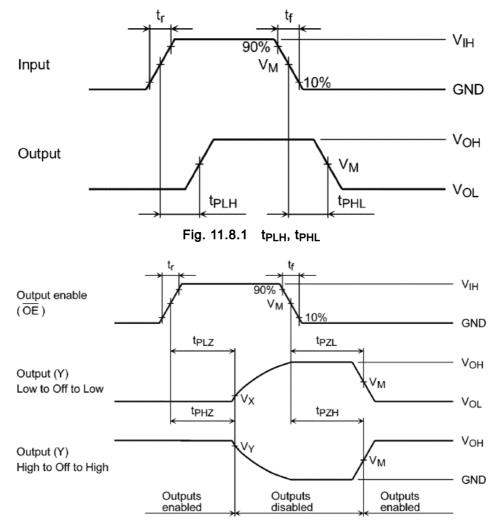


Fig. 11.8.2 t_{PLZ}, t_{PHZ}, t_{PZL}, t_{PZH}

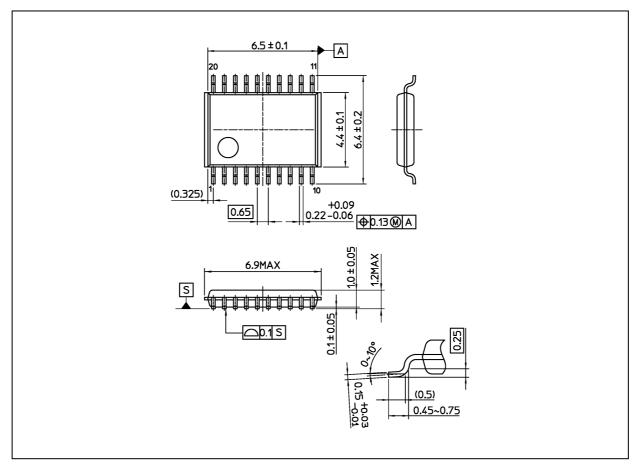
	Symbol	V_{CC} = 3.3 \pm 0.3 V	V_{CC} = 2.5 ± 0.2 V V_{CC} = 1.8 ± 0.15 V	V_{CC} = 1.5 ± 0.1 V V_{CC} = 1.2 V
Input	V _{IH}	2.7 V	V _{CC}	V _{CC}
	V _M	1.5 V	V _{CC} /2	V _{CC} /2
	t _r , t _f	2.0 ns	2.0 ns	2.0 ns
Output	V _M	1.5 V	V _{CC} /2	V _{CC} /2
	V _X	V _{OL} + 0.3 V	V _{OL} + 0.15 V	V _{OL} + 0.15 V
	V _Y	V _{OH} - 0.3 V	V _{OH} - 0.15 V	V _{OH} - 0.15 V
Load	CL	30 pF	30 pF	30 pF
	RL	500 Ω	500 Ω	2 kΩ

Table 11.8.1 AC Waveform Symbols

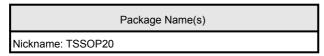
TC74VCX245FT

Package Dimensions

Unit: mm



Weight: 0.08 g (typ.)



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