

CMOS Digital Integrated Circuits Silicon Monolithic

# TC7MBL3253CFK

#### 1. Functional Description

· Dual 1-of-4 FET Multiplexer/Demultiplexer

#### 2. General

The TC7MBL3253CFK is a low-voltage/low-capacitance CMOS dual 1-of-4 Multiplexer/Demultiplexer. The low on-resistance of the switch allows connections to be made with minimal propagation delay time.

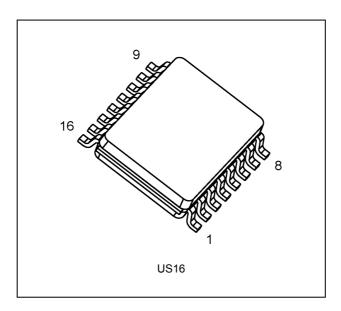
This device consists of two individual four-inputs multiplexer/demultiplexer with common select input (S1, S0) and output enable  $(\overline{OE})$ . The A input is connected to the B1 to B4 outputs as determined by the combination of both the select input (S1, S0) and output enable  $(\overline{OE})$ . When the output enable  $(\overline{OE})$  input is held at "H" level, the switches are open regardless of the state of the select inputs, and a high-impedance state exists between the switches.

All inputs are equipped with protection circuits against static discharge.

#### 3. Features

- (1) Operating voltage:  $V_{CC} = 1.65$  to 3.6 V
- (2) ON capacitance:  $C_{I/O} = 13 \text{ pF Switch On (typ.)} @V_{CC} = 3.0 \text{ V}$
- (3) ON resistance:  $R_{ON} = 9 \Omega$  (typ.) @ $V_{CC} = 3.0 \text{ V}$ ,  $V_{IS} = 0 \text{ V}$
- (4) Power-down protection for inputs  $(\overline{OE}, S1, S0 \text{ and I/O})$
- (5) Package: VSSOP16 (US16)

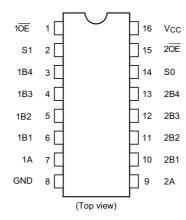
#### 4. Packaging



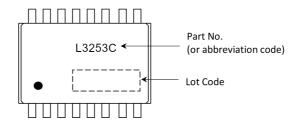
Start of commercial production



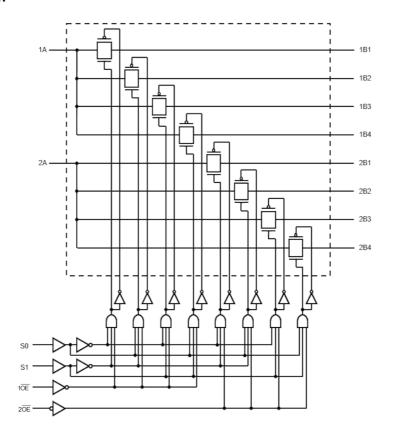
## 5. Pin Assignment



# 6. Marking



# 7. System Diagram





#### 8. Truth Table

Inputs OE	Inputs S1	Inputs S0	Function
L	L	L	A port = B1 port
L	L	Н	A port = B2 port
L	Н	L	A port = B3 port
L	Н	Н	A port = B4 port
Н	Х	Х	Disconnect

X: Don't care

### 9. Absolute Maximum Ratings (Note)

Characteristics	Symbol	Note	Test Condition	Rating	Unit
Supply voltage	V <sub>CC</sub>			-0.5 to 4.6	V
Input voltage (OE, S1, S0)	V <sub>IN</sub>			-0.5 to 4.6	V
Switch I/O voltage	Vs		V <sub>CC</sub> = 0 V or Switch = Off	-0.5 to 4.6	V
			Switch = On	-0.5 to V <sub>CC</sub> +0.5	
Clamp diode current	I <sub>IK</sub>			-50	mA
Switch I/O current	I <sub>S</sub>			50	mA
Power dissipation	P <sub>D</sub>			180	mW
V <sub>CC</sub> /ground current	I <sub>CC</sub> /I <sub>GND</sub>			±100	mA
Storage temperature	T <sub>stg</sub>			-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### 10. Operating Ranges (Note)

Characteristics	Symbol	Note	Test Condition	Rating	Unit
Supply voltage	V <sub>CC</sub>			1.65 to 3.6	V
Input voltage (OE, S1, S0)	V <sub>IN</sub>			0 to 3.6	V
Switch I/O voltage	Vs		V <sub>CC</sub> = 0 V or Switch = Off	0 to 3.6	V
			Switch = On	0 to V <sub>CC</sub>	
Operating temperature	T <sub>opr</sub>			-40 to 85	°C
Input rise time	dt/dv			0 to 10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused control inputs must be tied to either  $V_{\text{CC}}$  or GND.



#### 11. Electrical Characteristics

# 11.1. DC Characteristics (Unless otherwise specified, T<sub>a</sub> = -40 to 85 °C)

Characteristics	Symbol	Note	Test Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Unit
High-level input voltage (OE, S1, S0)	V <sub>IH</sub>		_	1.65 to 3.6	$0.7 \times V_{CC}$	_	_	V
Low-level input voltage (OE, S1, S0)	V <sub>IL</sub>		_	1.65 to 3.6	_	_	$0.3 \times V_{CC}$	V
Input leakage current (OE, S1, S0)	I <sub>IN</sub>		V <sub>IN</sub> = 0 to 3.6 V	1.65 to 3.6	_	_	±1.0	μА
Power-OFF leakage current	I <sub>OFF</sub>		OE, S, A, B = 0 to 3.6 V	0	_	_	10	μА
Switch OFF-state leakage current	I <sub>SZ</sub>		$\frac{A, B = 0 \text{ V to V}_{CC},}{OE = V_{CC}}$	1.65 to 3.6	_	_	±1.0	μА
ON-resistance	R <sub>ON</sub>	(Note 1), (Note 2)	$V_{IS} = 0 V,$ $I_{IS} = 30 \text{ mA}$	3.0	_	9	13	Ω
			$V_{IS} = 3.0 \text{ V},$ $I_{IS} = 30 \text{ mA}$	3.0	_	18	24	
			V <sub>IS</sub> = 2.4 V, I <sub>IS</sub> = 15 mA	3.0	_	20	28	
			V <sub>IS</sub> = 0 V, I <sub>IS</sub> = 24 mA	2.3	_	10	15	
			V <sub>IS</sub> = 2.3 V, I <sub>IS</sub> = 24 mA	2.3	_	23	32	
			V <sub>IS</sub> = 2.0 V, I <sub>IS</sub> = 15 mA	2.3	_	25	35	
			V <sub>IS</sub> = 0 V, I <sub>IS</sub> = 4 mA	1.65	_	12	18	
			V <sub>IS</sub> = 1.65 V, I <sub>IS</sub> = 4 mA	1.65	_	29	40	
Quiescent supply current	I <sub>CC</sub>		$V_{IN} = V_{CC}$ or GND, $I_{OUT} = 0$ A	3.6	_	_	10	μА

Note 1: All typical values are at  $T_a$  = 25 °C.

Note 2: Measured by the voltage drop between A and B pins at the indicated current through the switch. On-resistance is determined by the lower of the voltages on the two (A or B) pins.

# 11.2. AC Characteristics (Unless otherwise specified, T<sub>a</sub> = -40 to 85 °C)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit
Output enable time	$t_{PZL}, t_{PZH}$	See Fig. 11.4., 11.5.1,	$3.3 \pm 0.3$	_	6	ns
(OE to bus)		Table 11.4.1	$2.5\pm0.2$	_	7	
			$1.8 \pm 0.15$	_	11	
Output enable time	$t_{PZL}, t_{PZH}$	See Fig. 11.4., 11.5.1,	$3.3 \pm 0.3$	_	6	ns
(S1, S0 to bus)		Table 11.4.1	$2.5\pm0.2$	_	7	
			1.8 ± 0.15	_	11	
Output disable time	$t_{PLZ}, t_{PHZ}$	t <sub>PLZ</sub> ,t <sub>PHZ</sub> See Fig. 11.4., 11.5.1, Table 11.4.1	$3.3 \pm 0.3$	_	6	ns
(OE to bus)			$2.5\pm0.2$	_	7	
			1.8 ± 0.15	_	11	
Output disable time	ne t <sub>PLZ</sub> ,t <sub>PHZ</sub> See Fig. 11.4	See Fig. 11.4., 11.5.1, Table 11.4.1	$3.3 \pm 0.3$	_	6	ns
(S1, S0 to bus)			$2.5\pm0.2$	_	7	
			$1.8 \pm 0.15$	_	11	



## 11.3. Capacitive Characteristics (Note) (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Input capacitance (OE, S1, S0)	C <sub>IN</sub>	V <sub>IN</sub> = 0 V	3.0	5	pF
Switch terminal OFF-capacitance (Bn)	C <sub>I/O</sub>	$\overline{OE} = V_{CC}, V_{IS} = 0 V$	3.0	4	pF
Switch terminal OFF-capacitance (A)	C <sub>I/O</sub>	$\overline{OE} = V_{CC}, V_{IS} = 0 V$	3.0	9	pF
Switch terminal ON-capacitance (Bn)	C <sub>I/O</sub>	OE = GND, V <sub>IS</sub> = 0 V	3.0	13	pF
Switch terminal ON-capacitance (A)	C <sub>I/O</sub>	OE = GND, V <sub>IS</sub> = 0 V	3.0	13	pF

Note: Parameter guaranteed by design.

#### 11.4. AC Test Circuits

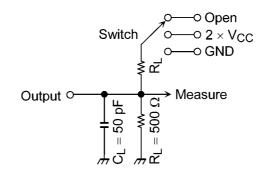


Table 11.4.1 Parameter for AC Test Circuit

Parameter	Switch
$t_{PLZ}$ , $t_{PZL}$	$2\times V_{CC}$
t <sub>PHZ</sub> , t <sub>PZH</sub>	GND

### 11.5. AC Waveform

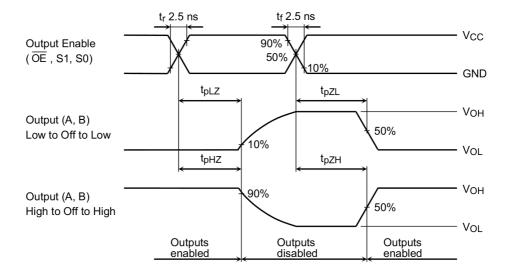


Fig. 11.5.1 AC Waveform t<sub>PLZ</sub>, t<sub>PHZ</sub>, t<sub>PZL</sub>, t<sub>PZH</sub>



#### 12. Rise and Fall Time (t<sub>r</sub>/t<sub>f</sub>)

The  $t_{r(out)}$  and  $t_{f(out)}$  values of the output signals are affected by the CR time constant of the input, which consists of the switch terminal capacitance ( $C_{I/O}$ ) and the on-resistance ( $R_{ON}$ ) of the input.

In practice, the  $t_{r(out)}$  and  $t_{f(out)}$  values are also affected by the circuit's capacitance and resistance components other than those of the TC7MBL3253CFK.

The  $t_{r(out)}/t_{f(out)}$  values can be approximated as follows. (Fig. 12.1, Table 12.1 shows the calculation circuit.)

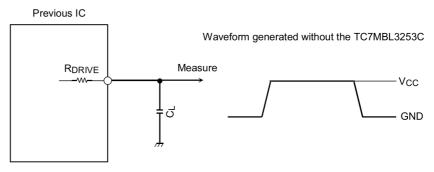
 $t_{r(out)}/t_{f(out)} \ (approx) = - (C_{L/O} + C_L) \ \cdot \ (R_{DRIVE} + R_{ON}) \ \cdot \ ln \ (((V_{OH} - V_{OL}) - V_M) \ / \ (V_{OH} - V_{OL}))$  Where,  $R_{DRIVE}$  is the output impedance of the previous-stage circuit.

#### Calculation example:

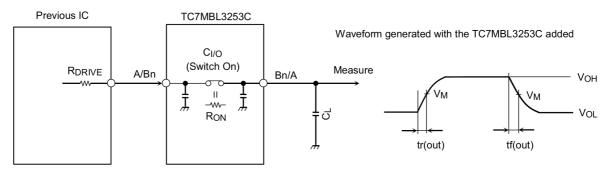
$$t_{r(out)}$$
 (approx) = - (13 + 15) E - 12 · (120 + 9) · ln (((3.0 - 0) - 1.5) / (3.0 - 0))  $\approx 2.5$  ns

#### Calculation conditions:

 $V_{CC}$  = 3.0 V,  $C_L$  = 15 pF,  $R_{DRIVE}$  = 120  $\Omega$  (output impedance of the previous IC),  $V_M$  = 1.5 V ( $V_{CC}$ /2) Output of the previous IC = digital (i.e., high-level voltage =  $V_{CC}$ , low-level voltage = GND)



RDRIVE = output impedance of the previous IC



RDRIVE = output impedance of the previous IC

Fig. 12.1 Calculation Circuit

Table 12.1 Calculation Circuit

Characteristics	$V_{CC}$ = 3.3 $\pm$ 0.3 $V$	$V_{CC}$ = 2.5 ± 0.2 V	V <sub>CC</sub> = 1.8 ± 0.15 V
$V_{M}$	V <sub>CC</sub> /2	V <sub>CC</sub> /2	V <sub>CC</sub> /2

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## 13. Characteristics Curves (Note)

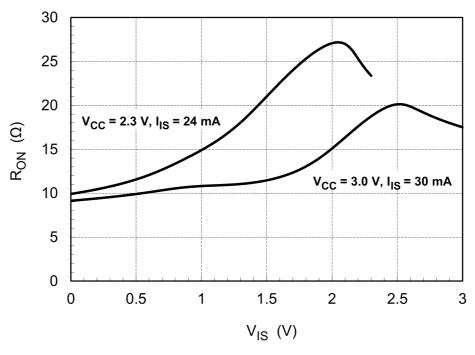


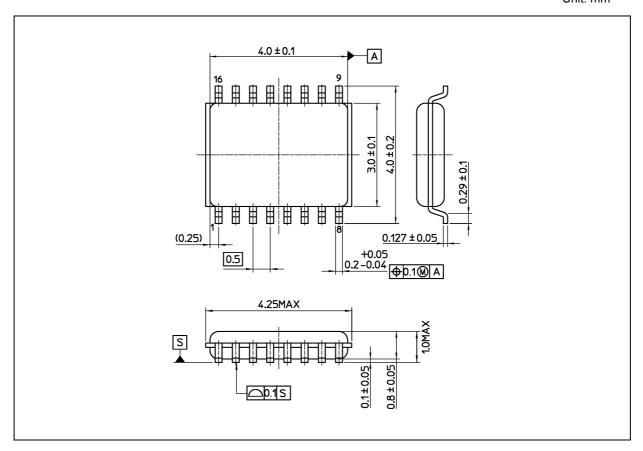
Fig. 13.1  $R_{ON} - V_{IS}$  (tpy.) ( $T_a = 25$  °C)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



# **Package Dimensions**

Unit: mm



Weight: 0.02 g (typ.)

	Package Name(s)
Nickname: US16	



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