MOSFETs Silicon N-channel MOS (U-MOSX-H)

# TPH2R408QM

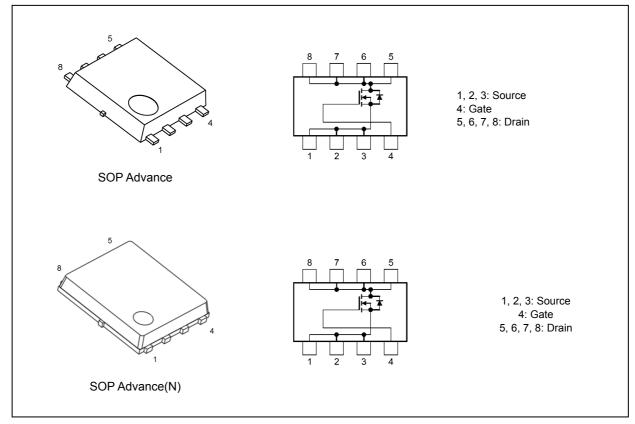
#### 1. Applications

- High-Efficiency DC-DC Converters
- Switching Voltage Regulators
- Motor Drivers

### 2. Features

- (1) High-speed switching
- (2) Small gate charge:  $Q_{SW} = 28 \text{ nC}$  (typ.)
- (3) Small output charge:  $Q_{oss} = 90 \text{ nC}$  (typ.)
- (4) Low drain-source on-resistance:  $R_{DS(ON)} = 1.9 \text{ m}\Omega$  (typ.) (V<sub>GS</sub> = 10 V)
- (5) Low leakage current:  $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 80 \ V)$
- (6) Enhancement mode:  $V_{th}$  = 2.5 to 3.5 V ( $V_{DS}$  = 10 V,  $I_D$  = 1.0 mA)

### 3. Packaging and Internal Circuit



The package can be selected according to your preference. For details, please contact your TOSHIBA sales representative.

#### 4. Absolute Maximum Ratings (Note) ( $T_a = 25 \ ^{\circ}C$ unless otherwise specified)

Characterist	tics		Symbol	Rating	Unit
Drain-source voltage			V <sub>DSS</sub>	80	V
Gate-source voltage			V <sub>GSS</sub>	±20	
Drain current (DC)	( T <sub>c</sub> = 25 °C )	(Note 1)	Ι <sub>D</sub>	120	A
Drain current (DC)	(Silicon limit)	(Note 1), (Note 2)	I <sub>D</sub>	200	
Drain current (pulsed)	( t = 100 µs )	(Note 1)	I <sub>DP</sub>	500	
Power dissipation	( T <sub>c</sub> = 25 °C )		PD	210	W
Power dissipation		(Note 3)	PD	3	]
Power dissipation		(Note 4)	PD	0.96	
Single-pulse avalanche energy		(Note 5)	E <sub>AS</sub>	144	mJ
Single-pulse avalanche current		(Note 5)	I <sub>AS</sub>	120	A
Channel temperature			T <sub>ch</sub>	175	°C
Storage temperature			T <sub>stg</sub>	-55 to 175	]

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note: This product is not designed for radiation resistance or cosmic ray resistance, and these natural environmental factors may affect reliability. In addition, radiation from the constituent materials of the product also becomes a natural environmental factor,

#### 5. Thermal Characteristics

Characteristics			Symbol	Max	Unit
Channel-to-case thermal resistance	( T <sub>c</sub> = 25 °C )		R <sub>th(ch-c)</sub>	0.71	°C/W
Channel-to-ambient thermal resistance	( T <sub>a</sub> = 25 °C )	(Note 3)	R <sub>th(ch-a)</sub>	50	
Channel-to-ambient thermal resistance	( T <sub>a</sub> = 25 °C )	(Note 4)	R <sub>th(ch-a)</sub>	156	

Note 1: Ensure that the channel temperature does not exceed 175 °C.

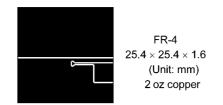
Note 2: Limited by silicon chip capability.

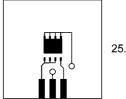
which may affect reliability.

Note 3: Device mounted on a glass-epoxy board (a), Figure 5.1

Note 4: Device mounted on a glass-epoxy board (b), Figure 5.2

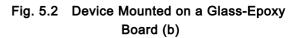
Note 5: V\_DD = 60 V, T\_ch = 25 °C (initial), L = 8.5  $\mu$ H, I<sub>AS</sub> = 120 A





FR-4 25.4 × 25.4 × 1.6 (Unit: mm) 2 oz copper

#### Fig. 5.1 Device Mounted on a Glass-Epoxy Board (a)



Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

## 6. Electrical Characteristics

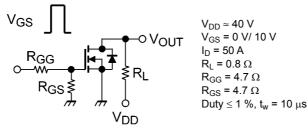
## 6.1. Static Characteristics ( $T_a = 25$ °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±20 V, $V_{DS}$ = 0 V	_	_	±0.1	μA
Drain cut-off current	I <sub>DSS</sub>	$V_{DS}$ = 80 V, $V_{GS}$ = 0 V	_	_	10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	80	_	—	V
Drain-source breakdown voltage (Note 6)	V <sub>(BR)DSX</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = -20 V	60	_	_	
Gate threshold voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1.0 mA	2.5	_	3.5	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 6 V, I <sub>D</sub> = 50 A	_	2.5	3.5	mΩ
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 50 A	_	1.9	2.43	

Note 6: If a reverse bias is applied between gate and source, this device enters V<sub>(BR)DSX</sub> mode. Note that the drainsource breakdown voltage is lowered in this mode.

## 6.2. Dynamic Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	5870	8300	pF
Reverse transfer capacitance	C <sub>rss</sub>		_	60	—	
Output capacitance	C <sub>oss</sub>		_	1340	—	
Gate resistance	r <sub>g</sub>	—	_	1.9	2.9	Ω
Switching time (rise time)	t <sub>r</sub>	See Figure 6.2.1	_	18.4	—	ns
Switching time (turn-on time)	t <sub>on</sub>		_	38	_	
Switching time (fall time)	t <sub>f</sub>		_	34	_	
Switching time (turn-off time)	t <sub>off</sub>		_	138	_	



#### Fig. 6.2.1 Switching Time Test Circuit

### 6.3. Gate Charge Characteristics ( $T_a = 25$ °C unless otherwise specified)

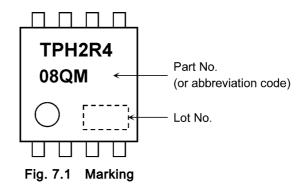
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus	Qg	$V_{DD}\approx 40$ V, $V_{GS}$ = 10 V, $I_{D}$ = 50 A	_	87	_	nC
gate-drain)		$V_{DD} \approx 40$ V, $V_{GS}$ = 6 V, $I_D$ = 50 A	_	55	_	
Gate-source charge 1	Q <sub>gs1</sub>	$V_{DD} \approx 40$ V, $V_{GS}$ = 10 V, $I_D$ = 50 A	_	23	_	
Gate-drain charge	Q <sub>gd</sub>		_	19	_	
Gate switch charge	Q <sub>SW</sub>		—	28	_	
Output charge	Q <sub>oss</sub>	$V_{DS}$ = 40 V, $V_{GS}$ = 0 V, f = 1 MHz		90		

## 6.4. Source-Drain Characteristics ( $T_a = 25$ °C unless otherwise specified)

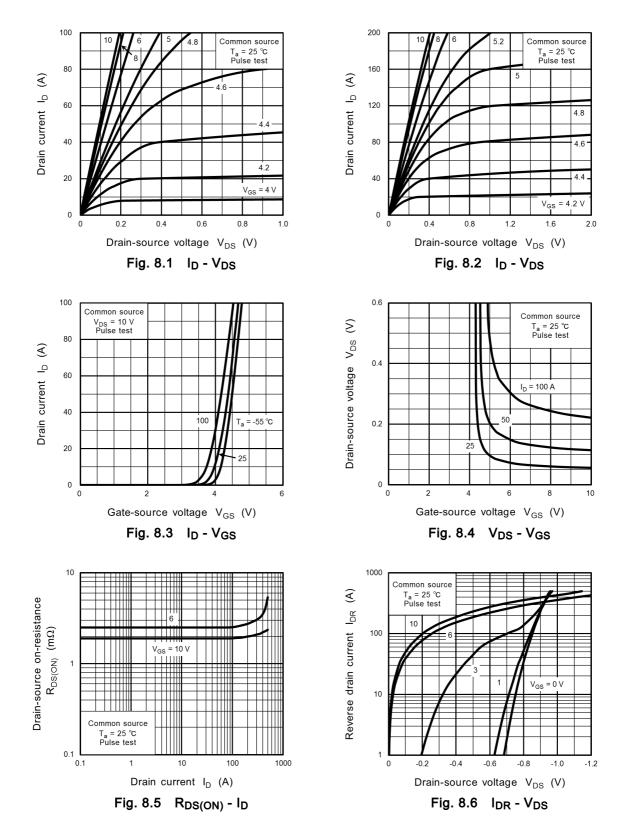
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (pulsed) (	(Note 7)	I <sub>DRP</sub>	(t = 100 μs)	_	—	500	А
Diode forward voltage		V <sub>DSF</sub>	I <sub>DR</sub> = 120 A, V <sub>GS</sub> = 0 V	_	_	-1.2	V
Reverse recovery time		t <sub>rr</sub>	I <sub>DR</sub> = 30 A, V <sub>GS</sub> = 0 V,	_	57	_	ns
Reverse recovery charge		Q <sub>rr</sub>	-dI <sub>DR</sub> /dt = 100 A/μs		74		nC

Note 7: Ensure that the channel temperature does not exceed 175 °C.

### 7. Marking



#### 8. Characteristics Curves (Note)



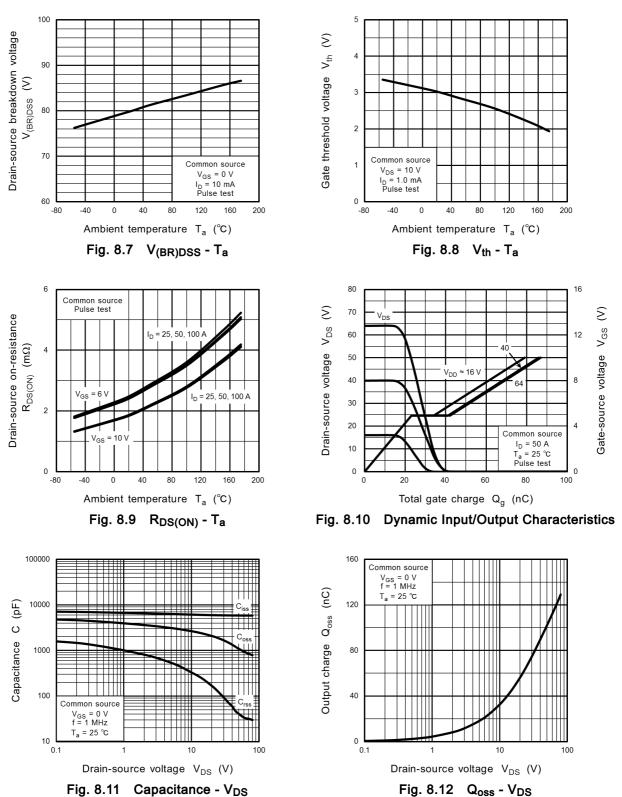
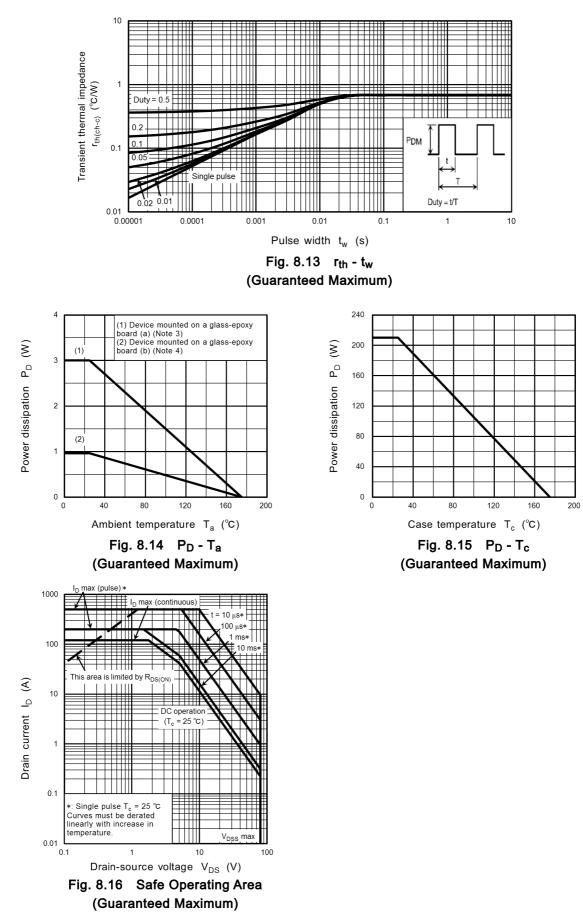


Fig. 8.11 Capacitance - V<sub>DS</sub>

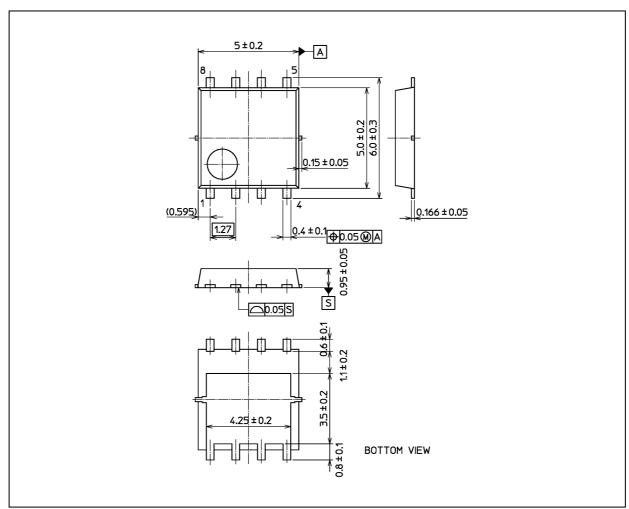


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

## TPH2R408QM

## Package Dimensions

Unit: mm



The package can be selected according to your preference. For details, please contact your TOSHIBA sales representative.

Weight: 0.087 g (typ.)

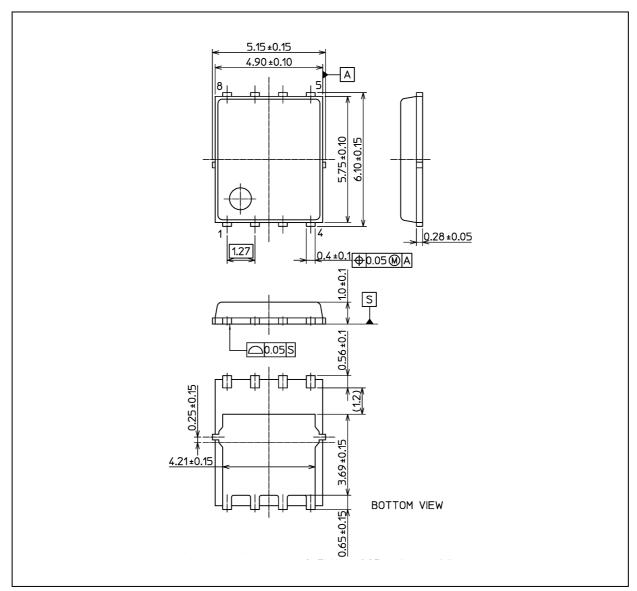
Package Name(s)
TOSHIBA: 2-5Q1S
Nickname: SOP Advance



## TPH2R408QM

#### **Package Dimensions**

Unit: mm



The package can be selected according to your preference. For details, please contact your TOSHIBA sales representative.

Weight: 0.111 g (typ.)

Package Name(s)				
TOSHIBA: 2-5W1A				
Nickname: SOP Advance(N)				

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