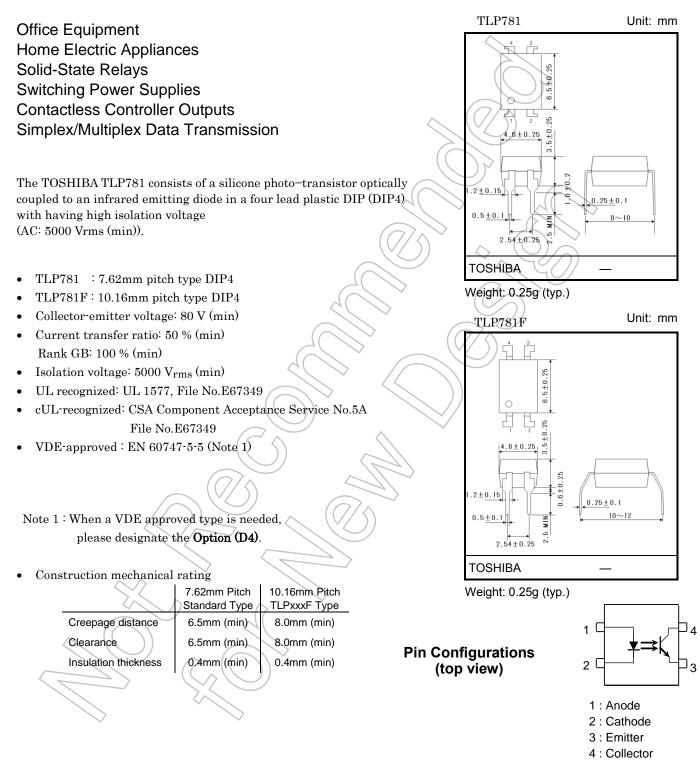
TOSHIBA Photocoupler IRED & Photo-Transistor

TLP781, TLP781F



Current Transfer Ratio

Туре	Classification		sfer Ratio (%) / IF) = 5V, Ta = 25°C	Marking Of Classification
		Min	Max	
	(None)	50	600	Blank, Y+, YE, G, G+, GR, B, BL,GB
	Rank Y	50	150	YE
	Rank GR	100	300	GR
	Rank BL	200	600	BL
TLP781	Rank GB	100	600	GB
	Rank YH	75	150	Y+
	Rank GRL	100	200	G
	Rank GRH	150	300	G+
	Rank BLL	200	400	В

(Note 1): e.g. rank GB: TLP781 (GB)

(Note 2): Application type name for certification test, please use standard product type name, e.g., TLP781 (GB): TLP781

Absolute Maximum Ratings (Ta = 25°C)

	Characteristics	Symbol	Rating	Unit
	Forward current	I IF	60	mA
	Forward current derating (Ta ≥ 39°C)	ΔIF / °C	_0.7	mA / °C
	Pulse forward current (Note 3)	IFP 4		А
LED	Power dissipation	PD	100	mW
	Power dissipation derating (Ta ≥ 39°C)	ΔP _D /°C	-1.0	mW / °C
	Reverse voltage	VR	5	V
	Junction temperature	Тј	125	°C
	Collector-emitter voltage	VCEO	80	V
	Emitter-collector voltage	VECO	7	V
Detector	Collector current	le	50	mA
Dete	Power dissipation	Pc	150	mW
	Power dissipation derating (Ta ≥ 25°C)	ΔP _C / °C	-1.5	mW / °C
	Junction temperature	Тј	125	°C
Ope	rating temperature range	T _{opr}	-55 to 110	°C
Stor	age temperature range	T _{stg}	-55 to 125	°C
Lea	d soldering temperature (10s)	T _{sol}	260	°C
Tota	I package power dissipation	PT	250	mW
	Il package power dissipation derating ≥ 25°C)	ΔP _T / °C	-2.5	mW / °C
Isola	ation voltage (Note 4)	BVS	5000	V _{rms}

(Note): Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

(Note 3): Pulse width:100 μs or less, 100 Hz frequency

(Note 4): AC, 60 s, R.H.≤ 60 %. Apply voltage to LED pin and detector pin together.

Recommended Operating Conditions

Characteristics	Symbol	Min	Тур.	Max	Unit
Supply voltage	V _{CC}	_	5	24	V
Forward current	lF		16	25	mA
Collector current	IC	_	1	10	mA
Operating temperature	Topr	-25	_	85	°C

(Note): Recommended operating conditions are given as a design guideline to obtain expected performance of the device.

Additionally, each item is an independent guideline respectively

In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

	Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	IF = 10 mA	<hr/>	1.15	1.3	V
LED	Reverse current	IR	VR = 5 V	1(77	10	μA
	Capacitance	CT	V = 0 V, f = 1 MHz	(G-	30	—	pF
	Collector-emitter breakdown voltage	V _(BR) CEO	Ic = 0.5 mA	80	/ _	—	V
Detector	Emitter–collector breakdown voltage	V(BR) ECO	JE = 0.1 mA	\mathcal{F}	Ι	—	V
Dete	Collector dark current		V _{CE} = 24 V	_	0.01	0.1	μA
		ID(ICEO)	V _{CE} = 24 V, Ta = 85 °C	_	0.6	50	μA
	Capacitance (collector to emitter)	CCE	V = 0 V, f = 1 MHz	_	10	—	pF

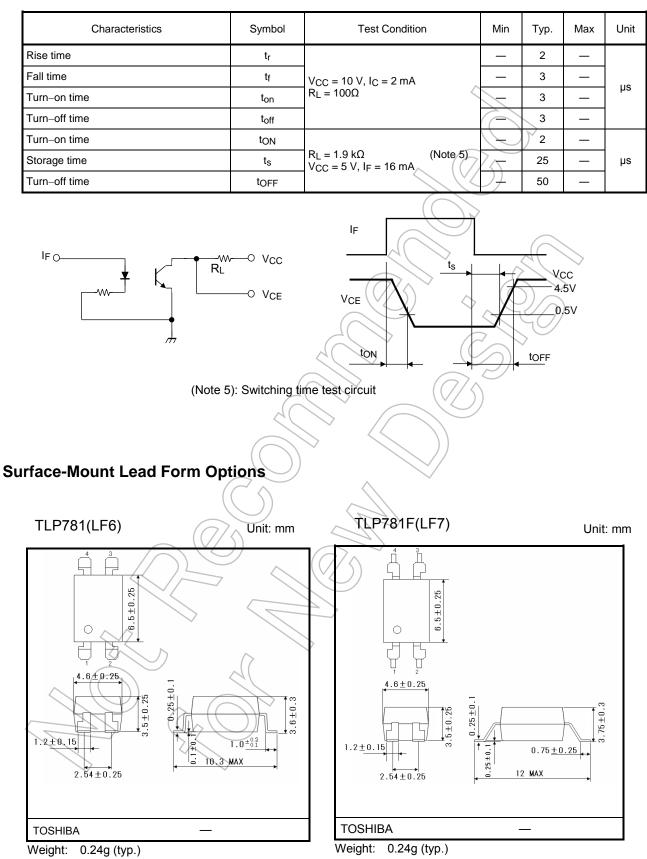
Coupled Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	1	Min	Тур.	Max	Unit
Current transfer ratio	lc / IF	IF = 5 mA, VCE = 5 V		50	_	600	%
			Rank GB	100		600	70
Saturated CTR	IC / IF (sat)	IF = 1 mA, VCE = 0.4 V		—	60		%
Saturated CTR	IC / IF (sat)		Rank GB	30			70
		$I_{C} = 2.4 \text{ mA}, I_{F} = 8 \text{ mA}$		—		0.4	
Collector-emitter saturation voltage	VCE (sat)	IC = 0.2 mA, IF = 1 mA		—	0.2		V
			Rank GB	_	_	0.4	

Isolation Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance (input to output)	Cs	V _S = 0 V, f = 1 MHz	_	0.8	-	pF
Isolation resistance	Rs	V _S = 500 V, R.H. ≤ 60 %	1×10 ¹²	10 ¹⁴	_	Ω
Isolation voltage	BVS	AC, 60 s	5000	_	_	V _{rms}

Switching Characteristics (Ta = 25°C)



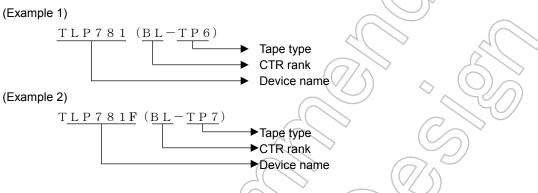
Specifications for Embossed-Tape Packing: (TP6), (TP7)

1. Applicable Package

Package Name	Product Type	
DIP4LF6	TLP781	\langle
DIP4LF7	TLP781F	

2. Product Naming System

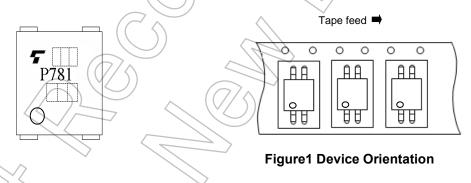
Type of package used for shipment is denoted by a symbol suffix after a product number. The method of classification is as below.



3. Tape Dimensions

3.1 Orientation of Device in Relation to Direction of Tape Movement

Device orientation in the recesses is as shown in Figure 1.



3.2 Tape Packing Quantity: 2000 devices per reel

3.3 Empty Device Recesses Are as Shown in Table 1.

Table1 Empty Device Recesses

>		Standard	Remarks
	Occurrences of 2 or more successive empty device recesses	0 device	Within any given 40mm section of tape, not including leader and trailer
	Single empty device recesses	6 devices (max.) per reel	Not including leader and trailer

3.4 Start and End of Tape

The start of the tape has 30 or more empty holes. The end of the tape has 50 or more empty holes.

3.5 Tape Specification

[1] TLP781 (TP6)

(1)Tape material: Synthetic Resin

(2)Dimensions: The tape dimensions are as shown in Figure 2.

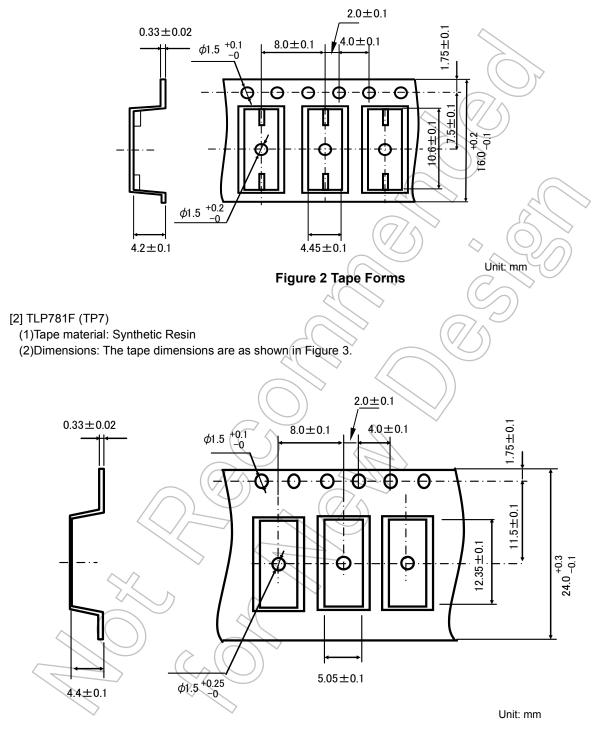


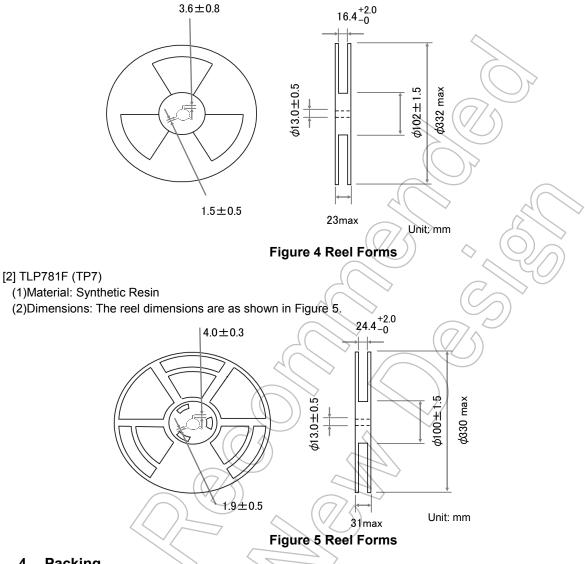
Figure 3 Tape Forms

3.6 Reel Specification

[1] TLP781 (TP6)

(1)Material: Synthetic Resin

(2)Dimensions: The reel dimensions are as shown in Figure 4.



4. Packing

One reel of photocouplers is packed in a shipping carton.

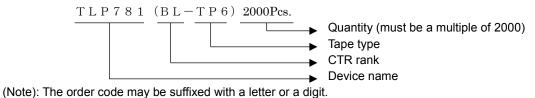
5. Label Indication

The carton bears a label indicating the product number, the symbol representing classification of standard, the quantity, the lot number and the Toshiba company name.

6. Ordering Information

When placing an order, please specify the product number, the CTR rank, the tape type and the quantity as shown in the following example.

(Example)



Please contact your nearest Toshiba sales representative for more details.

Soldering and Storage

1. Soldering

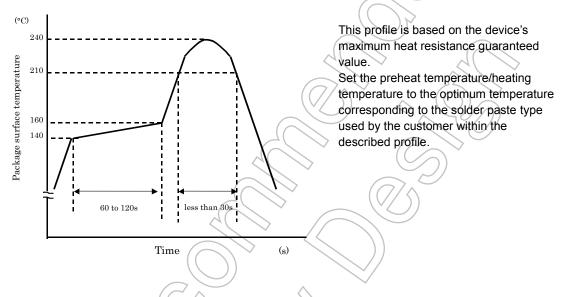
1.1 Soldering

When using a soldering iron or medium infrared ray/hot air reflow, avoid a rise in device temperature as

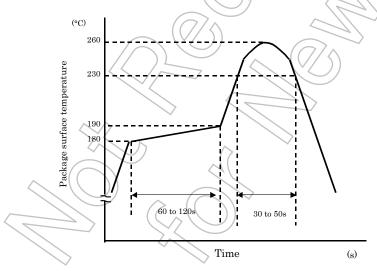
much as possible by observing the following conditions.

1) Using solder reflow

·Temperature profile example of lead (Pb) solder



·Temperature profile example of using lead (Pb)-free solder



This profile is based on the device's maximum heat resistance guaranteed value.

Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

2) Using solder flow (for lead (Pb) solder, or lead (Pb)-free solder)

- Please preheat it at 150°C between 60 and 120 seconds.
- Complete soldering within 10 seconds below 260°C. Each pin may be heated at most once.
- 3) Using a soldering iron

Complete soldering within 10 seconds below 260°C, or within 3 seconds at 350°C. Each pin may be heated at most once.

2. Storage

- 1) Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- 2) Follow the precautions printed on the packing label of the device for transportation and storage.
- 3) Keep the storage location temperature and humidity within a range of 5°C to 35°C and 45% to 75%, respectively.
- 4) Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- 5) Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- 6) When restoring devices after removal from their packing, use anti-static containers.
- 7) Do not allow loads to be applied directly to devices while they are in storage.
- 8) If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

EN 60747-5-5 'Option: (D4)'

Types: TLP781, TLP781F

Type designations for 'option: (D4) ', which are tested under EN 60747 requirements. D4: EN 60747 option

e.g.: TLP781 (D4-GR-LF6,F

GR: CTR rank name

LF6: standard lead bend name

F: [[G]]/RoHS COMPATIBLE (Note 1)

Note: Use TOSHIBA standard type number for safety standard application. e.g. TLP781 (D4-GR-LF6) \rightarrow TLP781

Note 1: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronics equipment.

EN 60747 Isolation Characteristics

		26	
Description	Symbol	Rating	Unit
Application classification			
for rated mains voltage ≤ 300 V _{rms} for rated mains voltage ≤ 600 V _{rms}		I-IV I-III	-
Climatic classification	(7/5)	55 / 115 / 21	_
Pollution degree		2	—
Maximum operating insulation voltage	VIORM	890	Vpk
Input to output test voltage, Vpr = 1.6×V _{IORM} , type and sample test t _p = 10s, partial discharge < 5pC	Vpr	1424	Vpk
Input to output test voltage, Vpr = 1.875×VIORM, 100% production test tp = 1s, partial discharge < 5pC	Vpr	1670	Vpk
Highest permissible overvoltage (transient overvoltage, t _{pr} = 60s)	VTR	6000	Vpk
Safety limiting values (max. permissible ratings in case of fault) current (input current IF, P _{si} = 0) power (output or total power dissipation) temperature	I _{si} Psi Tsi	300 500 150	mA mW °C
Insulation resistance, $V_{10} = 500V$, Ta=25°C	Rsi	≥10 ¹²	Ω

Insulation Related Specifications

		7.62mm pitch TLPxxx type	10.16mm pitch TLPxxxF type
Minimum creepage distance	Cr	6.5mm	8.0mm
Minimum clearance	CI	6.5mm	8.0mm
Minimum insulation thickness	ti	0.4,1	'nm
Comparative tracking index	CTI	17	5

- If a printed circuit is incorporated, the creepage distance and clearance may be reduced below this value.
 (e.g. at a standard distance between soldering eye centres of 7.5mm). If this is not permissible, the user shall take suitable measures.
- (2) This photocoupler is suitable for 'safe electrical isolation' only within the safety limit data. Maintenance of the safety data shall be ensured by means of protective circuits.

VDE test sign: Marking on product for EN 60747

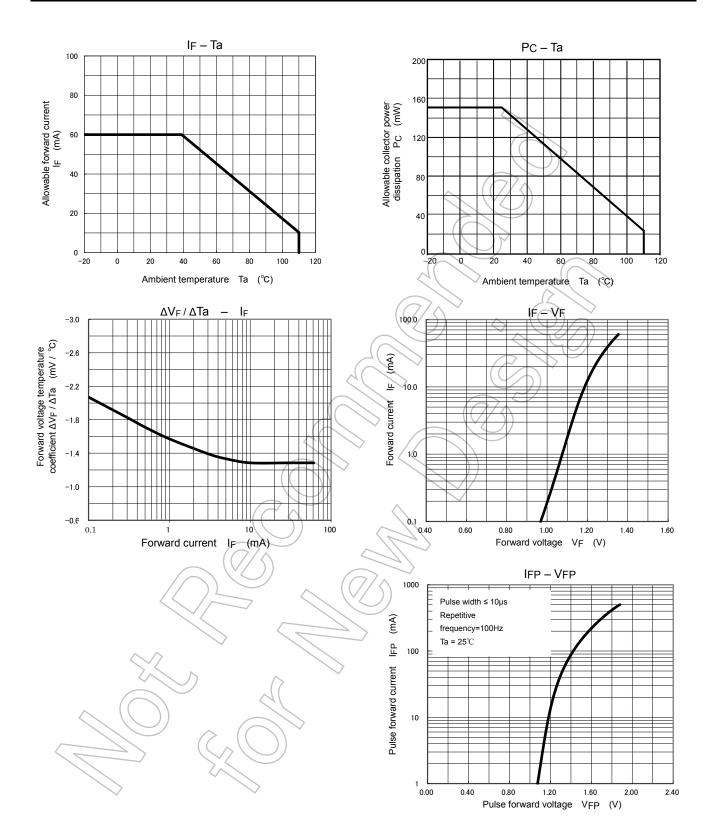
Marking on packing for EN 60747

Marking Example : TLP781, TLP781F

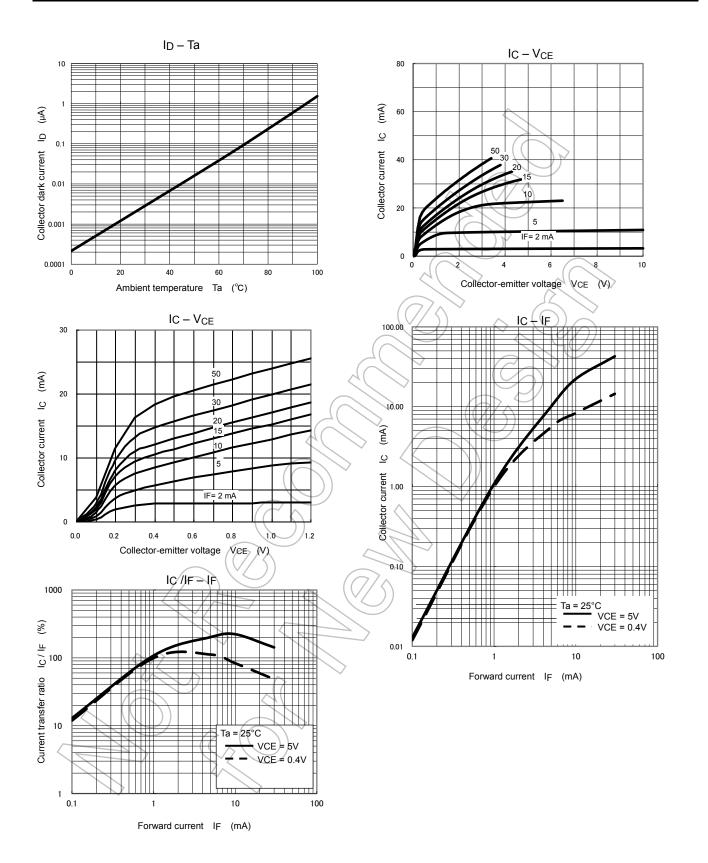
Lot No. P Part No. (or abbreviation code)

4: Mark for option (D4)

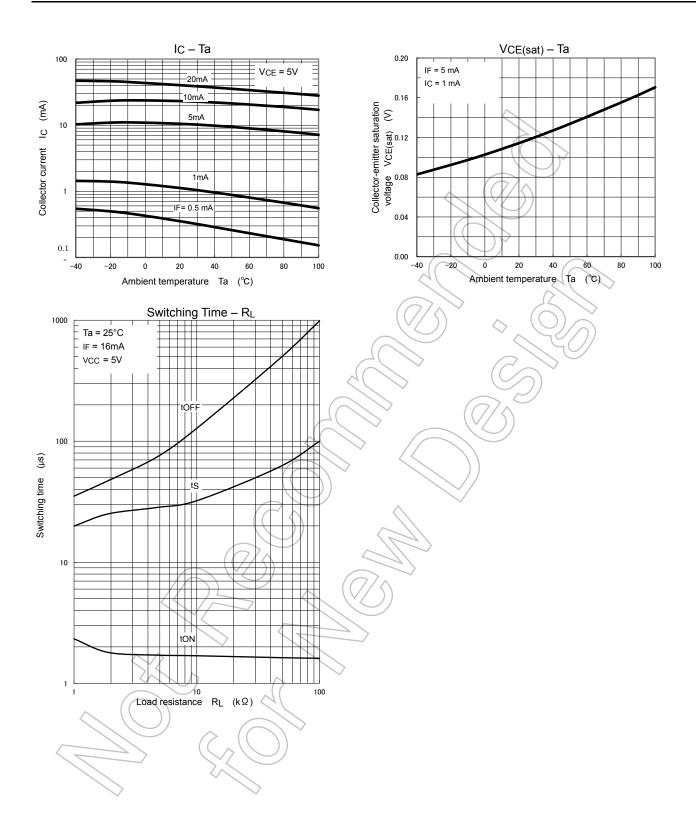
1pin indication



NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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