

SiC Schottky Barrier Diode

# TRS16N65FB

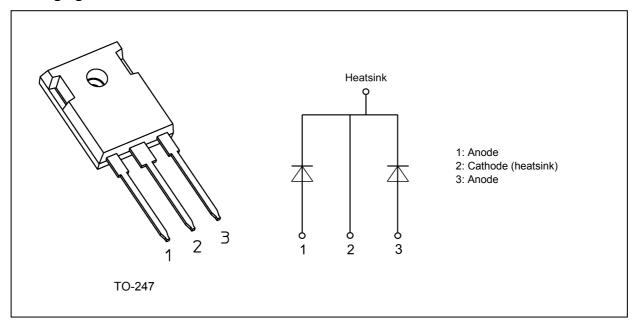
## 1. Applications

- · Power Factor Correction
- · Solar Inverters
- · Uninterruptible Power Supplies
- · DC-DC Converters

#### 2. Features

- (1) Chip design of 2nd generation
- (2) High non-repetitive peak forward surge current:  $I_{FSM}$  (Per Leg) / (Both Legs) = 65 A / 130 A
- (3) Low junction capacitance:  $C_i$  (Per Leg) = 30 pF (typ.)
- (4) Low reverse current:  $I_R$  (Per Leg) = 0.4  $\mu A$  (typ.)

## 3. Packaging and Internal Circuit





## 4. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Note	Test Condition	Rating	Unit
Repetitive peak reverse voltage	$V_{RRM}$			650	V
Forward DC current	I <sub>F(DC)</sub>		Per Leg	8	Α
			Both Legs	16	
Forward pulse current	I <sub>FP</sub>	(Note 1)	Per Leg	80	
			Both Legs	160	
Power dissipation	P <sub>D</sub>	(Note 2)	Per Leg	83	W
			Both Legs	166	
Non-repetitive peak forward surge current	I <sub>FSM</sub>	(Note 3)	Per Leg	65	Α
			Both Legs	130	]
Junction temperature	Tj			175	℃
Storage temperature	T <sub>stg</sub>			-55 to 175	]
Mounting torque	TOR			0.8	N · m

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $t = 50 \mu s$ Note 2:  $T_c = 25 °C$ 

Note 3: f = 50 Hz (half-sine wave, t = 10 ms)

#### 5. Thermal Characteristics

Characteristics	Symbol	Note	Test Condition	Max	Unit
Thermal resistance (junction-to-case)	R <sub>th(j-c)</sub>	(Note 1)	Per Leg	1.8	°C/W
			Both Legs	0.9	
Thermal resistance (junction-to-ambient)	R <sub>th(j-a)</sub>	(Note 2)		50	

Note 1:  $T_c = 25 \,^{\circ}\text{C}$ Note 2:  $T_a = 25 \,^{\circ}\text{C}$ 

# 6. Electrical Characteristics (Unless otherwise specified, Ta = 25 °C) (Per Leg)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Forward voltage	(pulse measurement)	$V_{F}$	I <sub>F</sub> = 4 A	_	1.2	_	V
			I <sub>F</sub> = 8 A	_	1.45	1.6	
Reverse current	(pulse measurement)	I <sub>R</sub>	V <sub>R</sub> = 650 V	_	0.4	40	μА
Junction capacitance		C <sub>j</sub>	V <sub>R</sub> = 400 V, f = 1 MHz	_	30	_	pF
Total junction capacitive charge		Q <sub>cj</sub>	V <sub>R</sub> = 0.1 to 400 V	_	19.4		nC



# 7. Marking (Note)

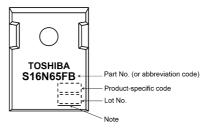


Fig. 7.1 Marking

Note: A line under a Lot No. identifies the indication of product Labels.

[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Abbreviation Code	Part Number			
S16N65FB	TRS16N65FB			



## 8. Characteristics Curves (Note)

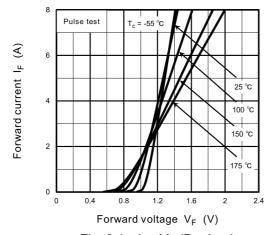


Fig. 8.1 I<sub>F</sub> - V<sub>F</sub> (Per Leg)

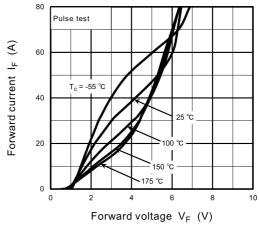


Fig. 8.2 I<sub>F</sub> - V<sub>F</sub> (Per Leg)

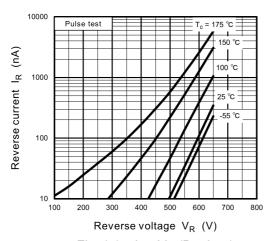


Fig. 8.3 I<sub>R</sub> - V<sub>R</sub> (Per Leg)

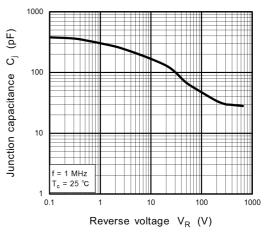


Fig. 8.4 C<sub>j</sub> - V<sub>R</sub> (Per Leg)

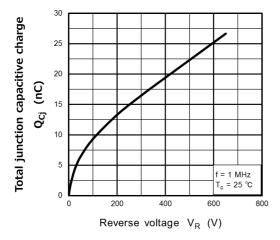


Fig. 8.5 Qci - VR

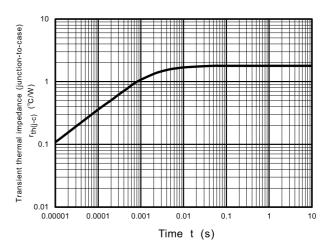


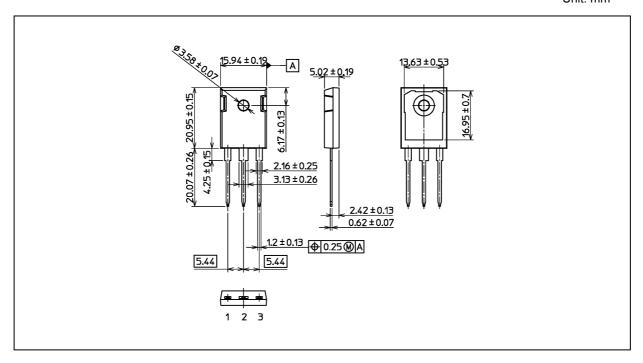
Fig. 8.6 r<sub>th(j-c)</sub> - t (Guaranteed Maximum) (Per Leg)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



# **Package Dimensions**

Unit: mm



Weight: 6.15 g (typ.)

	Package Name(s)
TOSHIBA: 2-16L1A	
Nickname: TO-247	



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