TOSHIBA Photocoupler IRED & Photo-Thyristor

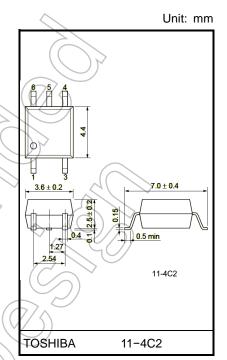
TLP148G

Office Machine Household Use Equipment Solid State Relay Switching Power Supply

The TOSHIBA mini-flat coupler TLP148G is a small outline coupler, suitable for surface mount assembly.

The TLP148G consists of a photo thyristor, optically coupled to an infrared emitting diode.

- Peak off-state voltage: 400 V (min)
- Trigger LED current: 10 mA (max)
- On-state current: 150 mA (max)
- Isolation voltage: 2500 Vrms (min)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349



Weight: 0.09 g (typ.)

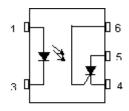
Trigger LED current

	Trigger LED	current (mA)		
Classification*	Vak=6V, Rgk=27kΩ,			Marking of
	// Ta≢25°C			classification
	Min Max			
(IFT7)	- \	7		T7
Standard		10		T7, blank

*Example: "(IFT7)"; "TLP148G(IFT7)"

(Note) When specifying the application type name for certification testing, be sure to use the standard product type name, e.g. TLP148G(IFT7):/ILP148G

Pin Connections



- 1 : Anode
- 3: Cathode
- 4: Cathode
- 5 : Anode.
- 6: Gate

Start of commercial production 2009-06

Absolute Maximum Ratings (Ta = 25°C)

	Characteristics	Symbol	Rating	Unit
	Forward current	lF	50	mA
	Forward current derating (Ta ≥ 53°C)	ΔI _F /°C	-0.7	mA / °C
LED	Peak forward current (100 µs pulse, 100 pps)	lfP	1	Á
쁘	Reverse voltage	VR	5	V (
	Diode power dissipation	P _D	100	mW
	Diode power dissipation derating (Ta ≥ 53°C)	△P _D /°C	-1.4	mW/°C
	Peak forward voltage(R _{GK} = 27kΩ)	VDRM	400	
	Peak reverse voltage(R _{GK} = 27kΩ)	VRRM	400) >
Detector	On-state current	I _{T(RMS)}	150	mA
	On-state current derating (Ta ≥ 25°C)	ΔI _T / °C	-2.0	mA / °C
	Peak on-state current (100 μs pulse, 120 pps)	ITP	3	Α
	Peak one cycle surge current	ITSM	(7/2)	A
	Peak reverse gate voltage	V _{GM}	5	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
	Output power dissipation	Po	150	mW
	Output power dissipation derating (Ta ≥ 25°C)	ΔP₀/°C	-1.5	mW/°C
Operating temperature range		Topr	-40 to 100	ိုင် (၂)
Storage	e temperature range	T _{stg}	-55 to 125	\\^¢
Lead s	oldering temperature (10 s)	T_{sol}	260	ç
Isolatio	on voltage (AC, 60 s, R.H. ≤ 60 %) (Note 1)	BVs	2500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

(Note 1) Device considered a two terminal device: pins 1 and 3 shorted together and pins 4, 5 and 6 shorted together.

Recommended Operating Conditions

Characteristics	Symbol	Min	Тур.	Max	Unit
Supply voltage	Vac	_	_	120	Vac
Forward current	V IF	15	_	25	mA
Operating temperature	Topr	-25	_	85	°C
Gate to cathode resistance	Rgk	_	27	33	kΩ
Gate to cathode capacitance	CGK	_	0.01	0.1	μF

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

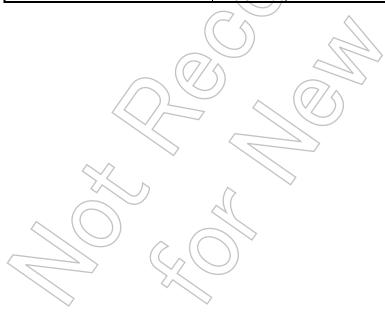


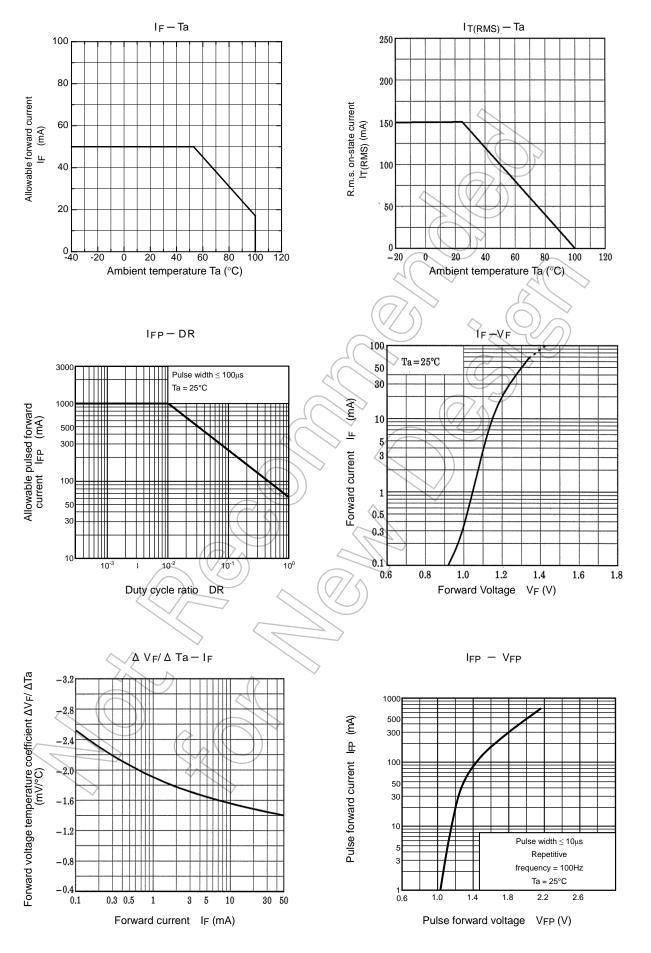
Individual Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition		Min	Тур.	Max	Unit
	Forward voltage	ard voltage V _F I _F = 10 mA		1.0	1.15	1.3	V	
ED	Reverse current	I _R	V _R = 5 V		_	_	10	μΑ
	Capacitance	CT	V _F = 0 V, f = 1 MHz		/-	30	_	pF
Detector	Off-state current	IDRM	VAK = 400 V, RGI			5	μΑ	
	Reverse current	IRRM	VKA = 400 V, RGI)~	5	μΑ	
	On-state voltage	V _{TM}	I _{TM} = 100 mA, I _F) 	1.25	1.45	V	
	Holding current	lн	$R_{GK} = 27 \text{ k}\Omega$	<i>)}</i>	_	1	mA	
	Off-state dv / dt	dv/dt	V _A κ = 280 V, R _G κ = 27 kΩ		15	_	_	V / μs
	l Capacitance	C.	V = 0 V,	Anode to gate	_	5		nΕ
		f = 1 MHz	Gate to cathode	_	500		pF	

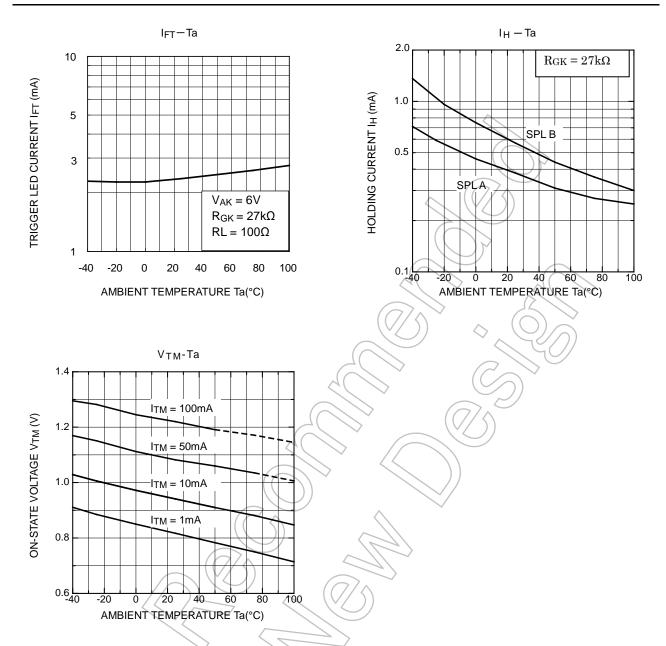
Coupled Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Trigger LED current	lfT	$V_{AK} = 6 \text{ V}, R_{GK} = 27 \text{ k}\Omega$		3	10	mA
Turn-on time	ton	IF = 30 mA, V_{AA} = 50 V, RGK = 27 k Ω		10		μS
Capacitance (input to output)	Cs C	V _S = 0 V, f = 1 MHz)) —	0.8	_	pF
Isolation resistance	Rs	V _S = 500 V, R.H. ≤ 60 %	5×10 ¹⁰	10 ¹⁴	_	Ω
Isolation voltage	BVs	AC, 60 s	2500	_	_	Vrms





NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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