

# 2SK4037

## 470 MHz Band Amplifier Applications

(Note)The TOSHIBA products listed in this document are intended for high frequency Power Amplifier of telecommunications equipment. These TOSHIBA products are neither intended nor warranted for any other use. Do not use these TOSHIBA products listed in this document except for high frequency Power Amplifier of telecommunications equipment.

- Output power:  $P_o = 36.5\text{dBmW}$  (typ)
- Gain:  $G_p = 11.5\text{dB}$  (typ)
- Drain Efficiency:  $\eta_D = 60.0\%$  (typ)

## Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	$V_{DSS}$	12	V
Gate-source voltage	$V_{GSS}$ (Note 1)	3	V
Drain current	$I_D$	3	A
Power dissipation	$P_D$ (Note 2)	20	W
Channel temperature	$T_{ch}$	150	°C
Storage temperature range	$T_{stg}$	-45 to 150	°C

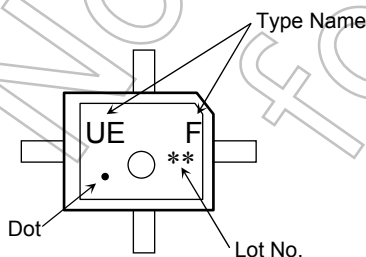
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Operating Ranges: 0 to 3V

Note 2:  $T_c = 25^\circ\text{C}$  (When mounted on a 0.8 mm glass epoxy PCB)

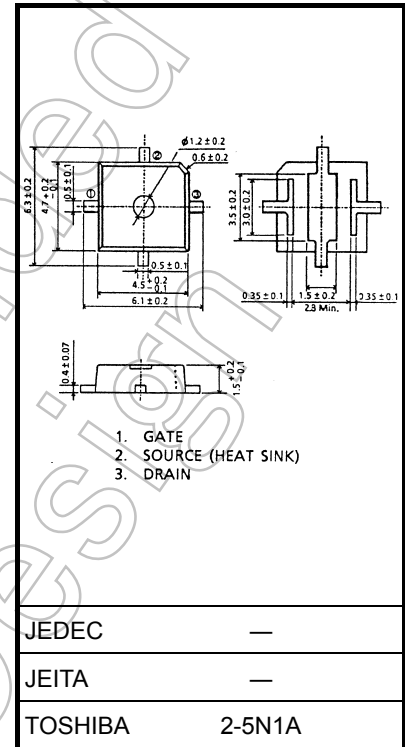
## Marking



**Caution:** This device is sensitive to electrostatic discharge.

Please make enough tool and equipment earthed when you handle.

Unit: mm



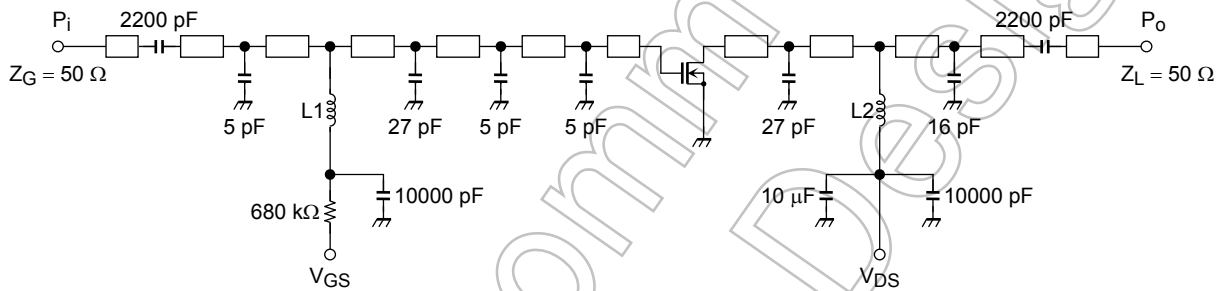
Weight: 0.08 g (typ.)

## Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Output power	$P_O$	$V_{DS} = 6.0\text{ V}$ , $I_{D} = 250\text{ mA}$ ( $V_{GS} = \text{adjust}$ ) $f = 470\text{ MHz}$ , $P_i = 25\text{ dBmW}$ $Z_G = Z_L = 50\ \Omega$	35.5	36.5	—	dBmW
Drain efficiency	$\eta_D$		55.0	60.0	—	%
Power gain	$G_p$		10.5	11.5	—	dB
Threshold voltage	$V_{th}$	$V_{DS} = 6.0\text{ V}$ , $I_D = 0.5\text{ mA}$	—	1.0	1.5	V
Drain cut-off current	$I_{DSS}$	$V_{DS} = 12\text{ V}$ , $V_{GS} = 0\text{ V}$	—	—	10	$\mu\text{A}$
Gate-source leakage current	$I_{GSS}$	$V_{GS} = 3\text{ V}$ , $V_{DS} = 0\text{ V}$	—	—	5	$\mu\text{A}$
Load mismatch (Note 3)	—	$V_{DS} = 6.0\text{ V}$ , $f = 470\text{ MHz}$ , $P_i = 25\text{ dBmW}$ , $P_o = 36.5\text{ dBmW}$ ( $V_{GS} = \text{adjust}$ ) VSWR LOAD 10:1 all phase	No degradation			—

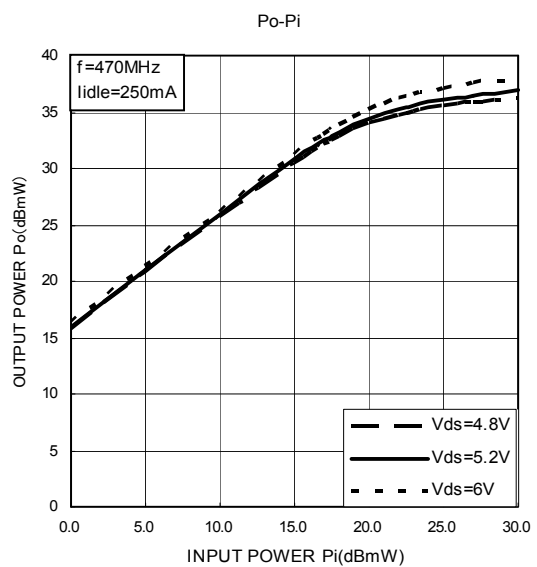
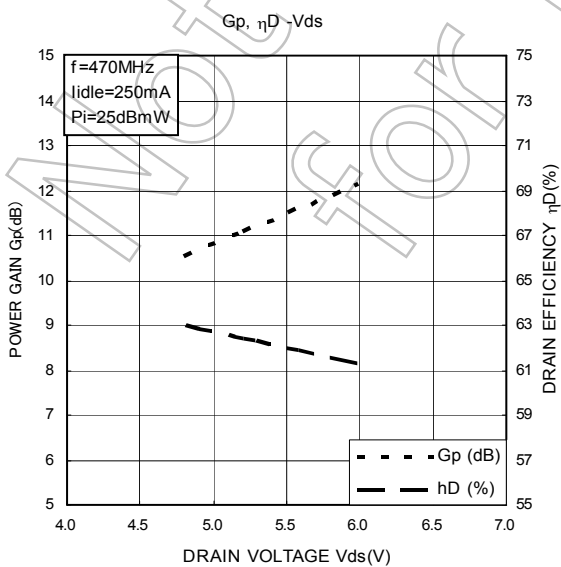
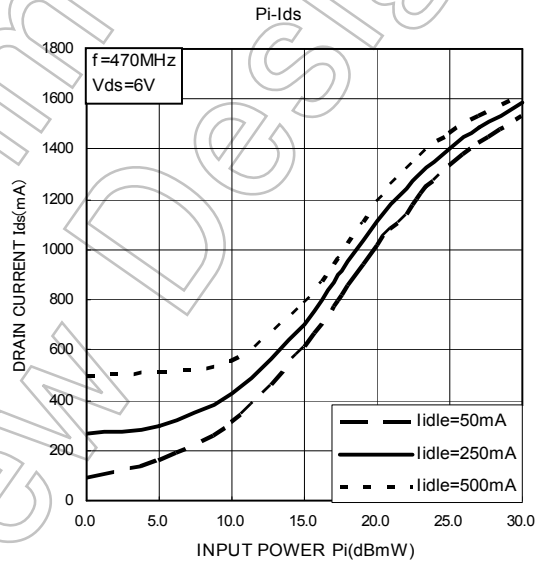
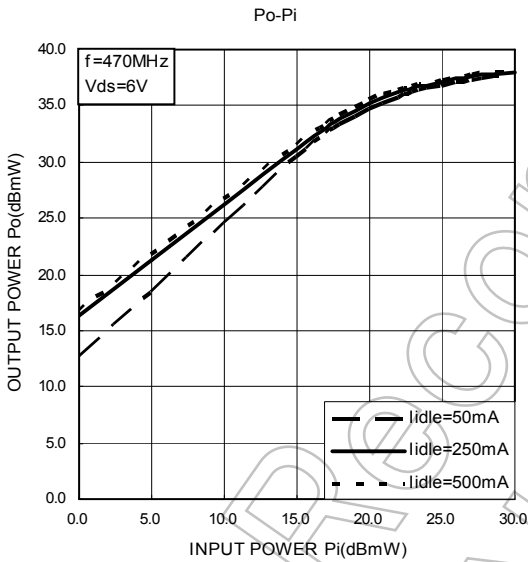
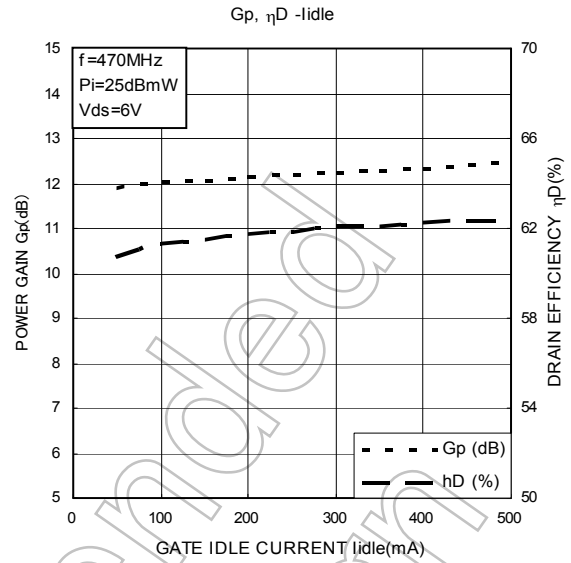
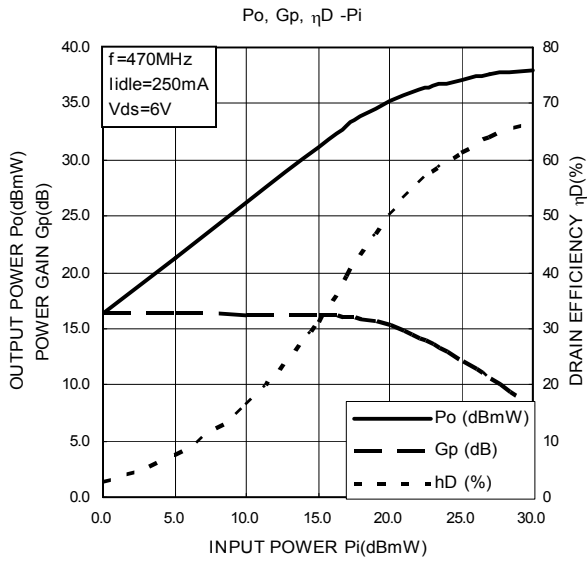
Note 3: These characteristic values are measured using measurement tools specified by Toshiba.

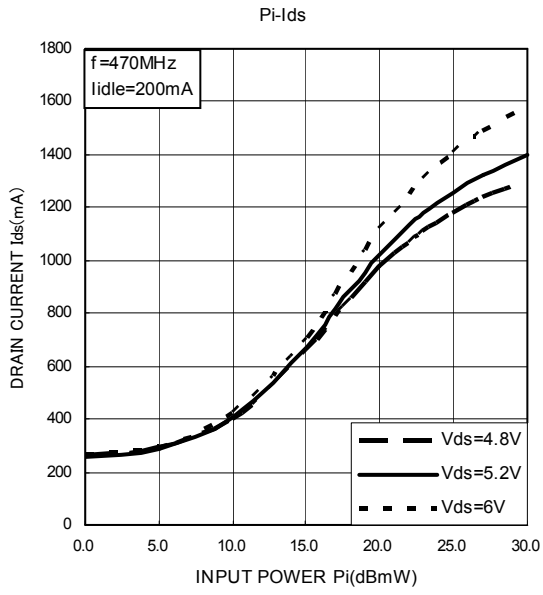
### Test Circuit



L1:  $\phi 0.6\text{ mm}$  enamel wire, 5.8ID, 8T  
L2:  $\phi 0.6\text{ mm}$  enamel wire, 5.8ID, 8T

Line: 2mm





Note 4: These are only typical curves and devices are not necessarily guaranteed at these curves.

Not Recommended for New Design

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