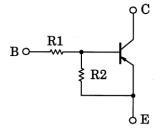
TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT Process) (Bias Resistor built-in Transistor)

RN2130MFV

Switching Applications Inverter Circuit Applications Interface Circuit Applications Driver Circuit Applications

- Ultra-small package, suited to very high density mounting
- Incorporating a bias resistor into the transistor reduces the number of parts, so enabling the manufacture of ever more compact equipment and lowering assembly cost.
- A wide range of resistor values is available for use in various circuits.
- Complementary to the RN1130MFV

Equivalent Circuit



Unit: mm 1.2 ±0.05 A 0.32 ±0.05 З 2 0.22 ±0.05 BOTTOM VIEW 1.BASE 2.EMITTER VESM **3.COLLECTOR** JEDEC JEITA TOSHIBA 1-1Q1S

Weight: 1.5 mg (typ.)

Absolute Maximum Ratings (Ta = 25°C)

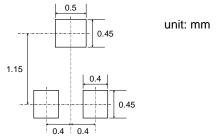
Characterisstic	Symbol	Rating	Unit	
Collector-base voltage	VCBO	-50	V	
Collector-emitter voltage	VCEO	-50	V	
Emitter-base voltage	V _{EBO}	-10	V	
Collector current	IC	-100	mA	
Collector power dissipation	Pc (Note1)	150	mW	
Junction temperature	Tj	150	°C	
Storage temperature range	T _{stg}	-55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1 : Mounted on FR4 board (25.4 mm \times 25.4 mm \times 1.6 mm)

Land Pattern Dimensions (for reference only)



Start of commercial production

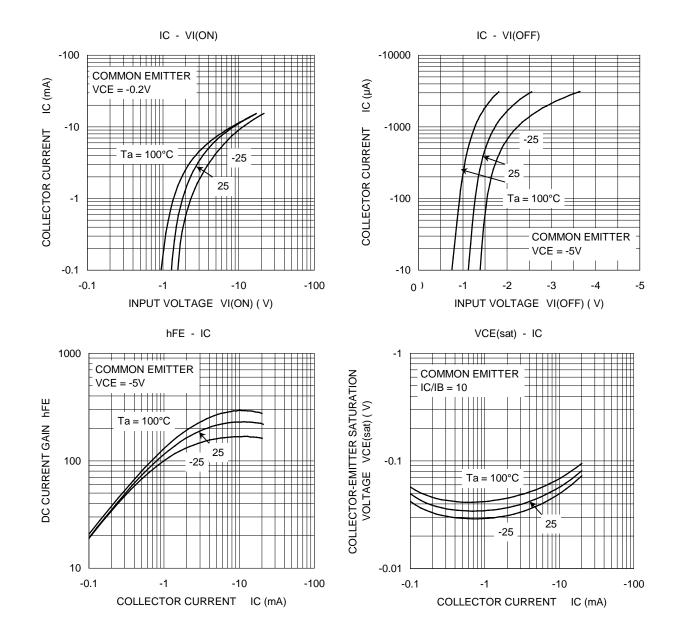
2005-04

Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	$V_{CB} = -50 \text{ V}, \text{ I}_{E} = 0 \text{ A}$	_	_	-100	nA
	ICEO	$V_{CE} = -50 \text{ V}, \text{ I}_{B} = 0 \text{ A}$	_	_	-500	nA
Emitter cut-off current	I _{EBO}	$V_{EB} = -10 \text{ V}, \text{ I}_{C} = 0 \text{ A}$	-38	—	-72	μA
DC current gain	hFE	$V_{CE} = -5 V$, $I_{C} = -10 mA$	100	—	—	_
Collector-emitter saturation voltage	V _{CE (sat)}	$I_{C} = -5 \text{ mA}, I_{B} = -0.5 \text{ mA}$	_	-0.1	-0.3	V
Input voltage (ON)	VI(ON)	V_{CE} = -0.2 V, I _C = -5 mA	-1.7	—	-8.2	V
Input voltage (OFF)	VI(OFF)	$V_{CE} = -5 V$, $I_{C} = -0.1 mA$	-1.0	—	-1.6	V
Collector output capacitance	Cob	$V_{CB} = -10 \text{ V}, \text{ I}_{E} = 0 \text{ A}, \text{ f} = 1 \text{ MH}_{Z}$	_	0.9	—	pF
Input resistor	R1	—	70	100	130	kΩ
Resistor ratio	R1/R2	—	0.8	1.0	1.2	_

TOSHIBA

RN2130MFV





Marking

Type Name	Marking	
RN2130MFV	Type Name Y 2	

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