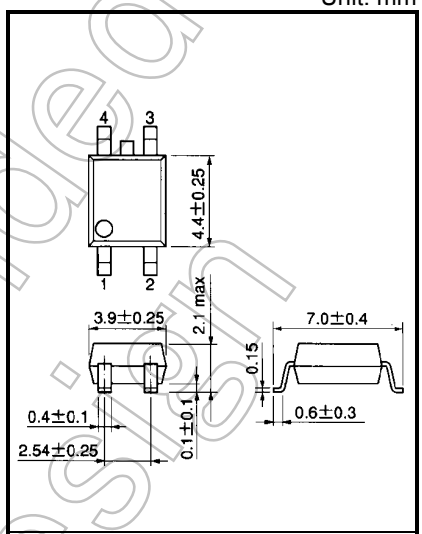
TOSHIBA PHOTOCOUPLER PHOTO RELAY

TLP3118

Measurement Instruments

Unit: mm



JEDEC	—
JEITA	—
TOSHIBA	11−5H1

Weight: 0.1 g (typ.)

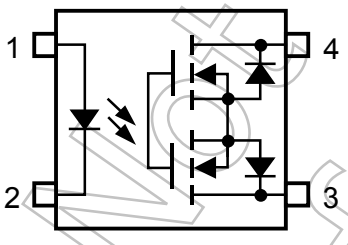
The TOSHIBA TLP3118 mini-flat photorelay is a small-outline photorelay, suitable for surface-mount assembly. The TLP3118 consists of an infrared-emitting diode optically coupled to a photo-MOSFET and is housed in a 4-pin package.

Features

- 4-pin SOP (2.54SOP4): 2.1 mm high, 2.54 mm pitch
- 1-Form-A
- Peak Off-State Voltage: 80 V (min)
- Trigger LED Current: 3 mA (max)
- On-State Current: 40 mA (max)
- On-State Resistance: 25 Ω (max)
- Output Capacitance: 3.5 pF (max)
- Isolation Voltage: 1500 Vrms (min)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A
  File No.E67349
- VDE-approved: EN 60747-5-5 (Note 1)

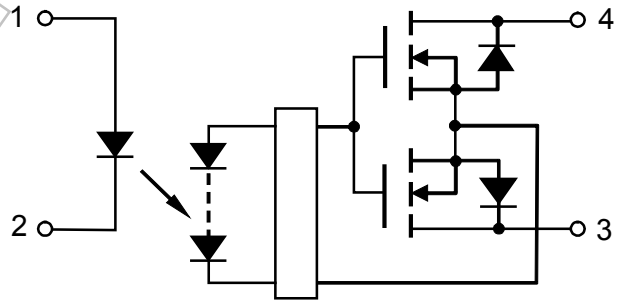
Note 1: When a VDE approved type is needed,
please designate the **Option(V4)**.

Pin Configuration (Top View)



1 : ANODE
2 : CATHODE
3 : DRAIN
4 : DRAIN

Schematic



Start of commercial production
2004-10

Absolute Maximum Ratings (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
LED	Forward Current	I_F	50	mA
	Forward Current Derating (Ta ≥ 25°C)	ΔI_F/°C	-0.5	mA/°C
	Reverse Voltage	V_R	5	V
	Diode Power Dissipation	P_D	50	mW
	Diode Power Dissipation Derating (Ta ≥25°C)	ΔP_D /°C	-0.5	mW/°C
	Junction Temperature	T_j	125	°C
DETECTOR	Off-State Output Terminal Voltage	V_{OFF}	80	V
	On-State Current	I_{ON}	40	mA
	On-State Current Derating (Ta ≥ 25°C)	ΔI_{ON}/°C	-0.4	mA/°C
	Output Power Dissipation	P_O	40	mW
	Output Power Dissipation Derating (Ta ≥ 25°C)	ΔP_O / °C	-0.4	mW / °C
	Junction Temperature	T_j	125	°C
Storage Temperature Range		T_{stg}	-40 to 125	°C
Operating Temperature Range		T_{opr}	-20 to 85	°C
Lead Soldering Temperature (10 s)		T_{sol}	260	°C
Isolation Voltage (AC, 60 s, R.H. ≤ (60 %) (Note 1)		BV_S	1500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two-terminal device: Pins 1 and 2 shorted together, and pins 3 and 4 shorted together.

Caution

This device is sensitive to electrostatic discharge. When using this device, please ensure that all tools and equipment are earthed.

Recommended Operating Conditions

CHARACTERISTIC	SYMBOL	MIN	TYP.	MAX	UNIT
Supply Voltage	V_{DD}	—	—	64	V
Forward Current	I_F	5	—	30	mA
On-State Current	I_{ON}	—	—	40	mA
Operating Temperature	T_{opr}	25	—	60	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
LED	Forward Voltage	V_F	I_F = 10 mA	1.0	1.15	1.3	V
	Reverse Current	I_R	V_R = 5 V	—	—	10	μA
	Capacitance between terminals	C_T	V_F = 0 V, f = 1 MHz	—	15	—	pF
DETECTOR	Off-State Current	I_{OFF}	V_{OFF} = 80 V, Ta = 60 °C	—	—	1	nA
	Capacitance between terminals	C_{OFF}	V = 0 V, f = 100 MHz, t < 1 s	—	2.5	3.5	pF

Coupled Electrical Characteristics (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Trigger LED Current	I_{FT}	I_{ON} = 40 mA	—	—	3	mA
Return LED Current	I_{FC}	I_{OFF} = 10 μA	0.1	—	—	mA
On-State Resistance	R_{ON}	I_{ON} = 40 mA, I_F = 5 mA, t < 1 s	—	16	25	Ω

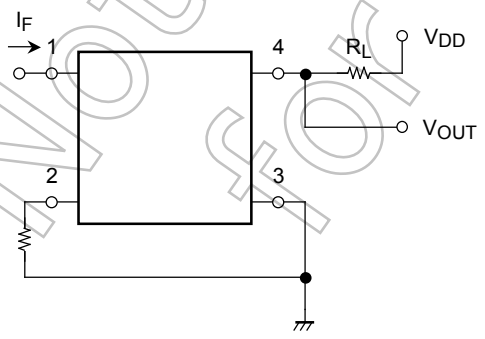
Isolation Characteristics (Ta = 25°C)

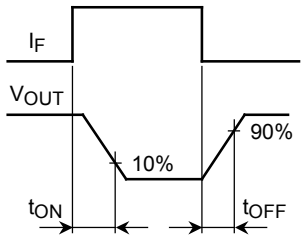
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Capacitance Input to Output	C_S	V_S = 0 V, f = 1 MHz	—	0.8	—	pF
Isolation Resistance	R_S	V_S = 500 V, R.H. ≤ 60 %	5×10^{10}	10^{14}	—	Ω
Isolation Voltage	BV_S	AC, 60 s	1500	—	—	Vrms

Switching Characteristics (Ta = 25°C)

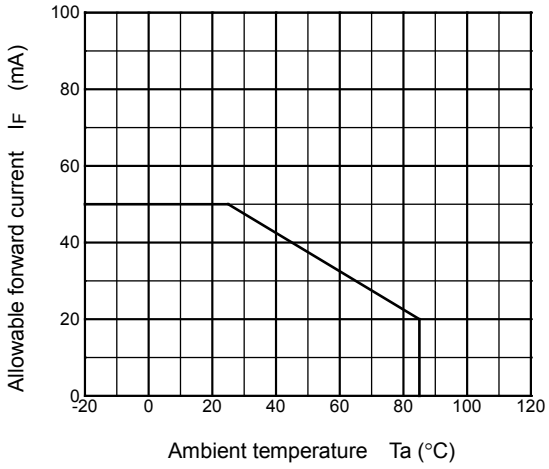
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Turn-on Time	t_{ON}	R_L = 200 Ω (Note 2) V_{DD} = 10 V, I_F = 5 mA	—	0.07	0.5	ms
Turn-off Time	t_{OFF}		—	0.07	0.5	

(Note 2) : Switching time test circuit

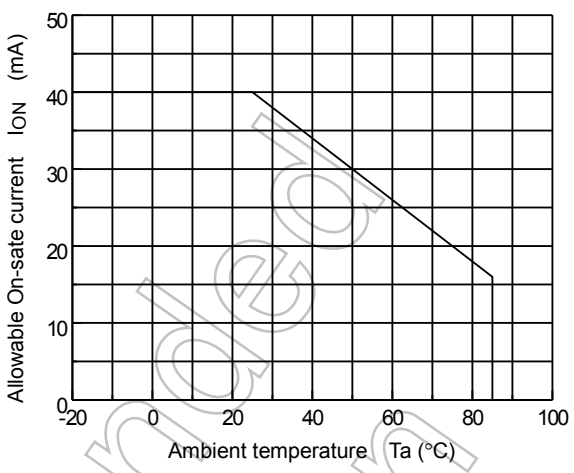




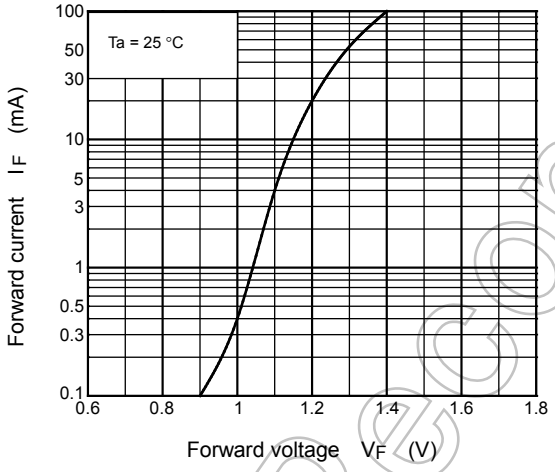
$I_F - Ta$



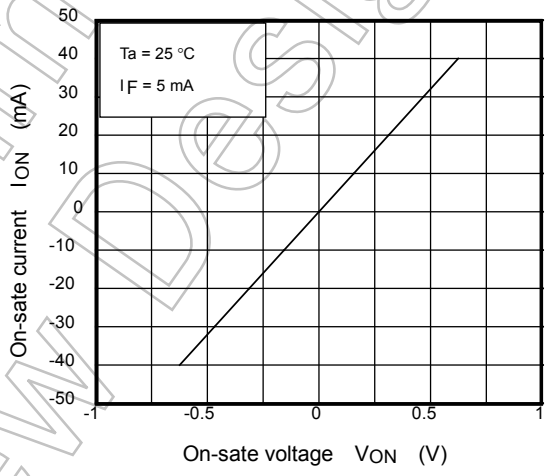
$I_{ON} - Ta$



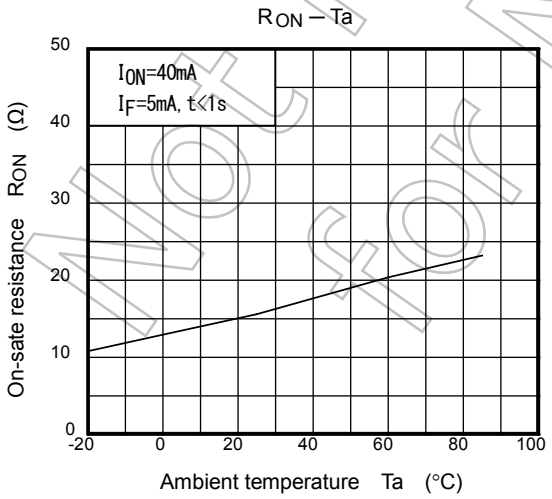
$I_F - V_F$



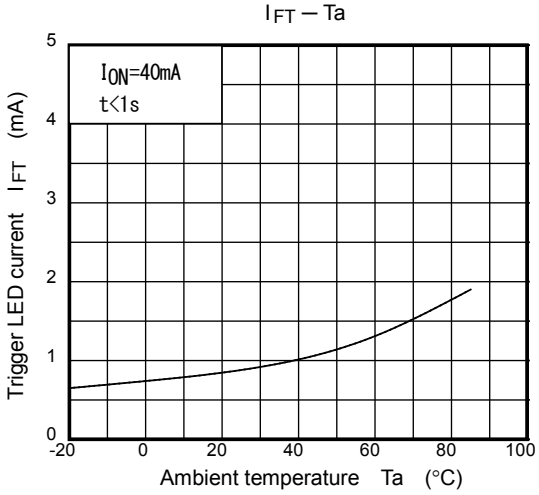
$I_{ON} - V_{ON}$



$R_{ON} - Ta$

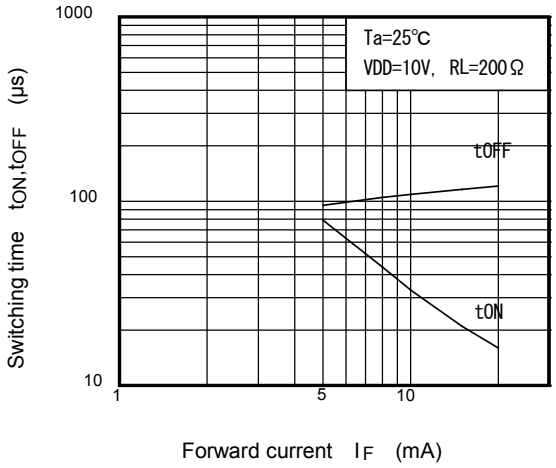


$I_{FT} - Ta$

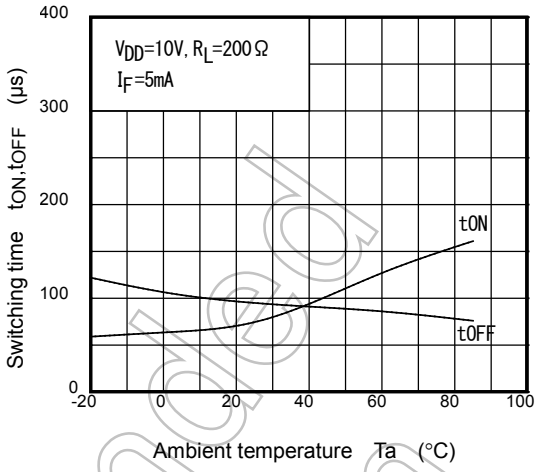


NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

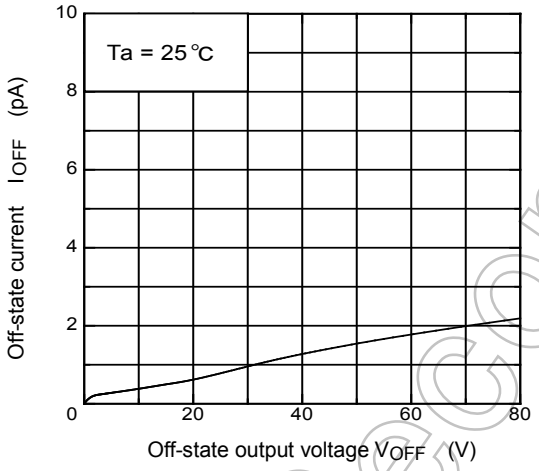
t_{ON}, t_{OFF} – I_F



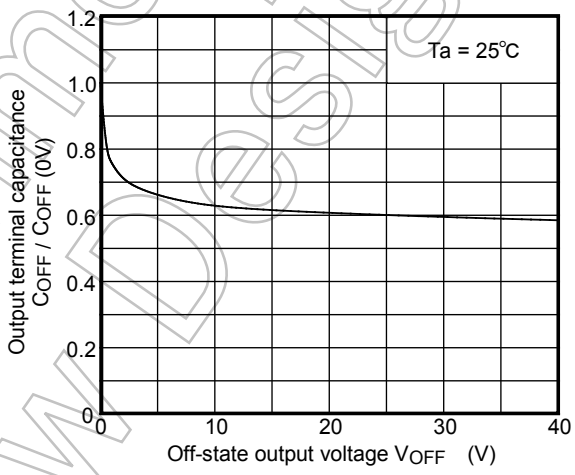
t_{ON}, t_{OFF} – Ta



I_{OFF} – V_{OFF}



Normalized C_{OFF} – V_{OFF}



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