Toshiba Intelligent Power Device Silicon Monolithic Power MOS Integrated Circuit

### **TPD1036F**

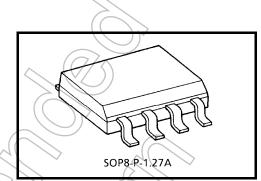
#### 2-IN-1 Low-Side Power Switch for Motor, Solenoid and Lamp Drivers

The TPD1036F is a 2-IN-1 low-side switch.

The output has a vertical MOSFET, and the input can be directly driven from CMOS or TTL logic (e.g., an MPU). The IC provides intelligent protection functions.

#### **Features**

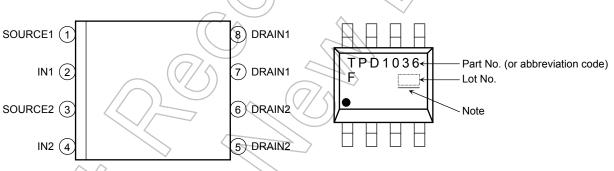
- Two built-in power IC chips with a structure that incorporates a control block and a vertical power MOSFET on each chip.
- Can be directly driven from a microprocessor, a CMOS logic IC, etc.
- Overvoltage (active clamp), overtemperature (thermal shutdown), and overcurrent (current limiter) protections are built in.
- Low ON-resistance:  $RDS(ON) = 0.5 \Omega \text{ (max)} (@V_{IN} = 5 \text{ V}, I_D = 0.7 \text{ A}, T_{ch} = 25^{\circ}\text{C})$
- Low drain cut-off current:  $I_{DSS} = 10 \mu A \text{ (max) } (@V_{IN} = 0 \text{ V}, V_{DS} = 30 \text{ V}, T_{ch} = 25 \text{°C})$
- Low input current:  $I_{IN} = 300 \mu A \text{ (max)} (@V_{IN} = 5 \text{ V}, T_{ch} = -40 \text{ to } 110 \text{°C})$
- Housed in the 8-pin SOP package and supplied in embossed carrier tape.



Weight: 0.08 g (typ.)

## Pin Assignment (top view)





Note: A line under a Lot No. identifies the indication of product Labels

Not underlined: [[Pb]]/INCLUDES > MCV

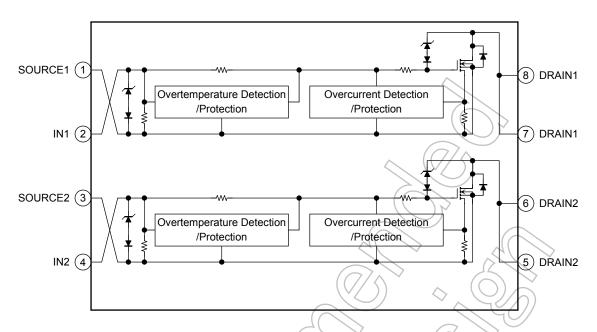
Underlined: [[G]]/RoH\$ COMPATIBLE or [[G]]/RoH\$ [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

This product has a MOS structure and is sensitive to electrostatic discharge.

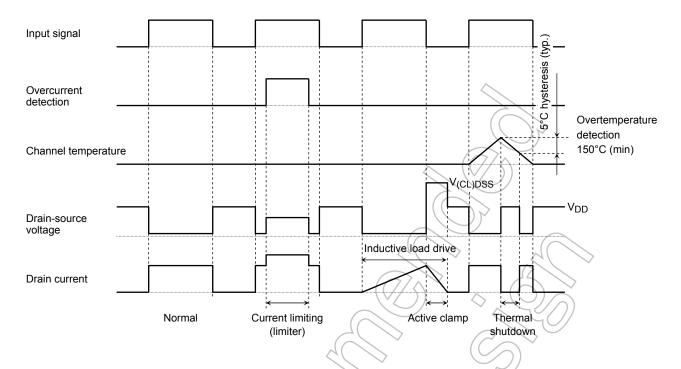
#### **Block Diagram**



#### **Pin Description**

Pin No.	Symbol	Pin Description
1	SOURCE1	Source pin 1.
2	IN1	Input pin 1.  This pin is connected to a pull-down resistor internally, so that even if the input is open-circuited, output never turns on inadvertently.
3	SOURCE2	Source pin 2.
4	IN2	Input pin 2.  This pin is connected to a pull-down resistor internally, so that even if the input is open-circuited, output never turns on inadvertently.
5, 6	DRAIN2	Drain pin 2.  Drain current is limited (by current limiter) if it exceeds 1 A (min) in order to protect the IC.
7, 8	DRAIN1	Drain pin 1.  Drain current is limited (by current limiter) if it exceeds 1 A (min) in order to protect the IC.

#### **Timing Chart**



#### **Truth Table**

V <sub>IN</sub>	V <sub>DS</sub>	Output State	Operating State		
L	Н	Off	Normal		
Н	L	On	)) Nomai		
L	Н	Off			
Н	Н	Current limiting (limiter)	Load short-circuited		
L	Н	((/off))	Overtemperature		
Н	Н//	Off	Overtealiperature		

#### Absolute Maximum Ratings (Ta = 25°C)

Characterist	Symbol	Rating	Unit	
Drain-source voltage	DC	$V_{DS}$	30	V
Drain current	ID	Internally limited	Α	
Input voltage	V <sub>IN</sub>	-0.3 to 6	V	
Power dissipation (t = 10 s) (Not	P <sub>D</sub>	2.0	W	
Single pulse active clamp capabil	E <sub>AS</sub>	23	mJ	
Active clamp current		I <sub>AR</sub>	1:5	/\h\
Repetitive active clamp capability	E <sub>AR</sub>	0.2	mJ	
Operating temperature		T <sub>opr</sub>	-40 to 110	°C °C
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature		T <sub>stg</sub>	-55 to 150	°C (

Note 1: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient (Note 2)	R <sub>th (ch-a)</sub>	62.5	°C/W

Note 2: Mount on glass epoxy boad [ $25.4 \times 25.4 \times 0.8$ mm] (with the two devices driving)(t =10 s)

Note 3: Single pulse active clamp capability test condition  $V_{DD}$  = 25 V,  $T_{Ch}$  = 25 °C (initial), L = 10 mH,  $I_{AR}$  = 1.5 A,  $R_G$  = 25 $\Omega$ 

Note 4: Repetitive rating: Pulse width limited by maximum channel temperature



#### **Electrical Characteristics**

Characteristics	Symbol	Test Circuit	Test Condition		Min	Тур.	Max	Unit
Drain-source clamp voltage	V <sub>(CL) DSS</sub>	_	T <sub>ch</sub> = -40 to 110°C	$V_{IN} = 0 V, I_D=1mA$	40	_	60	V
Input threshold voltage	V <sub>th</sub>	_	T <sub>ch</sub> = 25°C	V <sub>DS</sub> = 13 V, I <sub>D</sub> = 10mA	1.0	_	2.8	V
			T <sub>ch</sub> = -40 to 110°C		0.9	_	3.0	
Protective circuit operation input voltage range	V <sub>IN</sub> (opr)	_	T <sub>ch</sub> = 25°C	- (	3	_	6	· V
			T <sub>ch</sub> = -40 to 110°C	- (	3.5	<sup>7</sup> —	6	
Drain out off current		_	T <sub>ch</sub> = 25°C	V <sub>IN</sub> = 0 V, V <sub>DS</sub> = 30V	<u> </u>	_	10	μА
Drain cut-off current	I <sub>DSS</sub>		T <sub>ch</sub> = -40 to 110°C		))—	_	100	
	I <sub>IN (1)</sub>	_	$T_{ch} = -40 \text{ to } 110^{\circ}\text{C}$	V <sub>IN</sub> = 5 V, at normal operation			300	μА
Input current	I <sub>IN (2)</sub>	_	T <sub>ch</sub> = -40 to 110°C	V <sub>IN</sub> = 5 V, when overcurrent protective circuit is actuated	_		350	
Drain-source ON-resistance	R <sub>DS</sub> (ON)	_	$T_{ch} = 25^{\circ}C$ $T_{ch} = -40 \text{ to } 110^{\circ}C$	$V_{IN} = 5 \text{ V}, I_D = 0.7 \text{ A}$		0.3	0.5 0.75	Ω
Overtemperature detection	TS		- 0	V <sub>IN</sub> = 5 V	150	160		°C
Overeviewent detection	IS	2	T <sub>ch</sub> = 25°C	V <sub>IN</sub> = 5 V	1.5	2.5		- A
Overcurrent detection			T <sub>ch</sub> = -40 to 110°C		)1			
	ton	1	T <sub>ch</sub> = 25°C	V <sub>DD</sub> = 13 V, V <sub>IN</sub> = 0 V/5 V, J <sub>D</sub> = 0.7 A	/_	_	30	μ\$
Cuitohing times			T <sub>ch</sub> = -40 to 110°C		_	_	60	
Switching times	t <sub>OFF</sub>		T <sub>ch</sub> = 25°C		_	_	60	
			T <sub>ch</sub> = -40 to 110°C		_	_	90	
Drain-source diode forward voltage	V <sub>DSF</sub>		I <sub>ch</sub> = 25°C	V <sub>IN</sub> = 0 V, J <sub>F</sub> = 1.5 A	_	_	1.7	V

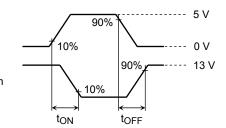
**Measured Waveforms** 

#### **Test Circuit 1**

#### Switching times measuring circuit

#### **Test Circuit**

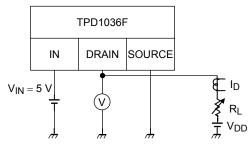
# TPD1036F IN DRAIN SOURCE ID = 0.7 A VDD = 13 V VDS Waveform

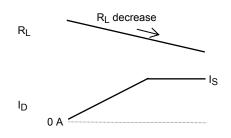


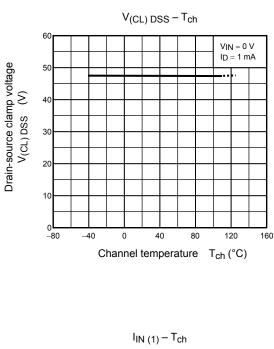
#### Test Circuit 2

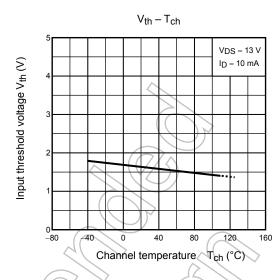
#### Overcurrent detection measuring circuit

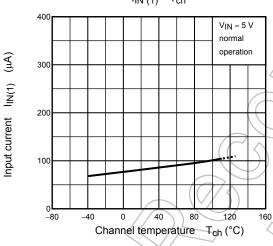
#### **Test Circuit**

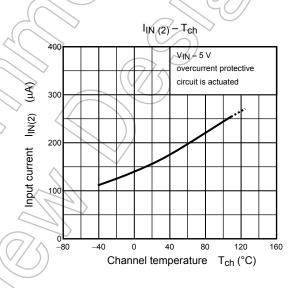


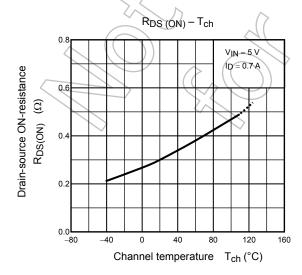


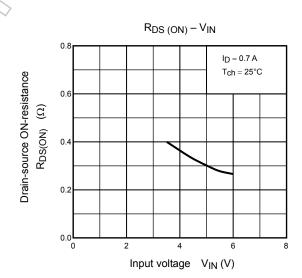




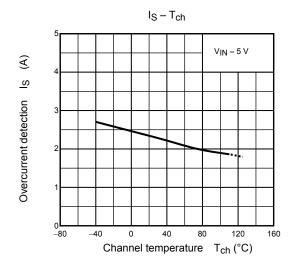


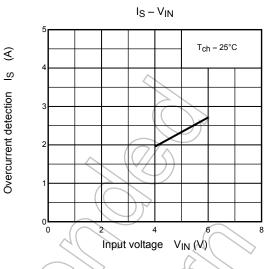


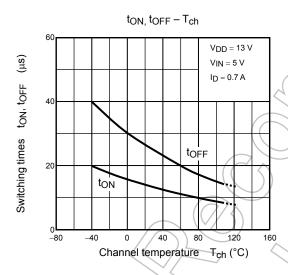


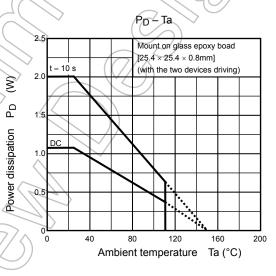


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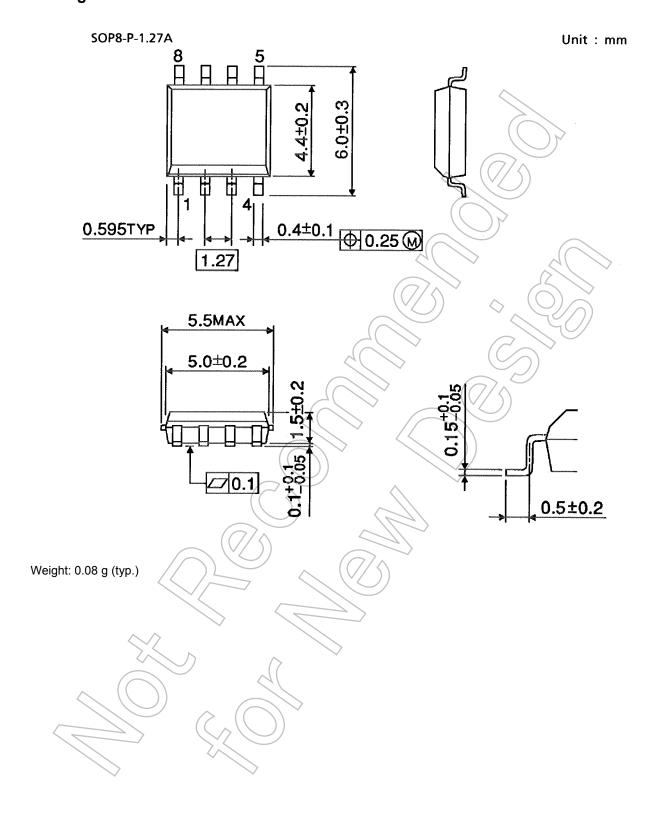








#### **Package Dimensions**



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