TOSHIBA Field Effect Transistor Silicon P-Channel MOS Type (L^2 - π -MOSV)

2SJ377

Relay Drive, DC/DC Converter and Motor Drive Applications

• 4 V gate drive

• Low drain-source ON-resistance : $R_{DS(ON)} = 0.16 \Omega \text{ (typ.)}$

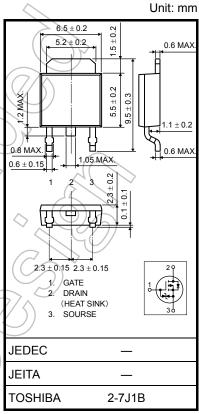
• High forward transfer admittance : $|Y_{fs}| = 4.0 \text{ S (typ.)}$

• Low leakage current : $I_{DSS} = -100 \,\mu\text{A} \,(\text{max}) \,(\text{V}_{DS} = -60 \,\text{V})$

• Enhancement mode : $V_{th} = -0.8$ to -2.0 V ($V_{DS} = -10$ V, $I_D = -1$ mA)

Absolute Maximum Ratings (Ta = 25°C)

Character	istic	Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	-60	< (
Drain-gate voltage (Ro	_{SS} = 20 kΩ)	V_{DGR}	-60	V
Gate-source voltage		V _{GSS}	±20	> v
Drain current	DC (Note 1)	ID	-5	Α
	Pulse (Note 1)	I _{DP}	-20	A
Drain power dissipatio	n (Tc = 25°C)	P _D	20	/W
Single-pulse avalanch	e energy (Note 2)	EAS	273	m Z
Avalanche current		lar.	-5	Α
Repetitive avalanche	energy (Note 3)	(E _{AR})	2	Lm
Channel temperature		T _{ch}	150	°C
Storage temperature r	ange	// T _{stg}	−55 to 150	°C



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	6.25	°C / W
Thermal resistance, channel to ambient	R _{th (ch-a)}	125	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = -25 V, T_{ch} = 25°C (initial), L = 14.84 mH, R_G = 25 Ω , I_{AR} = -5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

Electrical Characteristics (Ta = 25°C)

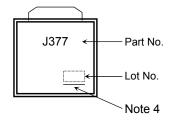
Charac	cteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cutoff curr	ent	I _{DSS}	V _{DS} = -60 V, V _{GS} = 0 V	_	_	-100	μΑ
Drain-source bre	eakdown voltage	V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-60	_	_	V
Gate threshold v	oltage	V_{th}	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$	-0.8	/	-2.0	V
Drain-source ON-resistance		R _{DS (ON)}	$V_{GS} = -4 \text{ V}, I_D = -2.5 \text{ A}$	1	0.24	0.28	Ω
Diam-source Orriesistance	$V_{GS} = -10 \text{ V}, I_D = -2.5 \text{ A}$		75	0.16	0.19		
Forward transfer	admittance	Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -2.5 \text{ A}$	2.0	4.0	-	S
Input capacitano	e	C _{iss}		_	630		
Reverse transfer	r capacitance	C _{rss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz	_	95	_	pF
Output capacitance		Coss		_	290	_	
Switching time Fa	Rise time	t _r	$\begin{array}{c c} V_{GS} \stackrel{0V}{\longrightarrow} & I_{D} = -2.5 A \\ \stackrel{\sim}{\longrightarrow} & V_{OUT} \\ \stackrel{\sim}{\longrightarrow} & R_{L} = \\ 12 \Omega \end{array}$	- (25	> I →	
	Turn-on time	t _{on}			45) _	
	Fall time	t _f	C 12Ω V _{DD} = −30V		55	-	113
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\mathbf{W}} = 10 \mu \text{s}$) –	200		
Total gate charg plus gate-drain)	e (Gate-source	Qg		_	22	_	
Gate-source cha	arge	Qgs	$V_{DD} \approx -48 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -5 \text{ A}$	_	16	_	nC
Gate-drain ("Mill	er") charge	Qgd			6	_	

Source-Drain Ratings and Characteristics (Ta = 25°C

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	-5	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	-20	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = -5 A, V _{GS} = 0 V	_	_	1.7	V
Reverse recovery time	t _{rr}	I _{DR} = -5 A, V _{GS} = 0 V	_	80	_	ns
Reverse recovery charge	Qrr	dl _{DR} / dt = 50 A / μS	_	0.1	_	μC

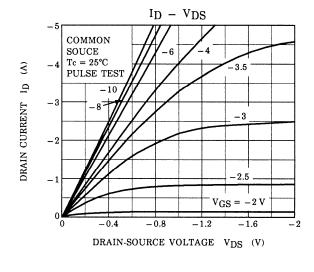
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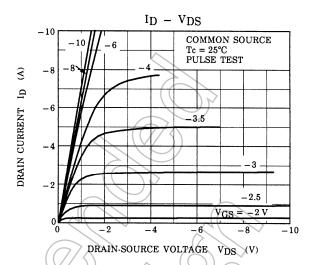
Marking

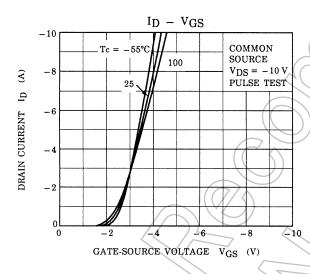


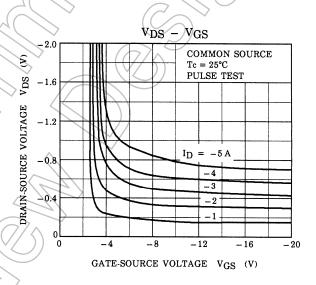
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

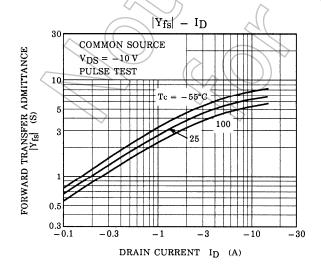
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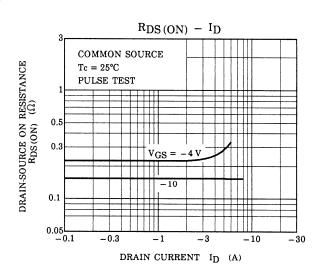


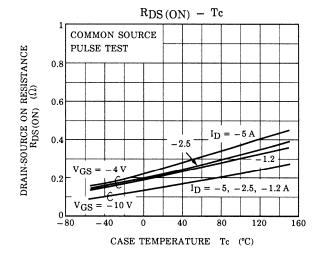


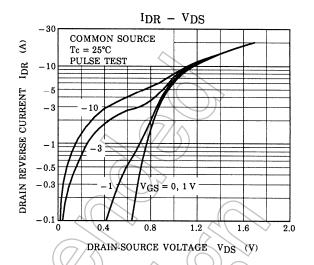


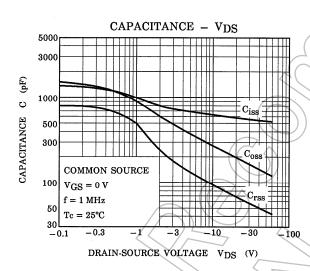


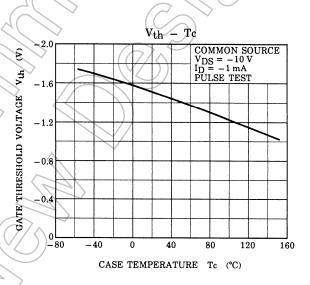


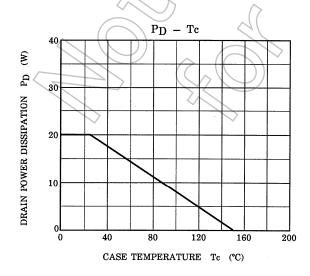


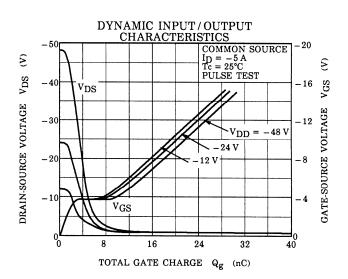




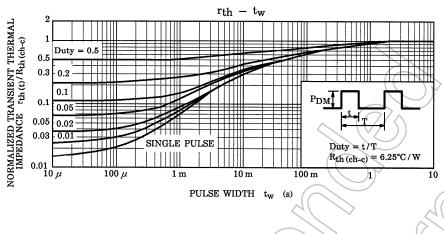


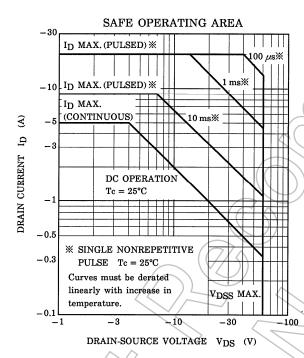


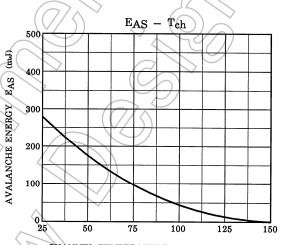




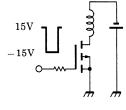
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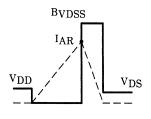






CHANNEL TEMPERATURE (INITIAL) Tch (°C)





TEST CIRCUIT

WAVE FORM

$$R_G = 25\Omega$$

 $V_{DD} = -25V$, L=14.84mH

$$E_{\text{AS}} \!=\! \frac{1}{2} \cdot L \cdot I^2 \cdot (\frac{BVDSS}{BVDSS - VDD})$$

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