

Press Pack IEGT Silicon N-Channel IEGT

ST2000GXH32

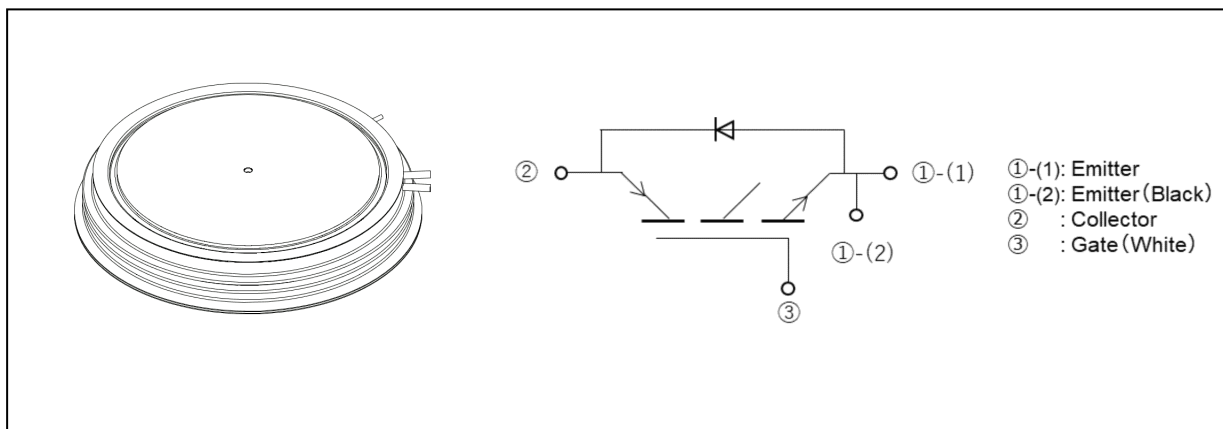
1. Applications

- Electric power transmission and distribution
- Motor Controllers
- High-Power Switching

2. Features

- (1) High reliability due to hermetic sealing structure.
- (2) Double side cooling type.

3. Packaging and Internal Circuit



Start of commercial production
2021-09

4. Absolute Maximum Ratings (Note) ($T_c = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Characteristics	Symbol	Note	Test Condition	Rating	Unit
Collector-emitter voltage	V_{CES}			4500	V
Gate-emitter voltage	V_{GES}			± 20	V
Collector current (DC)	I_C		$T_f = 101\text{ }^\circ\text{C}$	2000	A
Collector current (pulsed)	I_{CP}	(Note 1)		4000	A
Diode forward current (DC)	I_F		$T_f = 64\text{ }^\circ\text{C}$	2000	A
Diode forward current (pulsed)	I_{FP}	(Note 1)		4000	A
Non-repetitive peak forward surge current	I_{FSM}		10 ms half-sine wave, $V_R = 0\text{ V}$, $T_j = 150\text{ }^\circ\text{C}$	16	kA
Collector power dissipation	P_C	(Note 2)	Transistor part, $T_f = 25\text{ }^\circ\text{C}$	17123	W
Power dissipation	P_D	(Note 2)	Diode part, $T_f = 25\text{ }^\circ\text{C}$	9920	W
Junction temperature	T_j			-40 to 150	$^\circ\text{C}$
Operating junction temperature	$T_{j(opr)}$			-40 to 125	$^\circ\text{C}$
Storage temperature	T_{stg}			-40 to 125	$^\circ\text{C}$
Mounting force	—			59 to 70	kN

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Pulse width and repetition rate should be such that junction temperature (T_j) does not exceed maximum T_j rating.

Note 2: Refer to the application notes.

5. Thermal Characteristics (Note)

Characteristics	Symbol	Note	Test Condition		Max	Unit
Thermal resistance (junction-to-fin)	$R_{th(j-f)}$	(Note 3)	Transistor part	Double side	7.3	K/kW
Thermal resistance (junction-to-fin)	$R_{th(j-f)}$	(Note 3)	Diode part	Double side	12.6	K/kW

Note: Customers must also refer to and comply with the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and the instructions for the application with which the Product will be used with or for.

Note 3: Conductive thermal compound is added.

6. Electrical Characteristics

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Gate-emitter leakage current	I_{GES}	$V_{GE} = \pm 20 \text{ V}, V_{CE} = 0 \text{ V}, T_j = 25 \text{ }^\circ\text{C}$	—	—	± 100	nA
Collector-emitter cut-off current	I_{CES}	$V_{CE} = 4500 \text{ V}, V_{GE} = 0 \text{ V}, T_j = 25 \text{ }^\circ\text{C}$	—	—	0.2	mA
Gate-emitter cut-off voltage	$V_{GE(off)}$	$I_C = 2.0 \text{ A}, V_{CE} = 5 \text{ V}, T_j = 25 \text{ }^\circ\text{C}$	6.70	7.20	7.70	V
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C = 2000 \text{ A}, V_{GE} = 15 \text{ V}, T_j = 25 \text{ }^\circ\text{C}$	—	2.20	—	V
		$I_C = 2000 \text{ A}, V_{GE} = 15 \text{ V}, T_j = 150 \text{ }^\circ\text{C}$	—	2.70	3.30	
Input capacitance	C_{ies}	$V_{CE} = 10 \text{ V}, V_{GE} = 0 \text{ V}, f = 100 \text{ kHz}, T_j = 25 \text{ }^\circ\text{C}$	—	250	—	nF
Switching time (turn-on delay time)	$t_{d(on)}$	$V_{CC} = 2800 \text{ V}, I_C = 2000 \text{ A}, V_{GE} = \pm 15 \text{ V}, R_{G(on)} = 3.6 \text{ } \Omega,$	—	0.44	—	μs
Switching time (rise time)	t_r	$R_{G(off)} = 56 \text{ } \Omega, T_j = 150 \text{ }^\circ\text{C}$	—	0.37	—	μs
Switching time (turn-on time)	t_{on}	Diode side: ST2000GXH32	—	0.81	—	μs
Switching time (turn-off delay time)	$t_{d(off)}$	$T_j = 150 \text{ }^\circ\text{C}$	—	10.70	—	μs
Switching time (fall time)	t_f	(Inductive load, $L_s \approx 300 \text{ nH}$)	—	2.45	—	μs
Switching time (turn-off time)	t_{off}	See Fig. 6.1 and Fig. 6.2	—	13.15	—	μs
Forward voltage	V_F	$I_F = 2000 \text{ A}, T_j = 25 \text{ }^\circ\text{C}$	—	2.70	—	V
		$I_F = 2000 \text{ A}, T_j = 150 \text{ }^\circ\text{C}$	—	2.80	3.40	
Reverse recovery current	I_{rr}	$V_{CC} = 2800 \text{ V}, I_F = 2000 \text{ A}, V_{GE} = -15 \text{ V}, T_j = 150 \text{ }^\circ\text{C}$ Drive side: ST2000GXH32	—	2140	—	A
Reverse recovery time	t_{rr}	$di/dt \approx 4100 \text{ A}/\mu\text{s}, T_j = 150 \text{ }^\circ\text{C}$ (Inductive load, $L_s \approx 300 \text{ nH}$) See Fig. 6.3 and Fig. 6.4	—	1.05	—	μs
Turn-on switching loss	E_{on}	$V_{CC} = 2800 \text{ V}, I_C = 2000 \text{ A}, V_{GE} = \pm 15 \text{ V}, R_{G(on)} = 3.6 \text{ } \Omega,$ $R_{G(off)} = 56 \text{ } \Omega, T_j = 150 \text{ }^\circ\text{C}$ Diode side: ST2000GXH32	—	8.4	—	J
Turn-off switching loss	E_{off}	$T_j = 150 \text{ }^\circ\text{C}$ (Inductive load, $L_s \approx 300 \text{ nH}$) See Fig. 6.1 and Fig. 6.2	—	13.2	—	J
Reverse recovery loss	E_{rr}	$V_{CC} = 2800 \text{ V}, I_F = 2000 \text{ A}, V_{GE} = -15 \text{ V}, T_j = 150 \text{ }^\circ\text{C}$ Drive side: ST2000GXH32 $di/dt \approx 4100 \text{ A}/\mu\text{s}, T_j = 150 \text{ }^\circ\text{C}$ (Inductive load, $L_s \approx 300 \text{ nH}$) See Fig. 6.3 and Fig. 6.4	—	3.5	—	J
Short-circuit pulse width (Note 4)	t_{psc}	$V_{CC} = 3200 \text{ V}, V_{GE} = \pm 15 \text{ V}, R_{G(on)} = 3.6 \text{ } \Omega, R_{G(off)} = 56 \text{ } \Omega,$ $L_s \approx 200 \text{ nH}, T_j = 150 \text{ }^\circ\text{C}$	—	—	10	μs

Note 4: Measurement condition depends on the measurement equipment.

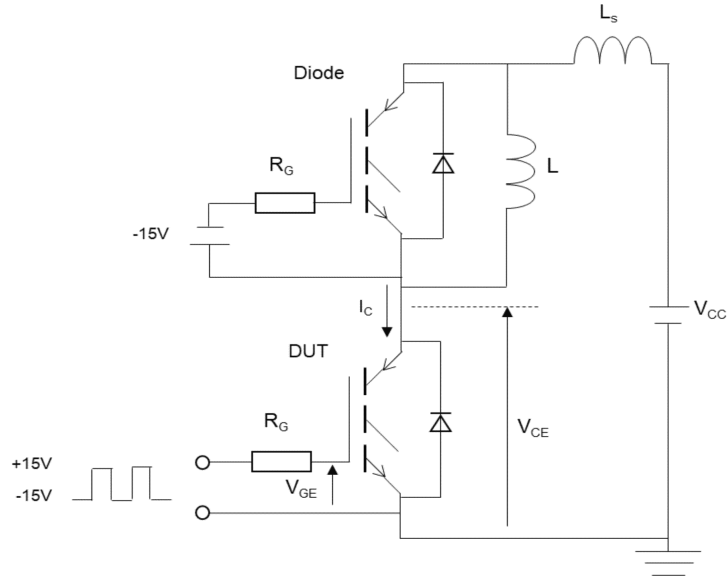


Fig. 6.1 Test Circuit (Transistor part)

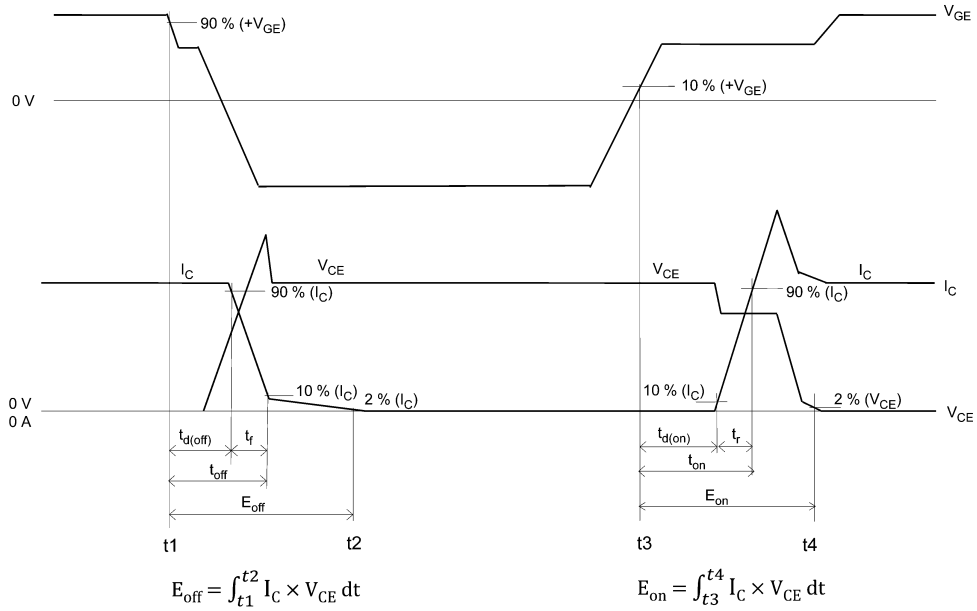


Fig. 6.2 Timing Chart (Transistor part)

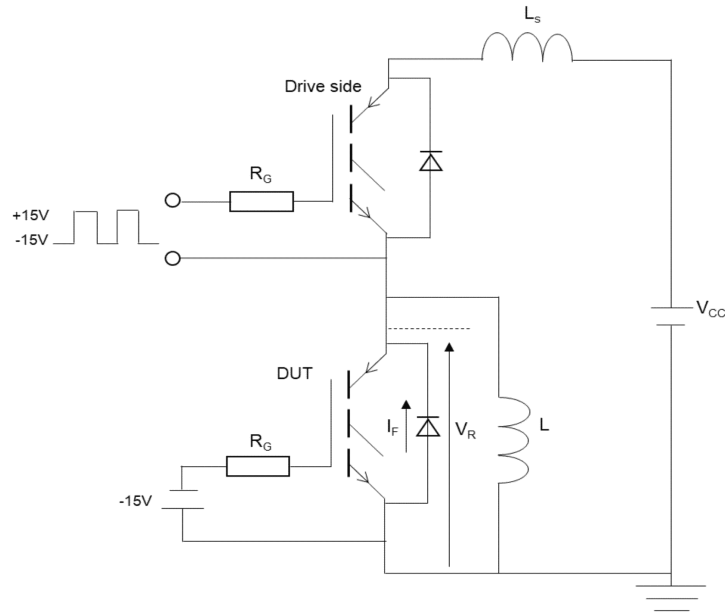


Fig. 6.3 Test Circuit (Diode part)

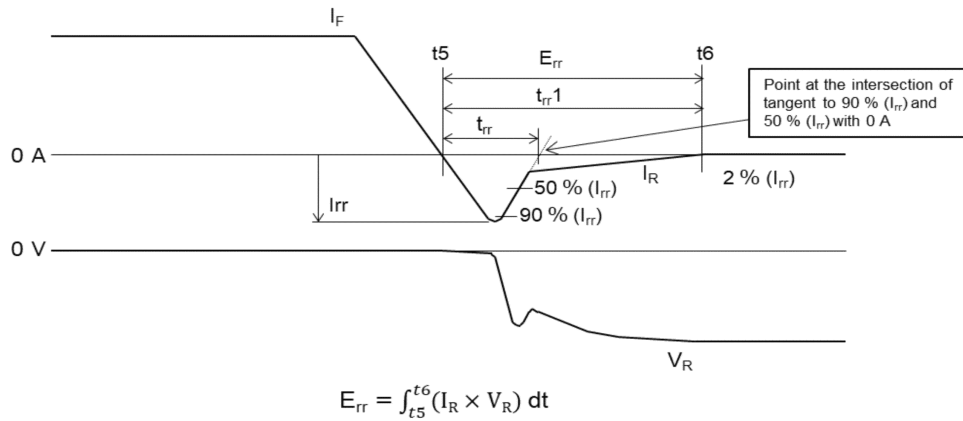


Fig. 6.4 Timing Chart (Diode part)

7. Characteristics Curves (Note)

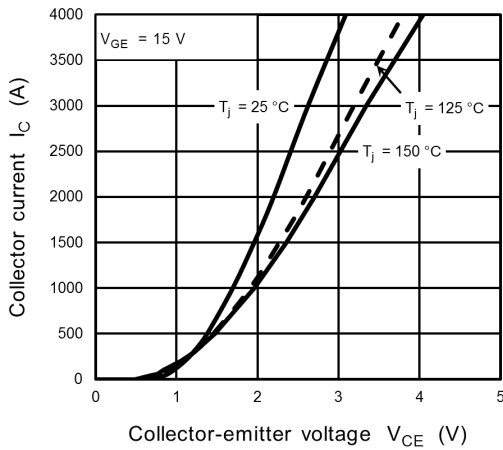


Fig. 7.1 $I_C - V_{CE}$

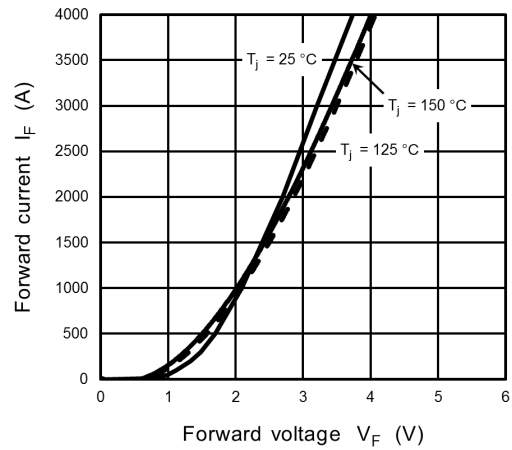


Fig. 7.2 $I_F - V_F$

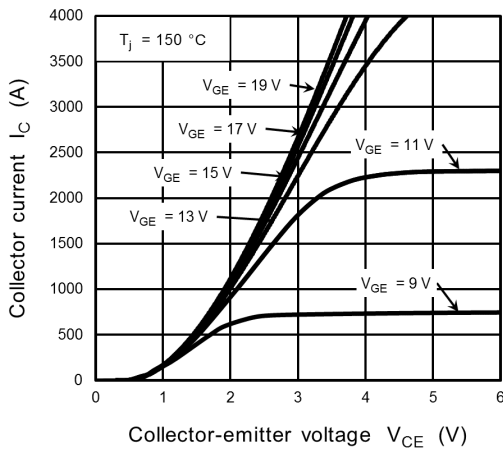


Fig. 7.3 $I_C - V_{CE}$

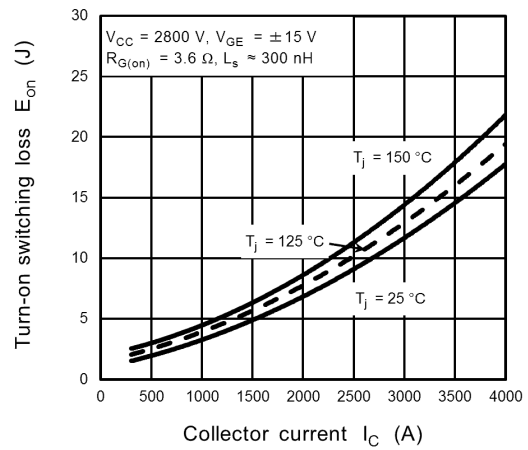


Fig. 7.4 $E_{on} - I_C$

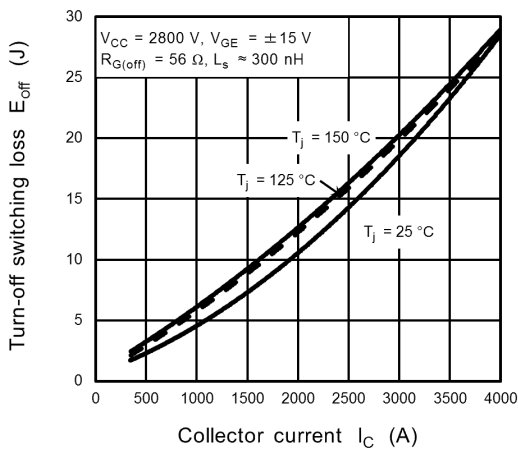


Fig. 7.5 $E_{off} - I_C$

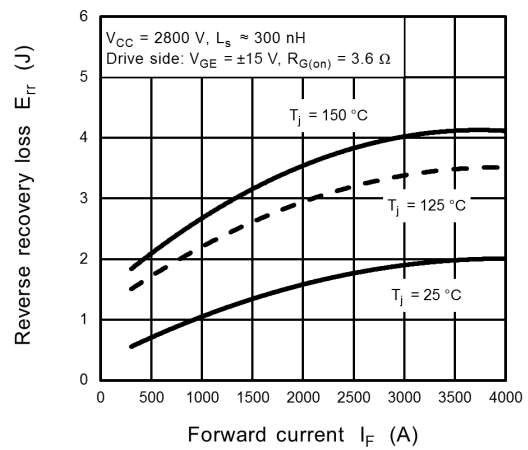


Fig. 7.6 $E_{rr} - I_F$

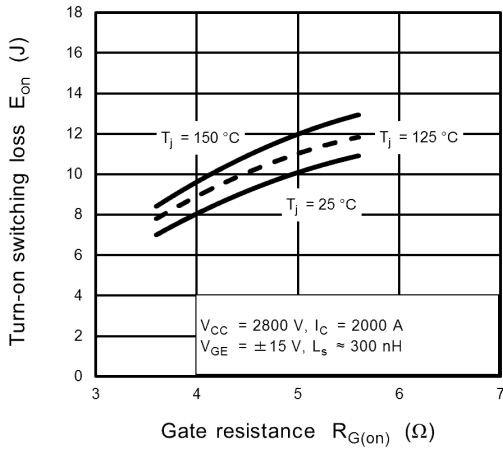


Fig. 7.7 E_{on} - $R_{G(on)}$

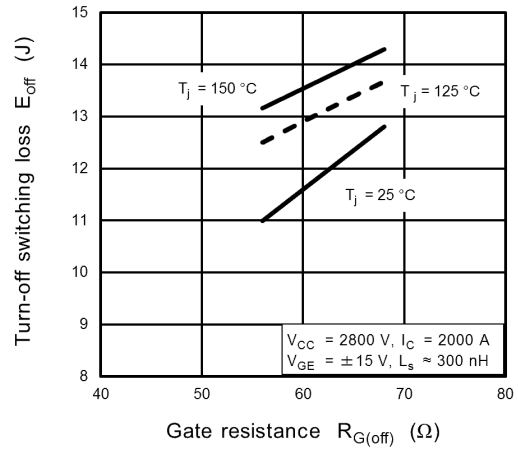


Fig. 7.8 E_{off} - $R_{G(off)}$

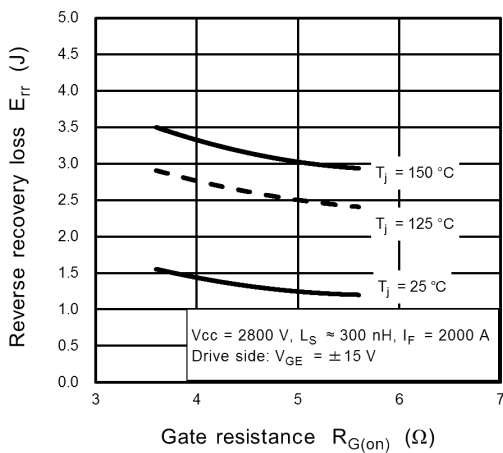


Fig. 7.9 E_{rr} - $R_{G(on)}$

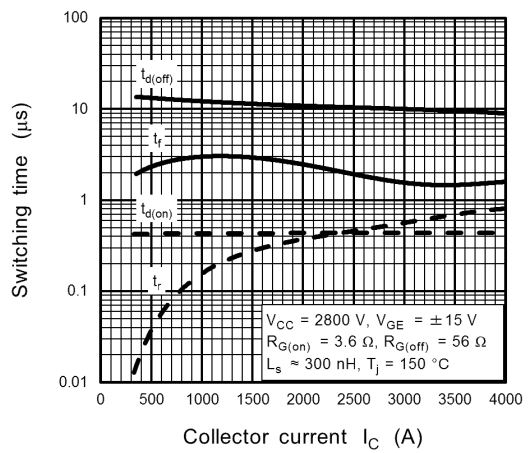


Fig. 7.10 Switching time - I_C

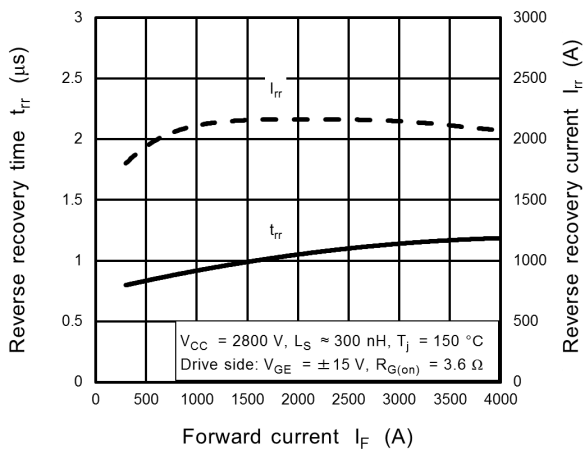


Fig. 7.11 t_{rr}, I_{rr} - I_F

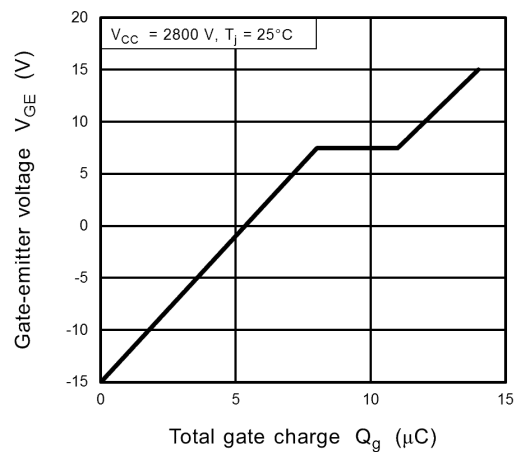


Fig. 7.12 V_{GE} - Q_g

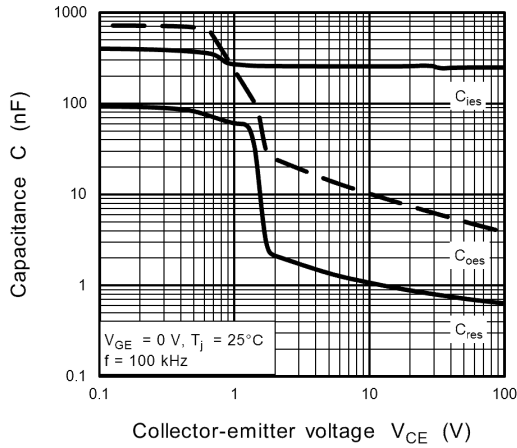


Fig. 7.13 Capacitance - V_{CE}

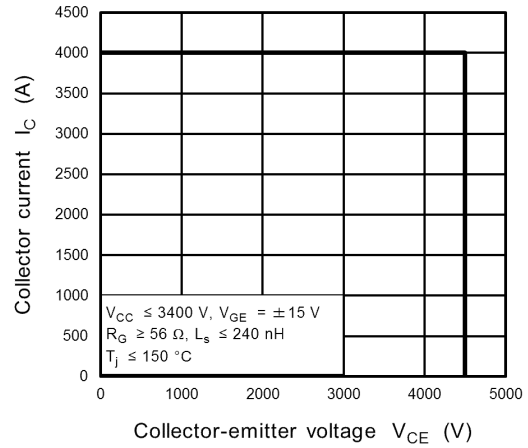


Fig. 7.14 RBSOA (Guaranteed value)

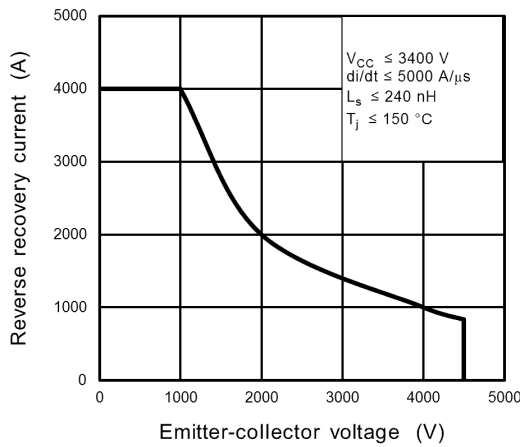


Fig. 7.15 RRSOA (Guaranteed value)

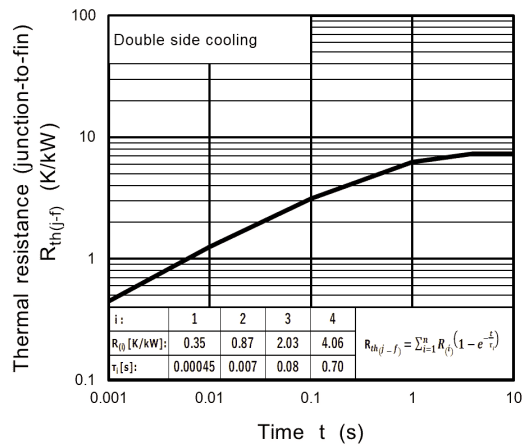


Fig. 7.16 $R_{th(j-f)} - t$ (Transistor part) (Guaranteed value)

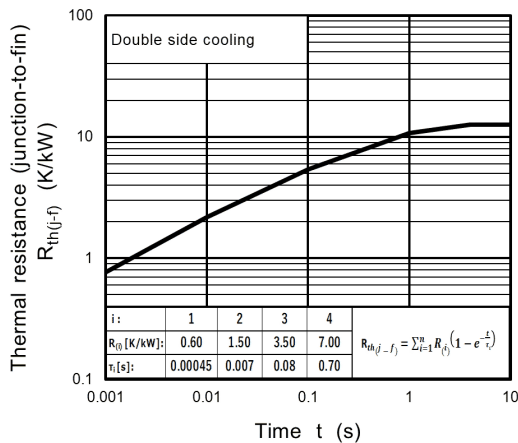
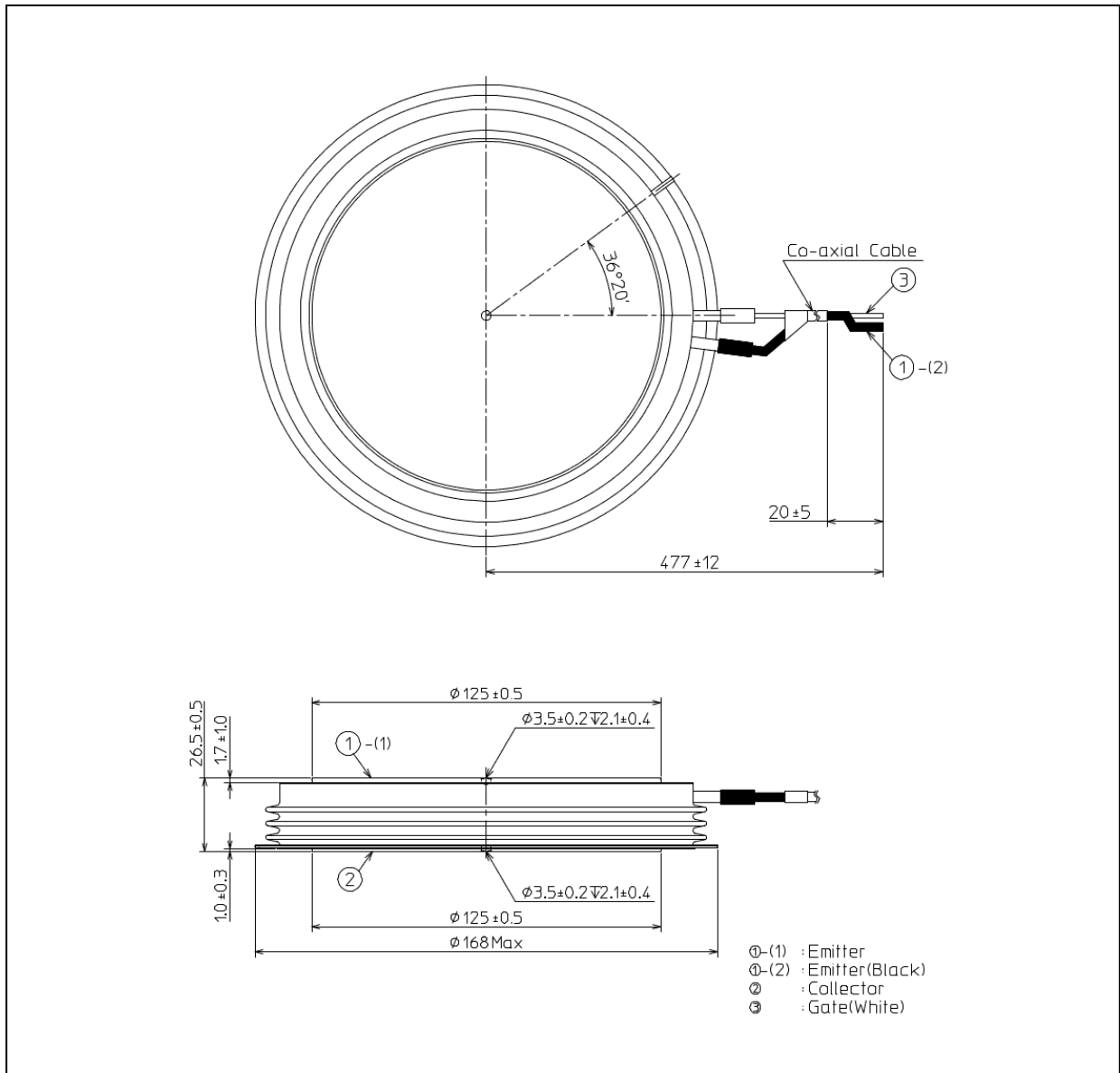


Fig. 7.17 $R_{th(j-f)} - t$ (Diode part) (Guaranteed value)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Package Dimensions

Unit: mm



Weight: 2700 g (typ.)

Package Name(s)
TOSHIBA: 2-168A2S

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