

TLP3330

1. Applications

- High-Speed Memory Testers
- High-Speed Logic IC Testers
- Radio-Frequency Measuring Instruments
- ATE (Automatic Test Equipment)

2. General

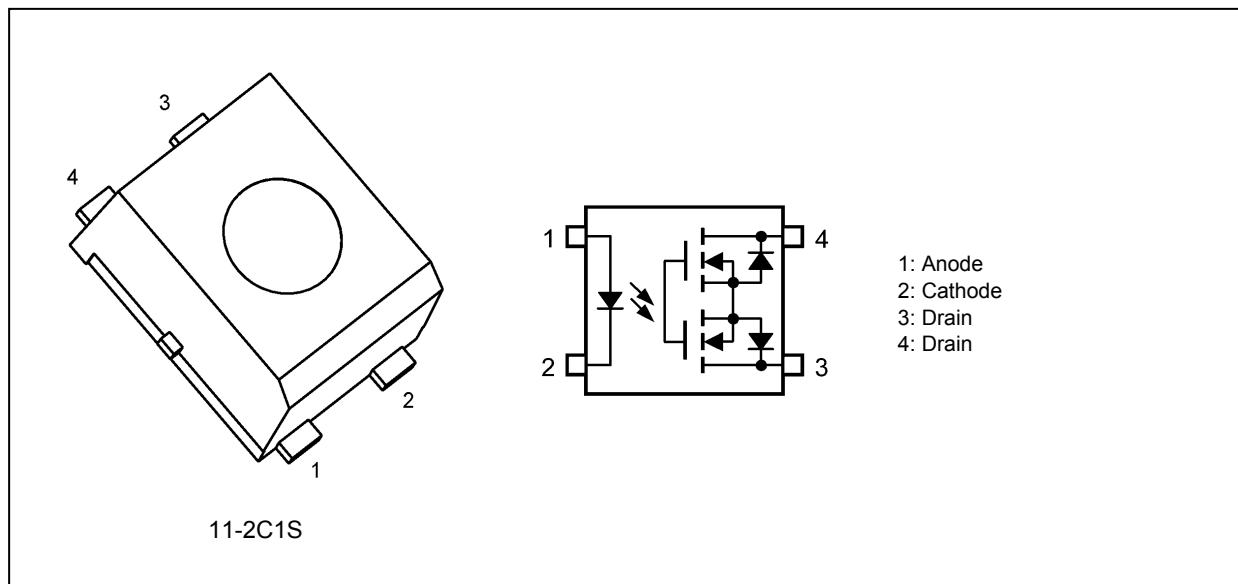
The TLP3330 is a photorelay in a 4-pin USOP that consists of a photo MOSFET optically coupled with an infrared light emitting diode. Its features include low Off-state current and low output pin capacitance, enabling it to be used for high-frequency measuring instrument applications.

3. Features

- (1) Normally open (1-Form-A)
- (2) OFF-state output terminal voltage: 20 V (min)
- (3) Trigger LED current: 3 mA (max)
- (4) ON-state current: 160 mA (max)
- (5) ON-state resistance: 5 Ω (typ.), 8 Ω (max)
- (6) OFF-state Capacitance: 1.0 pF (typ.), 2.5 pF (max)
- (7) Isolation voltage: 1000 Vrms (min)
- (8) Safety standard

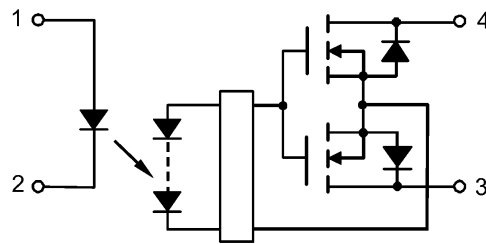
UL-approved: UL1577, File No.E67349

4. Packaging and Pin Assignment



Start of commercial production
2011-11

5. Internal Circuit



6. Absolute Maximum Ratings (Note) (Unless otherwise specified, $T_a = 25\text{ }^\circ\text{C}$)

	Characteristics	Symbol	Note	Rating	Unit
LED	Input forward current	I_F		50	mA
	Input forward current derating ($T_a \geq 25\text{ }^\circ\text{C}$)	$\Delta I_F/\Delta T_a$		-0.5	mA/ $^\circ\text{C}$
	Input reverse voltage	V_R		5	V
	Input power dissipation	P_D		50	mW
	Input power dissipation derating ($T_a \geq 25\text{ }^\circ\text{C}$)	$\Delta P_D/\Delta T_a$		-0.5	mW/ $^\circ\text{C}$
	Junction temperature	T_j		125	$^\circ\text{C}$
Detector	OFF-state output terminal voltage	V_{OFF}		20	V
	ON-state current	I_{ON}		160	mA
	ON-state current derating ($T_a \geq 25\text{ }^\circ\text{C}$)	$\Delta I_{ON}/\Delta T_a$		-1.6	mA/ $^\circ\text{C}$
	ON-state current (pulsed) ($t = 100\text{ ms}$, Duty = 1/10)	I_{ONP}		480	mA
	Output power dissipation	P_O		200	mW
	Output power dissipation derating ($T_a \geq 25\text{ }^\circ\text{C}$)	$\Delta P_O/\Delta T_a$		-2.0	mW/ $^\circ\text{C}$
	Junction temperature	T_j		125	$^\circ\text{C}$
Common	Storage temperature	T_{stg}		-40 to 125	$^\circ\text{C}$
	Operating temperature	T_{opr}		-40 to 85	
	Lead soldering temperature (10 s)	T_{sol}		260	
	Isolation voltage AC, 60 s, R.H. $\leq 60\%$	BV_S	(Note 1)	1000	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 1: This device is considered as a two-terminal device: Pins 1 and 2 are shorted together, and pins 3 and 4 are shorted together.

Caution: This device is sensitive to electrostatic discharge (ESD). Extreme ESD conditions should be guarded against by using proper antistatic precautions for the worktable, operator, solder iron, soldering equipment and so on.

7. Recommended Operating Conditions (Note)

Characteristics	Symbol	Note	Min	Typ.	Max	Unit
Supply voltage	V_{DD}		—	—	16	V
Input forward current	I_F		5	7.5	20	mA
ON-state current	I_{ON}		—	—	160	
Operating temperature	T_{opr}		-20	—	65	$^\circ\text{C}$

Note: The recommended operating conditions are given as a design guide necessary to obtain the intended performance of the device. Each parameter is an independent value. When creating a system design using this device, the electrical characteristics specified in this data sheet should also be considered.

8. Electrical Characteristics (Unless otherwise specified, $T_a = 25\text{ }^\circ\text{C}$)

	Characteristics	Symbol	Note	Test Condition	Min	Typ.	Max	Unit
LED	Input forward voltage	V_F		$I_F = 10\text{ mA}$	1.0	1.15	1.3	V
	Input reverse current	I_R		$V_R = 5\text{ V}$	—	—	10	μA
	Input capacitance	C_t		$V = 0\text{ V}, f = 1\text{ MHz}$	—	15	—	pF
Detector	OFF-state current	I_{OFF}		$V_{OFF} = 20\text{ V}$	—	—	200	pA
	Output capacitance	C_{OFF}		$V = 0\text{ V}, f = 100\text{ MHz}, t < 1\text{ s}$	—	1.0	2.5	pF

9. Coupled Electrical Characteristics (Unless otherwise specified, $T_a = 25\text{ }^\circ\text{C}$)

	Characteristics	Symbol	Note	Test Condition	Min	Typ.	Max	Unit
	Trigger LED current	I_{FT}		$I_{ON} = 100\text{ mA}$	—	0.6	3	mA
	Return LED current	I_{FC}		$I_{OFF} = 10\text{ }\mu\text{A}$	0.1	—	—	
	ON-state resistance	R_{ON}		$I_{ON} = 160\text{ mA}, I_F = 5\text{ mA}, t < 1\text{ s}$	—	5.0	8.0	Ω

10. Isolation Characteristics (Unless otherwise specified, $T_a = 25\text{ }^\circ\text{C}$)

	Characteristics	Symbol	Note	Test Condition	Min	Typ.	Max	Unit
	Total capacitance (input to output)	C_S	(Note 1)	$V_S = 0\text{ V}, f = 1\text{ MHz}$	—	0.4	—	pF
	Isolation resistance	R_S	(Note 1)	$V_S = 500\text{ V}, R.H. \leq 60\%$	5×10^{10}	10^{14}	—	Ω
	Isolation voltage	BV_S	(Note 1)	AC, 60 s	1000	—	—	Vrms

Note 1: This device is considered as a two-terminal device: Pins 1 and 2 are shorted together, and pins 3 and 4 are shorted together.

11. Switching Characteristics (Unless otherwise specified, $T_a = 25\text{ }^\circ\text{C}$)

	Characteristics	Symbol	Note	Test Condition	Min	Typ.	Max	Unit
	Turn-on time	t_{ON}		See Fig. 11.1. $R_L = 200\text{ }\Omega, V_{DD} = 10\text{ V}, I_F = 5\text{ mA}$	—	40	200	μs
	Turn-off time	t_{OFF}			—	180	300	

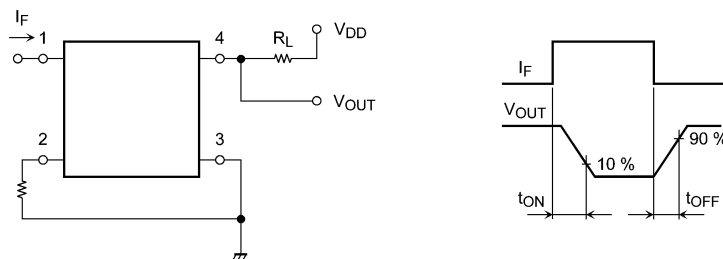


Fig. 11.1 Switching Time Test Circuit and Waveform

12. Characteristics Curves and Circuit Connections

12.1. Characteristics Curves (Note)

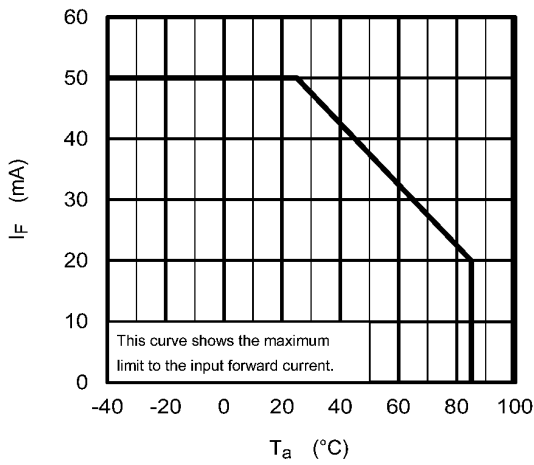


Fig. 12.1.1 $I_F - T_a$

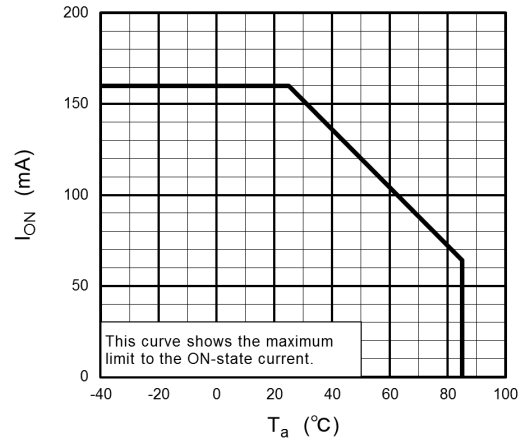


Fig. 12.1.2 $I_{ON} - T_a$

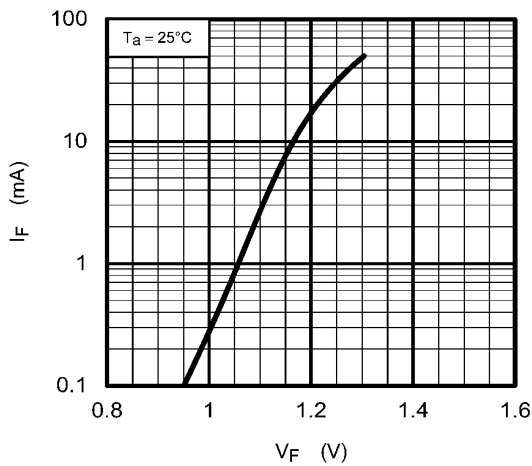


Fig. 12.1.3 $I_F - V_F$

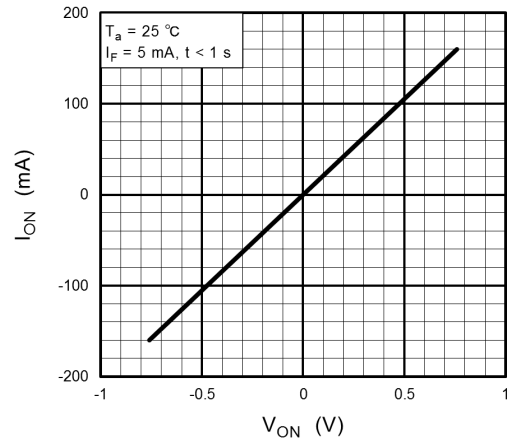


Fig. 12.1.4 $I_{ON} - V_{ON}$

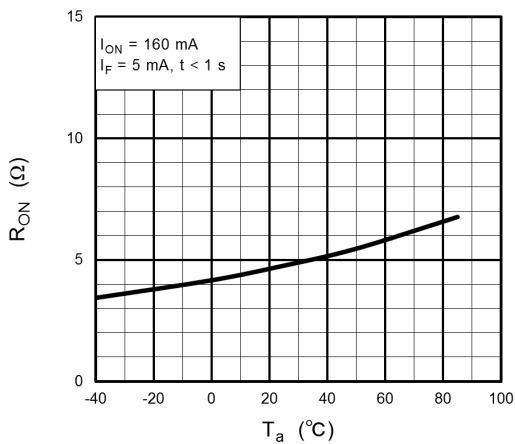


Fig. 12.1.5 $R_{ON} - T_a$

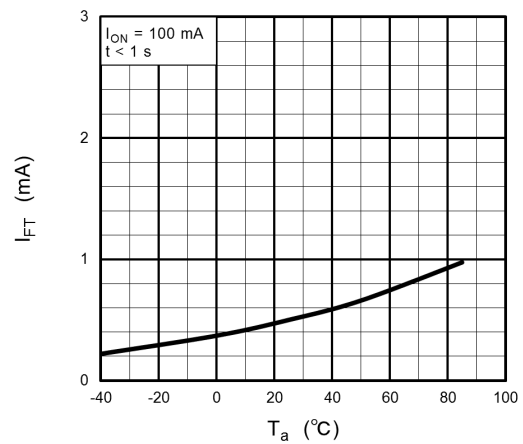


Fig. 12.1.6 $I_{FT} - T_a$

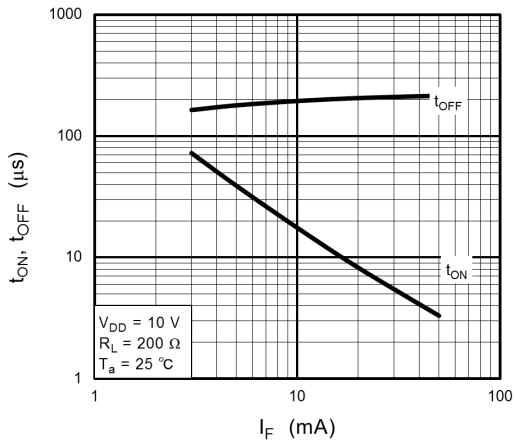


Fig. 12.1.7 $t_{ON}, t_{OFF} - I_F$

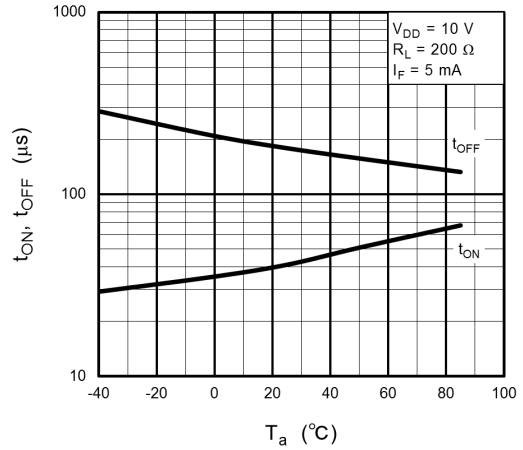


Fig. 12.1.8 $t_{ON}, t_{OFF} - T_a$

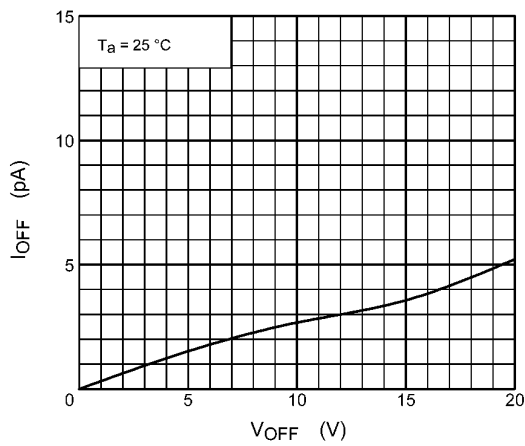


Fig. 12.1.9 $I_{OFF} - V_{OFF}$

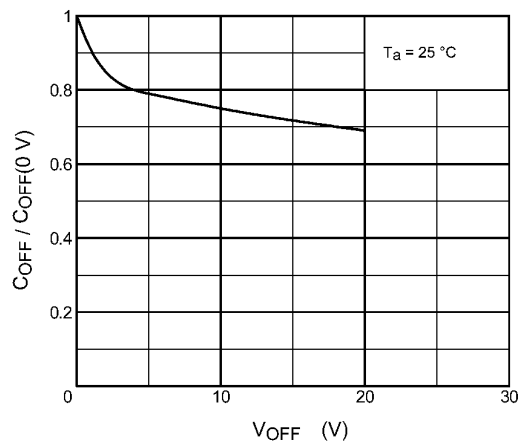
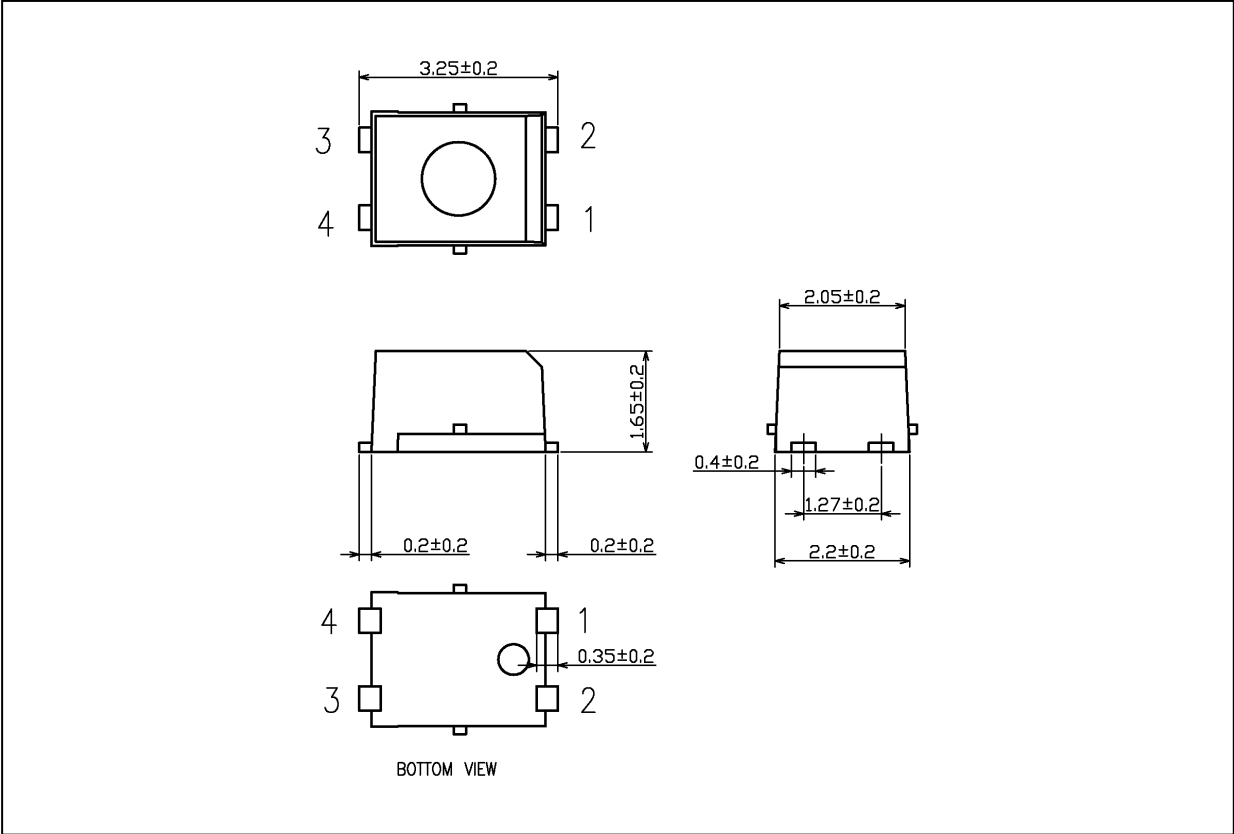


Fig. 12.1.10 $C_{OFF}/C_{OFF}(0 V) - V_{OFF}$

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Package Dimensions

Unit: mm



Weight: 0.03 g (typ.)

Package Name(s)
TOSHIBA: 11-2C1S

RESTRICTIONS ON PRODUCT USE

Toshiba Corporation and its subsidiaries and affiliates are collectively referred to as "TOSHIBA".

Hardware, software and systems described in this document are collectively referred to as "Product".

- TOSHIBA reserves the right to make changes to the information in this document and related Product without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA's written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product's quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before customers use the Product, create designs including the Product, or incorporate the Product into their own applications, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application with which the Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications.
TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.

- **PRODUCT IS NEITHER INTENDED NOR WARRANTED FOR USE IN EQUIPMENTS OR SYSTEMS THAT REQUIRE EXTRAORDINARILY HIGH LEVELS OF QUALITY AND/OR RELIABILITY, AND/OR A MALFUNCTION OR FAILURE OF WHICH MAY CAUSE LOSS OF HUMAN LIFE, BODILY INJURY, SERIOUS PROPERTY DAMAGE AND/OR SERIOUS PUBLIC IMPACT ("UNINTENDED USE").**

Except for specific applications as expressly stated in this document, Unintended Use includes, without limitation, equipment used in nuclear facilities, equipment used in the aerospace industry, lifesaving and/or life supporting medical equipment, equipment used for automobiles, trains, ships and other transportation, traffic signaling equipment, equipment used to control combustions or explosions, safety devices, elevators and escalators, and devices related to power plant.

IF YOU USE PRODUCT FOR UNINTENDED USE, TOSHIBA ASSUMES NO LIABILITY FOR PRODUCT.

For details, please contact your TOSHIBA sales representative or contact us via our website.

- Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- **ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE FOR PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (2) DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO SALE, USE OF PRODUCT, OR INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.**
- GaAs (Gallium Arsenide) is used in Product. GaAs is harmful to humans if consumed or absorbed, whether in the form of dust or vapor. Handle with care and do not break, cut, crush, grind, dissolve chemically or otherwise expose GaAs in Product.
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). Product and related software and technology may be controlled under the applicable export laws and regulations including, without limitation, the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export Administration Regulations. Export and re-export of Product or related software or technology are strictly prohibited except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. Please use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. **TOSHIBA ASSUMES NO LIABILITY FOR DAMAGES OR LOSSES OCCURRING AS A RESULT OF NONCOMPLIANCE WITH APPLICABLE LAWS AND REGULATIONS.**