Bipolar Transistors Silicon PNP Epitaxial Type

TTA011

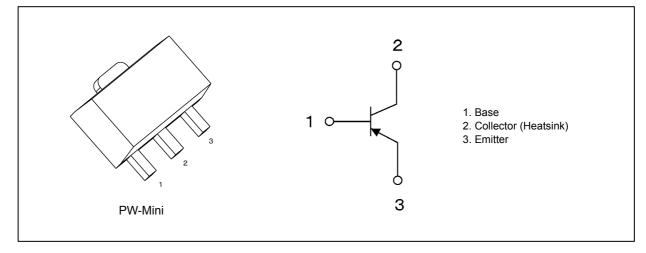
1. Applications

- High-Speed Switching
- DC-DC Converters

2. Features

- (1) High DC current gain: h_{FE} = 200 to 500 (V_{CE} = -2 V, I_C = -0.5 A)
- (2) Low collector-emitter saturation voltage: $V_{CE(sat)} = -0.27 \text{ V} (\text{max}) (I_C = -1.6 \text{ A}, I_B = -53 \text{ mA})$
- (3) High-speed switching: $t_f = 60$ ns (typ.) (I_C = -1.6 A)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) (Unless otherwise specified, $T_a = 25$ °C)

Characteristics			Rating	Unit	
Collector-base voltage		V _{CBO}	-50	V	
Collector-emitter voltage		V _{CEO}	-50	V	
Emitter-base voltage		V _{EBO}	-7	V	
Collector current (DC)	(Note 1)	Ι _C	-5	A	
Collector current (pulsed)	(Note 1)	I _{CP}	-10	A	
Base current		Ι _Β	-0.5	A	
Collector power dissipation	(Note 2)	P _C	1.0	W	
Collector power dissipation	(Note 3)	Pc	2.5	W	
Junction temperature		Tj	150	°C	
Storage temperature		T _{stg}	-55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note 1: Ensure that the junction temperature does not exceed 150 $^\circ \text{C}.$
- Note 2: Device mounted on a 25.4 mm \times 25.4 mm \times 1.6 mm FR-4 glass epoxy board (with a dissipating copper surface of 645 mm²)
- Note 3: Device mounted on a 40.0 mm \times 40.0 mm \times 0.8 mm ceramic board (with a dissipating copper surface of 1600 mm²)

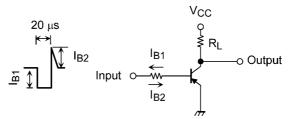
5. Electrical Characteristics

5.1. Static Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	V _{CB} = -50 V, I _E = 0 A	_	_	-100	nA
Emitter cut-off current	I _{EBO}	$V_{EB} = -7 V, I_{C} = 0 A$	_	—	-100	nA
Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C = -10 mA, I _B = 0 A	-50	_	_	V
DC current gain	h _{FE(1)}	V_{CE} = -2 V, I_{C} = -0.5 A	200	_	500	—
	h _{FE(2)}	V _{CE} = -2 V, I _C = -1.6 A	120	—	—	
Collector-emitter saturation voltage	V _{CE(sat)(1)}	I _C = -0.5 A, I _B = -17 mA	_	-0.06	-0.12	V
	V _{CE(sat)(2)}	I _C = -1.6 A, I _B = -0.16 A	_	-0.12	-0.21	
	V _{CE(sat)(3)}	I _C = -1.6 A, I _B = -53 mA	_	-0.16	-0.27	
Base-emitter saturation voltage	V _{BE(sat)}	I _C = -1.6 A, I _B = -53 mA		-0.89	-1.10	V

5.2. Dynamic Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector output capacitance	C _{ob}	V _{CB} = -10 V, I _E = 0 A, f = 1 MHz	_	40	—	pF
Switching time (rise time)		See Figure 5.2.1	_	55	—	ns
Switching time (storage time)		V _{CC} ≈ -24 V, R _L = 15 Ω, I _{B1} = -53 mA, I _{B2} = 53 mA	_	300	_	
Switching time (fall time)	t _f	$_{B1}^{B1} = -33$ m, $_{B2}^{B2} = 33$ mA	_	60	_	



Duty cycle $\leq 1\%$



6. Marking (Note)

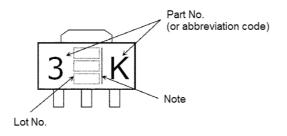


Fig. 6.1 Marking

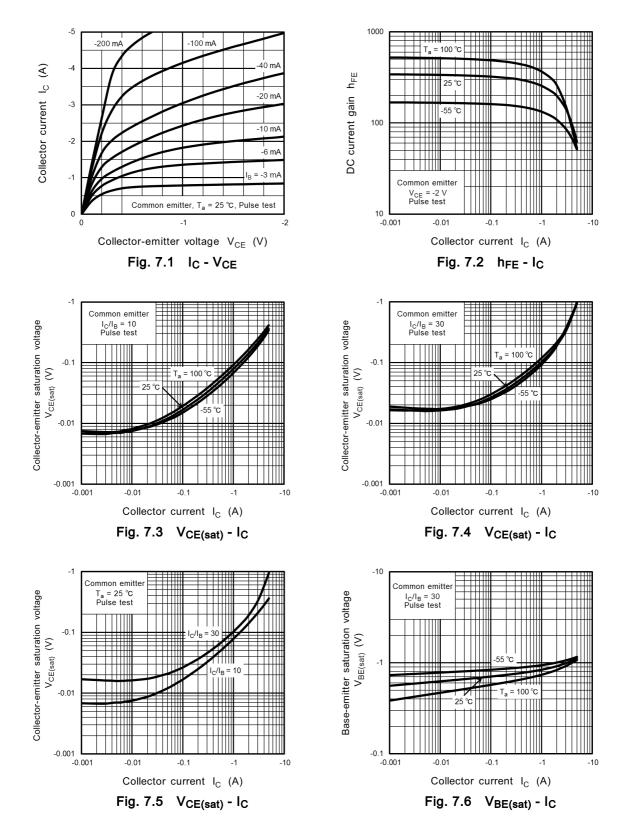
Note: A line beside a Lot No. identifies the indication of product Labels.

[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

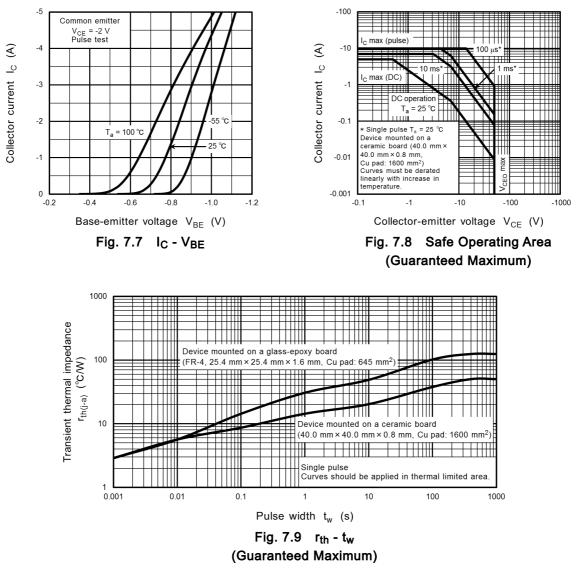
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The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

7. Characteristics Curves (Note)





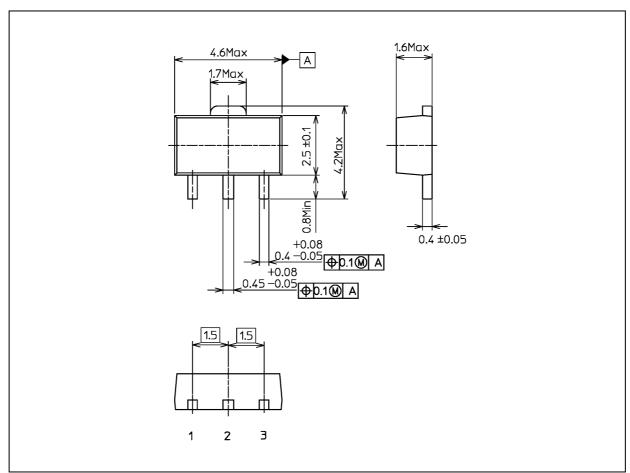


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

Unit: mm



Weight: 0.05 g (typ.)

	Package Name(s)
TOSHIBA: 2-5K1S	
Nickname: PW-Mini	

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