

MOSFETs Silicon P-Channel MOS (U-MOSVI)

TJ15S10M3

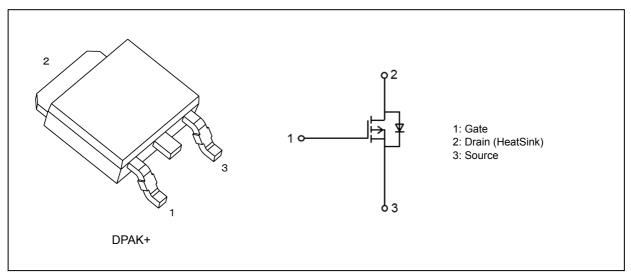
1. Applications

- Switching Voltage Regulators
- · Motor Drivers

2. Features

- (1) Low drain-source on-resistance: $R_{DS(ON)} = 100 \text{ m}\Omega$ (typ.) ($V_{GS} = -10 \text{ V}$)
- (2) Low leakage current: $I_{DSS} = -10 \mu A \text{ (max) (V}_{DS} = -100 \text{ V)}$
- (3) Enhancement mode: V_{th} = -2.0 to -4.0 V (V_{DS} = -10 V, I_D = -1 mA)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) (Ta = 25 °C unless otherwise specified)

Characteristic	Symbol	Rating	Unit		
Drain-source voltage			V_{DSS}	-100	V
Gate-source voltage			V _{GSS}	-20/+10	V
Drain current (DC)	-	(Note 1)	I _D	-15	Α
Drain current (pulsed)		(Note 1)	I _{DP}	-30	Α
Power dissipation	(T _c = 25°C)		P _D	75	W
Single-pulse avalanche energy		(Note 2)	E _{AS}	29.3	mJ
Avalanche current			I _{AR}	-15	Α
Channel temperature		(Note 3)	T _{ch}	175	°C
Storage temperature		(Note 3)	T _{stg}	-55 to 175	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production



5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R _{th(ch-c)}	2.0	°C/W

Note 1: Ensure that the channel temperature does not exceed 175 °C.

Note 2: V_{DD} = -25 V, T_{ch} = 25 °C (initial), L = 210 μ H, R_{G} = 25 Ω , I_{AR} = -15 A

Note 3: The definitions of the absolute maximum channel and storage temperatures are based on AEC-Q101.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

Rev.8.0



6. Electrical Characteristics

6.1. Static Characteristics (T_a = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	V _{GS} = -20/+10 V, V _{DS} = 0 V	_	_	±1	μΑ
Drain cut-off current	I _{DSS}	V _{DS} = -100 V, V _{GS} = 0 V	_	_	-10	μА
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = -10 mA, V _{GS} = 0 V	-100	_	_	V
Drain-source breakdown voltage (Note 4)	V _{(BR)DSX}	I _D = -10 mA, V _{GS} = 10 V	-75	_	_	
Gate threshold voltage	V_{th}	V _{DS} = -10 V, I _D = -1 mA	-2.0	_	-4.0	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = -10 V, I _D = -7.5 A	_	100	130	mΩ

Note 4: If a reverse bias is applied between gate and source, this device enters $V_{(BR)DSX}$ mode. Note that the drain-source breakdown voltage is lowered in this mode.

6.2. Dynamic Characteristics (T_a = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz	_	3200	_	pF
Reverse transfer capacitance	C _{rss}		_	135		
Output capacitance	C _{oss}		_	190	_	
Switching time (rise time)	t _r	See Fig. 6.2.1.	_	12	_	ns
Switching time (turn-on time)	t _{on}		_	28	_	
Switching time (fall time)	t _f		_	41	_	
Switching time (turn-off time)	t _{off}		_	290	1	

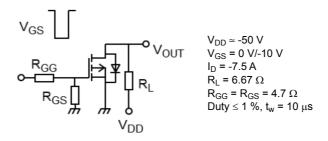


Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics (T_a = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Q_g	$V_{DD} \approx -80 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -15 \text{ A}$		69		nC
Gate-source charge 1	Q _{gs1}		_	9.4		nC
Gate-drain charge	Q_{gd}		_	20		nC

6.4. Source-Drain Characteristics (T_a = 25 °C unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 5)	I_{DR}	_	_	_	-15	Α
Reverse drain current (pulsed)	(Note 5)	I _{DRP}	_	_	_	-30	Α
Diode forward voltage		V_{DSF}	$I_{DR} = -15 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	1.4	V
Reverse recovery time			$I_{DR} = -15 \text{ A}, V_{GS} = 0 \text{ V},$	_	70	_	ns
Reverse recovery charge		Q_{rr}	dl _{DR} /dt = 50 A/μs	_	95		nC

Note 5: Ensure that the channel temperature does not exceed 175 $^{\circ}\text{C}$.



7. Marking (Note)

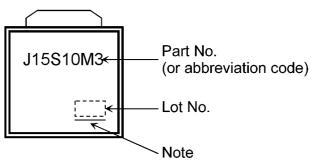


Fig. 7.1 Marking

Note: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.



8. Characteristics Curves (Note)

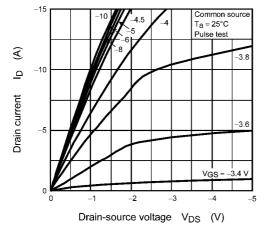


Fig. 8.1 I_D - V_{DS}

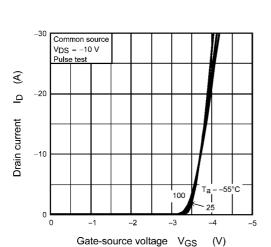


Fig. 8.3 I_D - V_{GS}

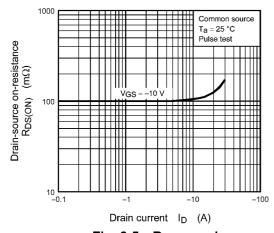


Fig. 8.5 R_{DS(ON)} - I_D

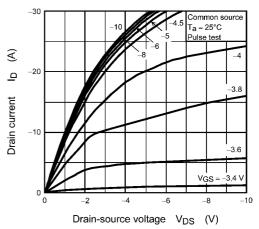


Fig. 8.2 I_D - V_{DS}

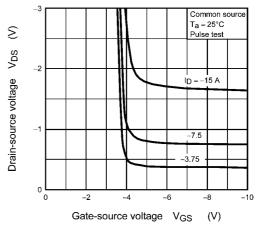


Fig. 8.4 VDS - VGS

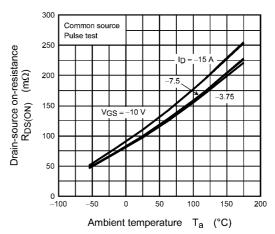


Fig. 8.6 R_{DS(ON)} - T_a



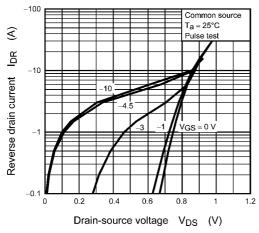


Fig. 8.7 I_{DR} - V_{DS}

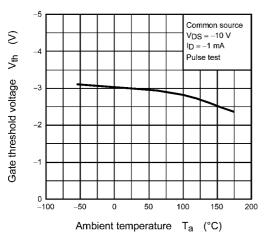


Fig. 8.9 V_{th} - T_a

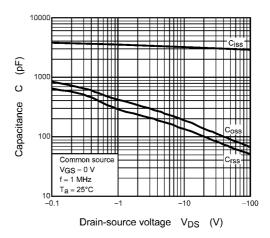


Fig. 8.8 Capacitance - V_{DS}

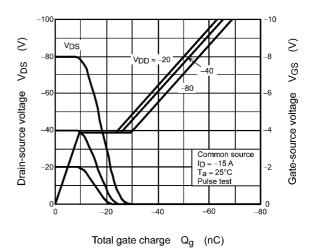


Fig. 8.10 Dynamic Input/Output Characteristics



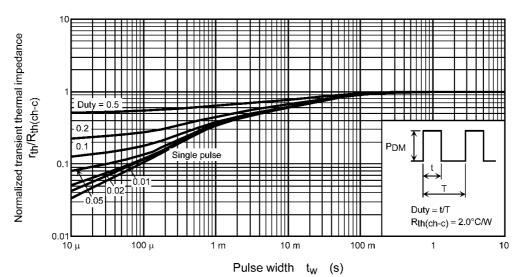


Fig. 8.11 r_{th} - t_w (Guaranteed Maximum)

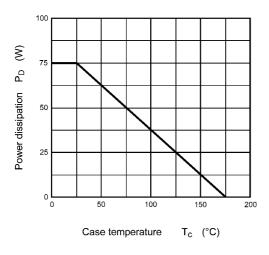


Fig. 8.12 P_D - T_c (Guaranteed Maximum)

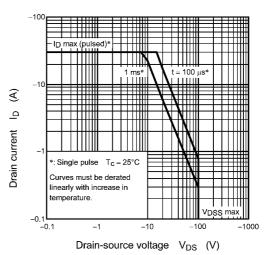


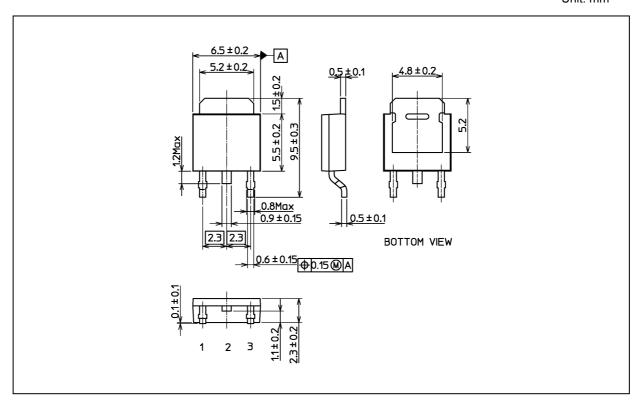
Fig. 8.13 Safe Operating Area (Guaranteed Maximum)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

Unit: mm



Weight: 0.36 g (typ.)

	Package Name(s)
TOSHIBA: 2-7M1A	
Nickname: DPAK+	



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