ESD Protection Diodes Silicon Epitaxial Planar

DF6S25P3NU

1. General

The DF6S25P3NU is a TVS diode (ESD protection diode) protects semiconductor devices used in mobile device interfaces and other applications to protect against static electricity and noise.

The DF6S25P3NU has realized high I_{PP} , in order to protect a semiconductor devices from the indirect lightning stroke and the transition voltage (at the time of power activation).

Furthermore, the DF6S25P3NU is housed in an ultra-compact package (2.0 mm \times 2.0 mm) to meet applications that require a small footprint.

2. Applications

Mobile Equipment

- · Smartphones
- · Tablets
- · Notebook PCs

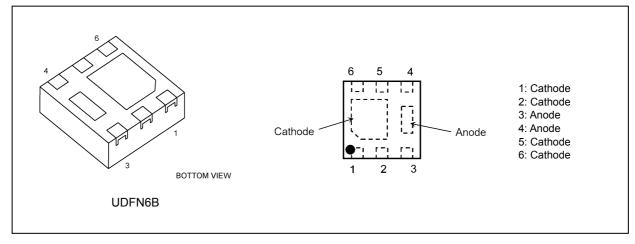
Desktop PCs

Note: This product is designed for protection against electrostatic discharge (ESD) and is not intended for any other purpose, including, but not limited to, voltage regulation.

3. Features

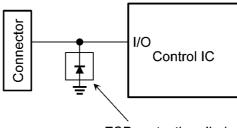
- (1) Suitable for use with a 20 V signal line. (V_{RWM} \leq 22 V)
- (2) Protects devices with its high ESD performance. $(V_{ESD} = \pm 30 \text{ kV} (\text{Contact / Air}) @\text{IEC61000-4-2})$
- (3) Low dynamic resistance protects semiconductor devices from static electricity and noise. $(R_{DYN} = 0.01 \Omega (typ.))$
- (4) Snapback characteristics realizing low clamping voltage protects semiconductor devices. $(V_C = 42 \text{ V}@I_{PP} = 110 \text{ A (typ.)})$
- (5) Compact package is suitable for use in high density board layouts such as in mobile devices.
 (2.0 mm × 2.0 mm size (Nickname: UDFN6B))

4. Packaging and Pin Assignment



Start of commercial production 2022-12

5. Example of Circuit Diagram



ESD protection diode

6. Quick Reference Data

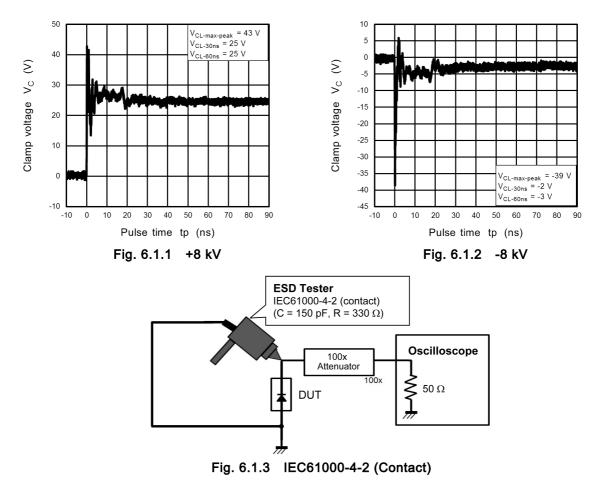
Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Working peak reverse voltage	V _{RWM}	(Note 1)	_	_	_	22	V
Dynamic resistance	R _{DYN}	(Note 2)	_	_	0.01	—	Ω
Electrostatic discharge voltage (IEC61000-4-2) (Contact)	V_{ESD}	(Note 3)	_			30	kV

Note 1: Recommended operating condition.

Note 2: TLP parameters: $Z0 = 50 \Omega$, tp = 100 ns, tr = 300 ps, averaging window: t1 = 30 ns to t2 = 60 ns, extraction of dynamic resistance using least squares fit of TLP characteristics between I_{PP1} = 16 A and I_{PP2} = 30 A.

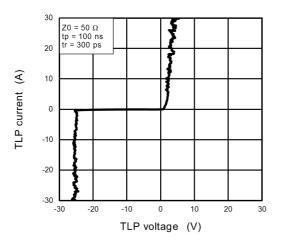
Note 3: Criterion: No damage to devices.

6.1. ESD Clamp Waveform (Note)



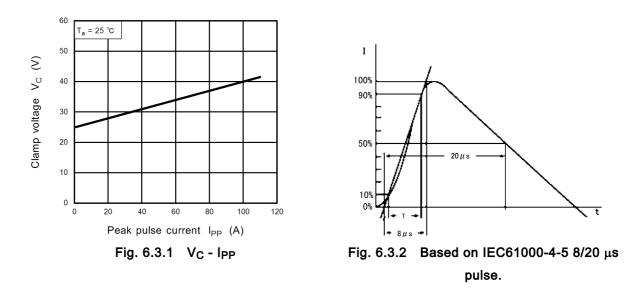
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

6.2. TLP Characteristics (Note)



Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

6.3. Clamp Voltage - Peak Pulse Current (V_C - I_{PP}) (Note)



Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

7. Absolute Maximum Ratings (Note) (Unless otherwise specified, $T_a = 25$ °C)

Characteristics	Symbol	Note	Rating	Unit
Electrostatic discharge voltage (IEC61000-4-2) (Contact)	V _{ESD}	(Note 1)	±30	kV
Electrostatic discharge voltage (IEC61000-4-2) (Air)			±30	
Peak pulse power (tp = 8/20 μs)	P _{PK}		5060	W
Peak pulse current (tp = 8/20 μs)	I _{PP}	(Note 2)	110	А
Junction temperature	Тj		150	°C
Storage temperature	T _{stg}		-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: According to IEC61000-4-2.

Note 2: According to IEC61000-4-5.

8. Electrical Characteristics (Unless otherwise specified, T_a = 25 °C)

 $\label{eq:V_RWM} \begin{array}{l} \text{Working peak reverse voltage} \\ V_{BR}: \text{Reverse breakdown voltage} \\ I_{BR}: \text{Reverse breakdown current} \\ I_{R}: \text{Reverse current} \\ V_{C}: \text{Clamp voltage} \\ I_{PP}: \text{Peak pulse current} \\ R_{DYN}: \text{Dynamic resistance} \end{array}$

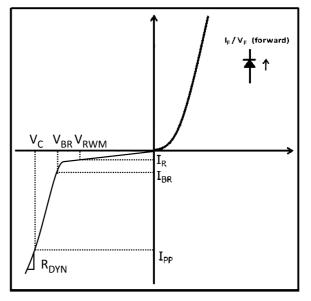


Fig. 8.1 Definitions of Electrical Characteristics

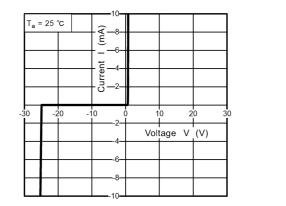
Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Working peak reverse voltage	V _{RWM}	(Note 1)	_	_	_	22	V
Total capacitance	Ct		V _R = 0 V, f = 1 MHz	_	650	_	pF
Dynamic resistance	R _{DYN}	(Note 2)	_		0.01	_	Ω
Reverse breakdown voltage	V_{BR}		I _{BR} = 1 mA	22.5	25	26.5	V
Reverse current	I _R		V _{RWM} = 22 V	_	_	0.1	μA
Clamp voltage	V _C	(Note 3)	I _{PP} = 1 A	_	25	_	V
			I _{PP} = 110 A		42	46	
		(Note 2)	I _{TLP} = 30 A	_	25.8	_	V

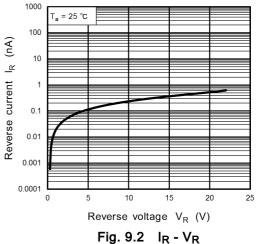
Note 1: Recommended operating condition.

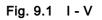
Note 2: TLP parameters: Z0 = 50 Ω , tp = 100 ns, tr = 300 ps, averaging window: t1 = 30 ns to t2 = 60 ns, extraction of dynamic resistance using least squares fit of TLP characteristics between I_{PP1} = 16 A and I_{PP2} = 30 A.

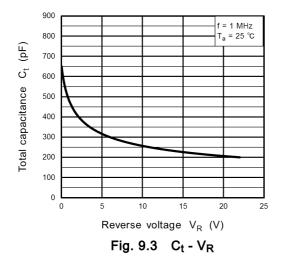
Note 3: Based on IEC61000-4-5 8/20 μs pulse.

9. Characteristics Curves (Note)



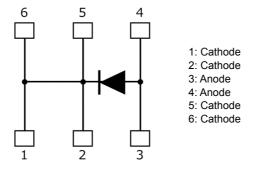




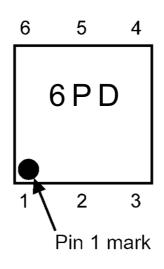


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

10. Equivalent Circuit



11. Marking (Top view)

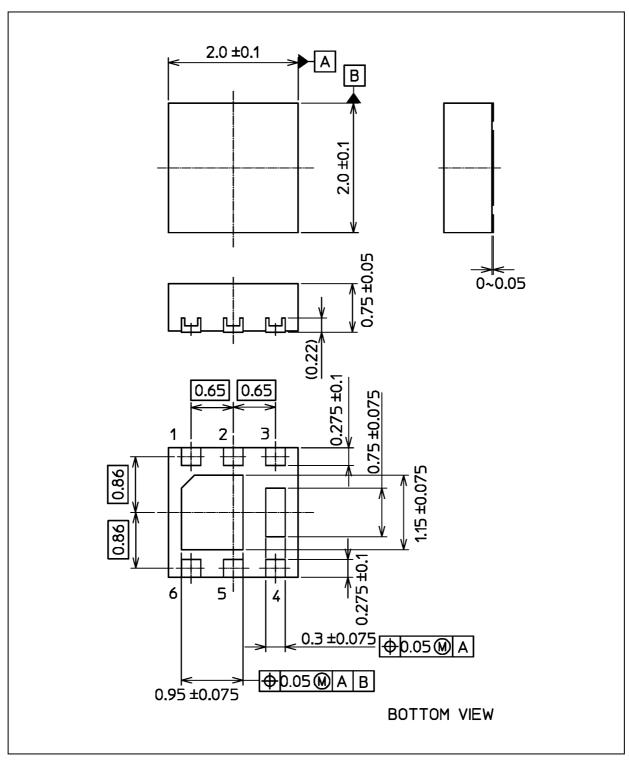




DF6S25P3NU

Package Dimensions

Unit: mm



Weight: 8.5 mg (typ.)

Package Name(s)

Nickname: UDFN6B

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