# Non-isolated Synchronous Buck DC-DC Power Supply Basic Simulation Circuit Reference Guide

# RD047-RGUIDE-01

## **TOSHIBA ELECTRONIC DEVICES & STORAGE CORPORATION**

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### 1. Introduction

Most information and communication systems, home appliances, and other electric devices operate on DC voltage. Since they cannot operate on mains AC voltage, it is necessary to convert AC voltage to DC voltage. An AC-DC power supply is used for this purpose. It might be necessary to further convert the DC voltage from an AC-DC power supply to different DC voltage levels, depending on the requirement of each load in a system.

The device that converts one DC voltage level to another is called a DC-DC power supply. Fig. 1.1 shows an example of a system power line configuration. There are several power lines that supply different DC voltages to different loads. One load is directly supplied from the AC-DC power supply whereas other loads are supplied through one or two stages of DC-DC power supplies following the AC-DC power supply.



Fig. 1.1 Example of a system power line configuration

DC-DC power supplies are broadly divided into two categories: series regulators and switched-based regulators. This Reference Guide discusses switched-based regulators that are the mainstream of DC-DC power supplies. Switched-based regulators achieve voltage regulation by varying the on-off time ratio of switching devices. Switched-based regulators require more complicated control circuitry than series regulators, but generally exhibit less power loss.

There are two types of switched-based regulators: isolated DC-DC power supplies that incorporate a transformer to provide electrical isolation between the input and output sides and non-isolated DC-DC power supplies that do not provide electrical isolation between the input and output sides. This Reference Guide discusses non-isolated DC-DC power supplies.

A non-isolated DC-DC power supply is composed of an inductor, switching devices, and rectifier diodes. The switching devices operate at several hundreds of kilohertz to convert an input DC voltage to a regulated output DC voltage.

Three types of DC-DC power supplies are available: (a) buck (step-down) power supplies that step down voltage from input to output, (b) boost (step-up) power supplies that step up voltage from input to output, and (c) buck-boost power supplies with an output voltage that is either greater than or less than the input voltage. Fig. 1.2 shows the basic configurations of buck, boost, and buck-boost DC-DC power supplies. Because diodes cause a significant power loss during conduction, some DC-DC power supplies use MOSFETs instead of diodes. This technique is called synchronous rectification. Also, a step-down non-isolated DC-DC power supply is sometimes called a buck converter.



### Fig. 1.2 Examples of block diagrams of non-isolated DC-DC power supplies

This Reference Guide discusses a buck synchronous rectification DC-DC power supply that generates an output voltage lower than an input voltage and uses MOSFETs for rectification instead of diodes. Synchronous rectification requires more complicated control than diode rectification. However, despite the added MOSFET driver circuit, synchronous rectification helps improve efficiency under high-load conditions. Therefore, synchronous rectification is commonly used for power supplies with large output current. A basic simulation circuit of a non-isolated synchronous rectification buck DC-DC power supply (RD047-SPICE-01) is available for download on Toshiba's website, which will help you understand its operation.

The Reference Guide provides an overview of this simulation circuit and shows how to simulate. OrCAD<sup>®</sup> Capture and PSpice<sup>®</sup> A/D from Cadence are necessary to simulate this circuit. Both the simulation circuit and the Reference Guide are based on OrCAD<sup>®</sup> 17.2.

# 2. Overview of the non-isolated synchronous rectification buck DC-DC power supply

The basic simulation circuit (RD047-SPICE-01) is a 40 W non-isolated synchronous rectification DC-DC converter.

### 2.1. Power supply specifications

The specifications of the DC-DC converter are as follows:

- Input voltage: 12 V
- Output voltage: 5 V
- Output power: 40 W
- Operating frequency: 300 kHz
- Inductor current ripple ratio: 40 %
- Output inductor: 3.3 µH

### 2.2. Circuit configuration

Fig. 2.1 shows the simulation circuit for OrCAD<sup>®</sup>. It is a non-isolated synchronous rectification DC-DC power supply, which mainly consists of a DC-DC converter and a PWM controller. The low side of the power supply section is a MOSFET synchronous rectifier. The PWM controller is a general-purpose controller with a MOSFET gate driver, which was prepared to create this DC-DC power supply. As an example, the TPH6R003NL is used as the high-side MOSFET whereas the TPH2R903P is used as the low-side MOSFET.





Fig. 2.1 Simulation circuit for a 40 W non-isolated synchronous rectification buck DC-DC power supply

### Selection of the high-side MOSFET

The high-side MOSFET (TPH6R003NL:  $V_{DSS}$ =30 V,  $I_D$ =38 A) was selected, taking the following into consideration:

1. Withstand voltage

Since the input voltage of the DC-DC power supply is 12 V, a MOSFET with a withstand voltage of 30 V or higher was selected for the simulation circuit, considering voltage surge that occurs during switching.

2. Current rating

When the input current is maximum, it is at the maximum output power. Suppose that the DC-DC power supply has a conversion efficiency of 85 % at the maximum output power of 40 W. Then, the maximum input power is calculated to be 47.1 A. The average current applied to the high-side MOSFET is roughly 3.9 A when the output power is at the maximum. Therefore, a MOSFET with a current rating of 8 A or higher and a peak current rating of 16 A or higher was selected for the high-side MOSFET.

### Selection of the low-side MOSFET

The low-side MOSFET (TPH2R903PL:  $V_{DSS}$ =30 V,  $I_D$ =70 A) was selected, taking the following into consideration:

1. Withstand voltage

Since the input voltage of the DC-DC power supply is 12 V, a MOSFET with a withstand voltage of 30 V or higher was selected for the simulation circuit, considering voltage surge that occurs during switching.

2. Current rating

When the input current is maximum, it is at the maximum output power. The maximum output power of 40 W. Since the on-time of the low-side MOSFET is roughly 7/12 of the switching cycle, the average current applied to the low-side MOSFET is roughly 4.7 A when the output power is at the maximum. Therefore, a MOSFET with a current rating of 9 A or higher and a peak current rating of 18 A or higher was selected for the low-side MOSFET.

### Selection of the inductor

The following paragraphs describe how to select an inductor. The value of the inductor to be used in the simulation circuit can be calculated from the following power supply parameters:

- Input voltage: V<sub>in</sub> (V)
- Output voltage: V<sub>out</sub> (V)
- Switching frequency:  $F_C$  (Hz)
- Output current: I<sub>out</sub> (A)
- Inductor value: L (H)
- Inductor current ripple ratio:  $\Delta I_{L}$  (%)

The ripple fluctuation rate ( $\Delta I_L$ ) of the inductor current is expressed as follows.

$$\Delta I_{L} = \left(\frac{Vin - Vou}{L} \times \frac{Vout}{Vin \times Fc}\right) \div Iout \times 100$$

The DC-DC power supply has the maximum inductor ripple current when the output current has the maximum value. From the power supply specifications, substituting 12 V for V<sub>in</sub> (input voltage), 5 V for V<sub>out</sub> (output voltage), 300 kHz for F<sub>c</sub> (switching frequency), 8 A for I<sub>out</sub> (output current), and 40 % for  $\Delta$ I<sub>L</sub> (inductor current ripple ratio), the inductor value (L) is calculated to be 3.04 µH. Therefore, a 3.3 µH inductor was selected for the simulation circuit.

In practice, the inductor value varies because of DC bias characteristics. Select an inductor that exhibits inductance greater than the above result even when inductance decreases because of DC bias characteristics.

### 3. Simulation results

This section shows the simulation waveforms at the points (1) to (2) shown in Fig. 3.1.

- (1) A non-isolated DC-DC power supply (buck converter) basic operation (Drain-source voltage and drain current of the high-side and low-side MOSFETs, and output inductor current)
- (2) Output voltage and current from the DC-DC power supply

The simulation circuit model also allows you to view other waveforms. See Section 5 for how to view waveforms.



Fig. 3.1 Points at which simulation waveforms are measured

### (1) Principle of operation of a buck converter

The following describes the basic operation of the buck DC-DC converter using the circuit shown in Fig. 3.2.



Fig. 3.2 Buck converter

 $Q_1$  is the high-side MOSFET,  $Q_2$  is the low-side MOSFET, and  $L_0$  is the output inductor. D ( $Q_2$ ) is the drain-source body diode in  $Q_2$ . Generally, a buck DC-DC converter is controlled with pulse-width modulation (PWM) by switching  $Q_1$  at a constant frequency while varying its duty cycle (D=T<sub>on</sub>/T). Its output voltage is expressed as:

$$V_{out} = \frac{T_{on}}{T} \times V_{in}$$

T: Cycle period (s)

 $T_{on}$ : On-time of the high-side MOSFET (s)

The operation of this buck converter during each on-off period is outlined below.

a. Q1 ON, Q2 OFF

When Q1 turns on,  $V_{Lo} = V_{in} - V_{out}$  is applied to the output inductor (L<sub>0</sub>).

Therefore, the inductor current increases linearly according to the following equation, providing output current ( $I_{out}$ ) while charging  $C_{out}$ .

$$i_{Lo(on)} = \frac{V_{in} - V_{out}}{L_o} \times T_{on}$$

During this period, magnetic energy is stored in  $L_0$ .

When  $Q_1$  turns on, a pulsed current flows instantaneously, charging the output capacitance ( $C_{oss}$ ) of  $Q_2$ .

b. Q<sub>1</sub> OFF, Q<sub>2</sub> OFF.

This is a dead time during which both  $Q_1$  and  $Q_2$  are set to off in order to prevent a shoot-through current due to cross conduction. During this period, the energy stored in  $L_0$  freewheels through D(Q2), supplying  $I_{out}$ . (V)

### c. Q1 OFF, Q2 ON

 $Q_2$  turns on, causing the D( $Q_2$ ) freewheeling current to flow from the source to the drain of  $Q_2$ , supplying  $I_{out}$ . The conduction loss that occurs while  $Q_2$  is on is  $I_D^2 \propto R_{DS(ON)}$ , which is smaller than diode conduction loss ( $V_F \propto I_F$ ). During this period, the current flowing through the inductor ( $L_0$ ) decreases linearly according to the following equation:

$$i_{Lo(off)} = \frac{-V_{out}}{L_0} \times T_{off}$$

### d. Q1 OFF, Q2 OFF

This is a dead time inserted to prevent shoot-through current in the same manner as b. During this period, a freewheeling current flows through D  $(Q_2)$ .





### (2) Output voltage and current from the DC-DC power supply

Fig. 3.4 shows the waveforms of the output voltage and current from the DC-DC power supply. It shows that the DC-DC power supply achieves proper output voltage and current regulation following a soft-start period of 1 ms.



Fig. 3.4 Output voltage and current waveforms

### 4. Product Overview

This section provides an overview of Toshiba's devices used as PSpice<sup>®</sup> models in the simulation circuit.

### 4.1. TPH6R003NL

### **Characteristics**

- V<sub>DSS</sub>=30 V, I<sub>D</sub>=38 A
- Fast switching
- Low input gate charge: Q<sub>sw</sub>=4.3 nC (typ.)
- Low on-resistance:  $R_{DS(ON)}=6.8 \text{ m}\Omega \text{ (typ.)} (V_{GS}=4.5 \text{ V})$
- Low leakage current:  $I_{DSS}=10 \ \mu A \ (max) \ (V_{DS}=30 \ V)$
- Easy-to-use enhanced-mode MOSFET:  $V_{th}=1.3$  to 2.3 V ( $V_{DS}=10$  V,  $I_D=0.2$  mA)

### External view and pin assignment



6.0mm (W) × 5.0mm (L) × 0.95mm (H)

### 4.2. TPH2R903PL

### **Characteristics**

- V<sub>DSS</sub>=30 V, I<sub>D</sub>=70 A
- Fast switching
- Low input gate charge: Q<sub>SW</sub>=5.6 nC (typ.)
- Low output charge: Q<sub>oss</sub>=17 nC (typ.)
- Low on-resistance:  $R_{DS(ON)}=2.1 \text{ m}\Omega \text{ (typ.)} (V_{GS} = 10 \text{ V})$
- Low leakage current:  $I_{DSS}=10 \ \mu A \ (max) \ (V_{DS}=30 \ V)$
- Easy-to-use enhanced-mode MOSFET:  $V_{th}=1.1$  to 2.1 V ( $V_{DS}=10$  V,  $I_D=0.2$  mA)

### External view and pin assignment



6.0mm (W) × 5.0mm (L) × 0.95mm (H)

### 5. Using the simulation circuit

You can freely change various parameters with OrCAD<sup>®</sup> Capture to verify the circuit operation according to the actual power supply specifications and evaluate how these parameters affect the circuit operation. This section shows how to set simulation parameters and verify the circuit operation.

### Parameter settings

Table 5.1 shows the parameters you can set for the simulation circuit. Double-click a parameter name in the PARAMETERS section, then the Display Properties dialog box appears as shown in Fig. 5.1. Change the value in the Value field.

Parameter name	Unit	Description
Vin	V	Input voltage
Vout	V	Output voltage
L	Н	Inductor value
Cin	F	Input capacitor
Cout	F	Output capacitor
Fc	Hz	Switching frequency
Vdrv_H	V	MOSFET drive voltage
Ddry off b	_off_h Ω	Internal resistance of the turn-off gate driver for the
Kurv_on_n		high-side MOSFET
Ddn/ on h	0	Internal resistance of the turn-on gate driver for the
	22	high-side MOSFET
Pdpy off I	f_l Ω	Internal resistance of the turn-off gate driver for the
		low-side MOSFET
Pdry on L	0	Internal resistance of the turn-on gate driver for the
	22	low-side MOSFET

 Table 5.1 Parameters that can be modified in the Parameters section



Fig. 5.1 Display Properties dialog box

### Setting analysis parameters

The following describes how to run a simulation on the simulation circuit.

 From the menu bar of OrCAD<sup>®</sup> Capture, select **PSpice** - **New Simulation Profile**. Then, the New Simulation dialog box shown in Fig. 5.2 appears. Enter an arbitrary profile name and click **Create**.

New Simulation	<b>—</b>
Name:	Create
Tran_500ms	
Inherit From:	Cancel
none 🔻 🗔	]
Root Schematic: SCHEMATIC1	

Fig. 5.2 New Simulation dialog box

(2) The Simulation Settings dialog shown in Fig. 5.3 appears, which allows you to set various analysis parameters. Select the Analysis tab first. Select Time Domain (Transient) from the Analysis Type drop-down list. Enter an analysis end time in the Run To Time field and the maximum step size in the Maximum Step Size field.

	General Analysis Co	nfiguration Files	Options	Data Collection	Probe Window	
a. Select "Time Domain (Transient)"	Time Domain (Transient) Options: General Settings Monte Carlo/Worst Case Parametric Sweep Temperature (Sweep) Save Bias Point Load Bias Point Save Check Point Restart Simulation	Start savir Transien Maximun Skip ir Run in	ng data after : It options: n Step Size nitial transient resume mode	0 0.1u bias point calculati	seconds seconds on (SKIPBP) c. Ente	b. Enter the simulation end time er the maximum step size

Fig. 5.3 Simulation Settings - Analysis dialog box

3. Click the **Options** tab to choose analysis options. For the simulation of our model, it is recommended to check **Analog Simulation** - **Auto Converge** - **AutoConverge** as shown in Fig. 5.4 to enable the automatic convergence feature.

eneral Analysis Configu	ration Files Options	Data Collection Probe	Window
Analog Simulation General	Name	Value	Default Value
MOSFET Option	ITL1	1000	1000
Analog Advanced	ITL2		1000
Bias Point Transient		To check "Aut is recomm	oConverge"
Gate Level Simulation	VNTOL	.001	.001
Advanced	PIVTOL	1.0E-10	1.0E-10
L General			

### Fig. 5.4 Simulation Settings - Options dialog box

- 4. Click **OK** to close the Simulation Settings dialog box.
- 5. To run a simulation, select **PSpice Run** from the menu bar of OrCAD<sup>®</sup> Capture. Then, PSpice A/D starts automatically and runs a simulation.

### Viewing simulation results

The following describes how to view the simulation results. You can display the waveforms of the simulation results in two ways.

#### Method 1: Selecting traces

- 1. Right-click outside the graph area and select **Add Trace** as shown in Fig. 5.5.
- Then, the Add Traces dialog box shown in Fig. 5.6 appears. Select traces to be added to a selected plot. To view a voltage waveform, select V(trace\_name). To view a current waveform, select I(device\_name). See Fig. 5.6.
- 3. Click **OK**. Then, the selected waveform appears as shown in Fig. 5.7.



Fig. 5.5 Graph window

officiation output variables			Functions of Macros	
			Analog Operators and Fu	inctions 🔹 🔻
13(U1) 13(U5)	*	🔽 Analog	#	
13(U7) 14(G1)		Digital	*	
I4(MULT1)		Voltages		
14(U5) 14(U7)		🔽 Currents	() (@ (ABS())	
Time		Power	ARCTAN()	
V(D8:1) V(drv_out) V(eramp_out)		Noise (V²/Hz)	AVG() AVG() AVGX(,)	
V(G1:IN+) V(H1:1)		🔲 Alias Names	COS() D()	
V(lac) V(pie. out)		Subcircuit Nodes		
V(R10:2) V(R1:1) V(R1:1) V(R8:1) V(R8:1) V(R9:1)			ENVMIN(,) EXP() G() IMG() LOG()	
V(R9:2) V(U5:+) V(U5:-)	-	98 variables listed	LOGÌÔ() M() MAX()	





Fig. 5.7 Simulation waveform view (Example: PFC output voltage waveform)

### Method 2: Adding markers

- 1. From the menu bar of OrCAD<sup>®</sup> Capture, select **PSpice Markers** and then a type of marker as shown in Fig. 5.8.
- 2. Place the selected marker on the desired node in the simulation circuit as shown in Fig. 5.9.
- 3. Then, its waveform appears in the graph window of PSpice A/D as shown in Fig. 5.10.



Fig. 5.8 Selecting a marker type



Fig. 5.9 Placing a marker in the circuit



Fig. 5.10 Simulation waveform view (Example: Output voltage waveform)

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