

**1.6 kW T-Type 3-Level PFC  
Power Supply  
Reference Guide**

**RD172-RGUIDE-01**

---

**TOSHIBA ELECTRONIC DEVICES & STORAGE CORPORATION**

## Table of Contents

<b>1. Introduction</b>	<b>3</b>
<b>2. Specifications</b>	<b>4</b>
2.1. Power Supply Specifications	4
2.2. Block Diagram	5
2.3. External View	6
2.4. PCB Component Layout	7
<b>3. Circuit Diagram, Bill of Material, and PCB Pattern Diagram</b>	<b>8</b>
3.1 Circuit Diagram	8
3.2. Bill of Material	8
3.3. PCB Pattern Diagram	8
<b>4. Operating Procedure</b>	<b>11</b>
4.1. Connections	11
4.2. Start and Stop Procedures	11
4.3. Evaluation Precautions (To Prevent Electric Shock, Burn Injury, etc.)	11
<b>5. Power Characteristics</b>	<b>12</b>
5.1. Efficiency	12

## **1. Introduction**

This reference guide (hereinafter referred to as "this guide") is a document that describes the specifications, usage method, and characteristics of 1.6 kW T-Type 3-Level PFC Power Supply (hereinafter referred to as "this power supply").

## 2. Specifications

### 2.1. Power Supply Specifications

Table 2.1 lists the main specifications of this power supply.

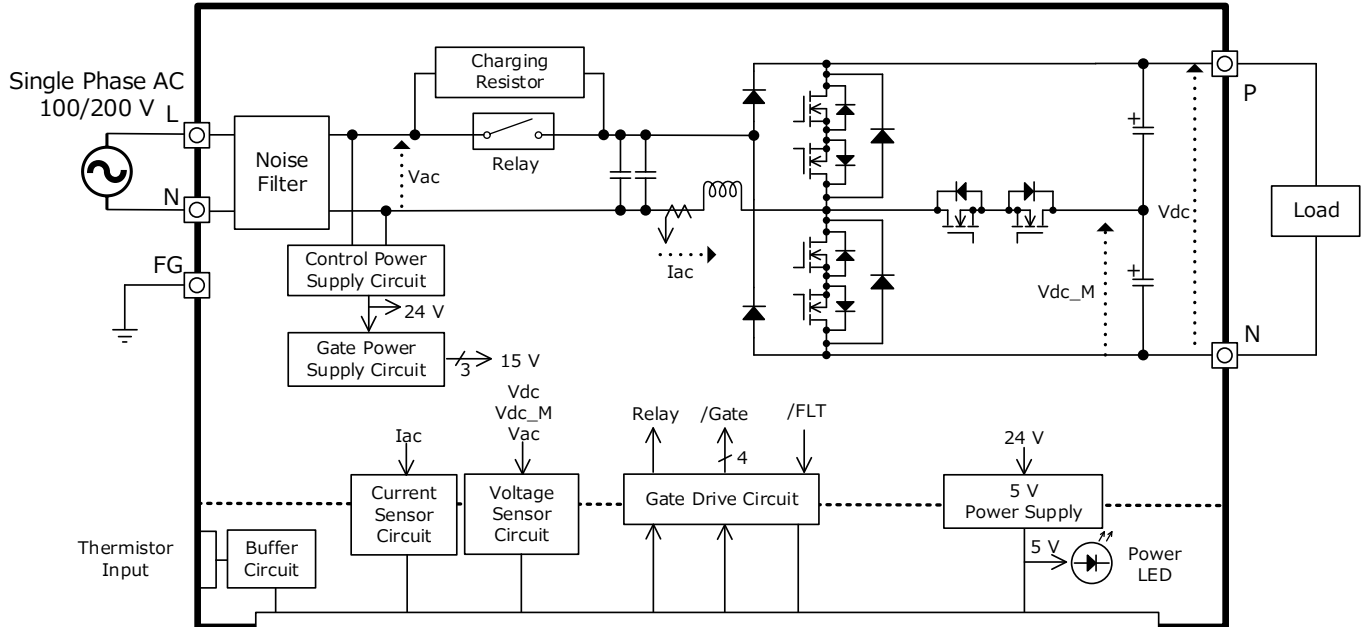
**Table 2.1 Specifications of 1.6 kW T-Type 3-Level PFC Power Supply**

Item	Specification	Remark
AC Input Voltage Range	90 to 264 V	
AC Input Frequency Range	47 to 53 Hz, 57 to 63 Hz	Considering the fundamental frequency is 50 Hz / 60 Hz.
DC Output Rated Voltage	380 V	
Control Power Supply Voltage	DC 24 V	
Gate Voltage	DC 15 V	
DC Output Maximum Current	4.2 A	
Power Rating	1.6 kW @ AC 180 to 264 V 800 W @ AC 90 to 115 V	Between AC 115 V and 180 V with fixed DC output voltage of 380 V the outputs power changes according to the input AC voltage.
Switching Frequency	100 kHz	
Sensor Input	<ul style="list-style-type: none"> <li>•AC input voltage</li> <li>•AC input current</li> <li>•DC output voltage</li> <li>•DC midpoint voltage</li> </ul>	The DC midpoint voltage is the voltage between two series capacitors and the DC output N terminal.

### 2.2. Block Diagram

Fig. 2.1 shows the block diagram of this power supply. This reference consists of a main circuit board and a control board.

#### Main Circuit Board



#### Control Board

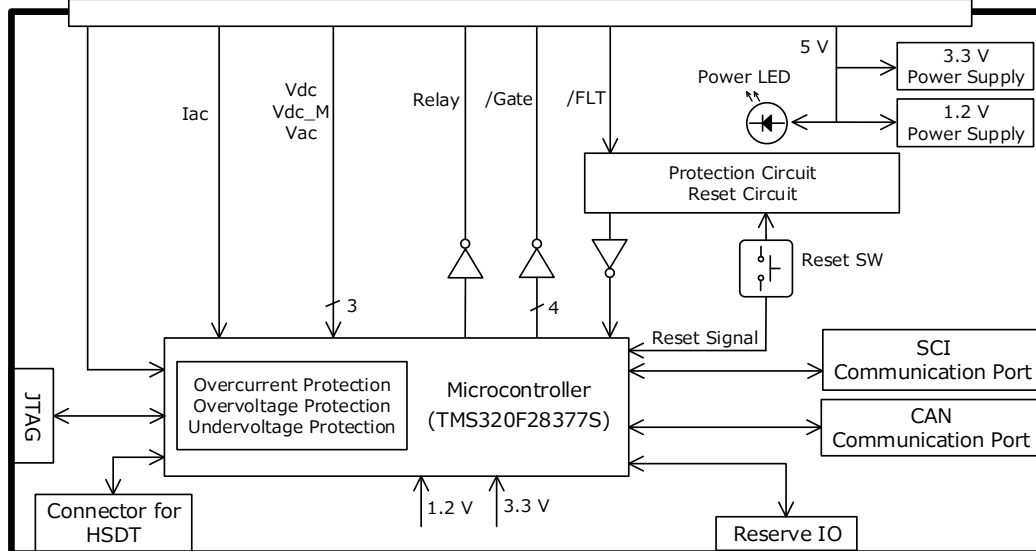


Fig. 2.1 Block Diagram

### 2.3. External View

Fig. 2.2 and Fig. 2.3 show the appearance of this power supply.

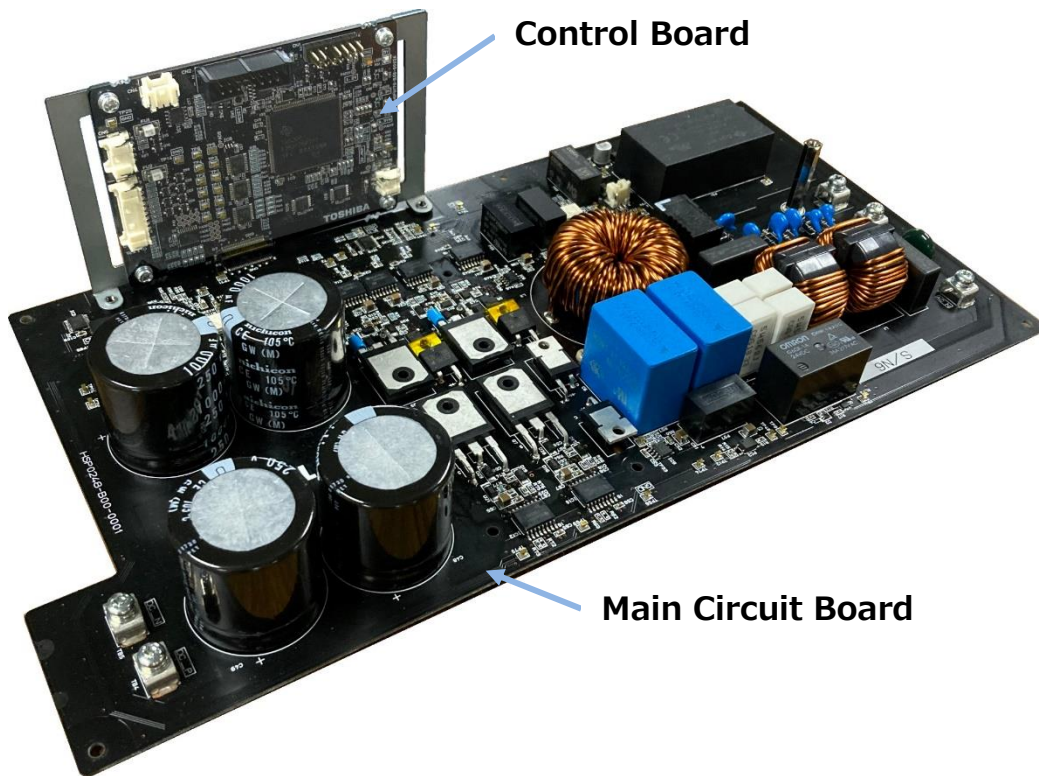


Fig. 2.2 Side View of 1.6 kW T-Type 3-Level PFC Power Supply

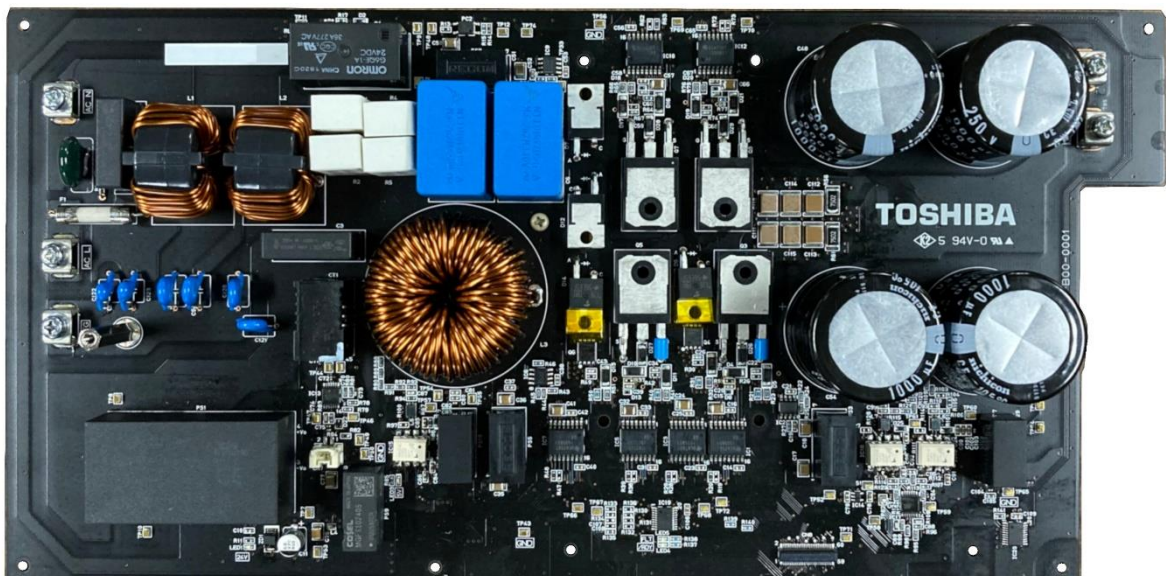
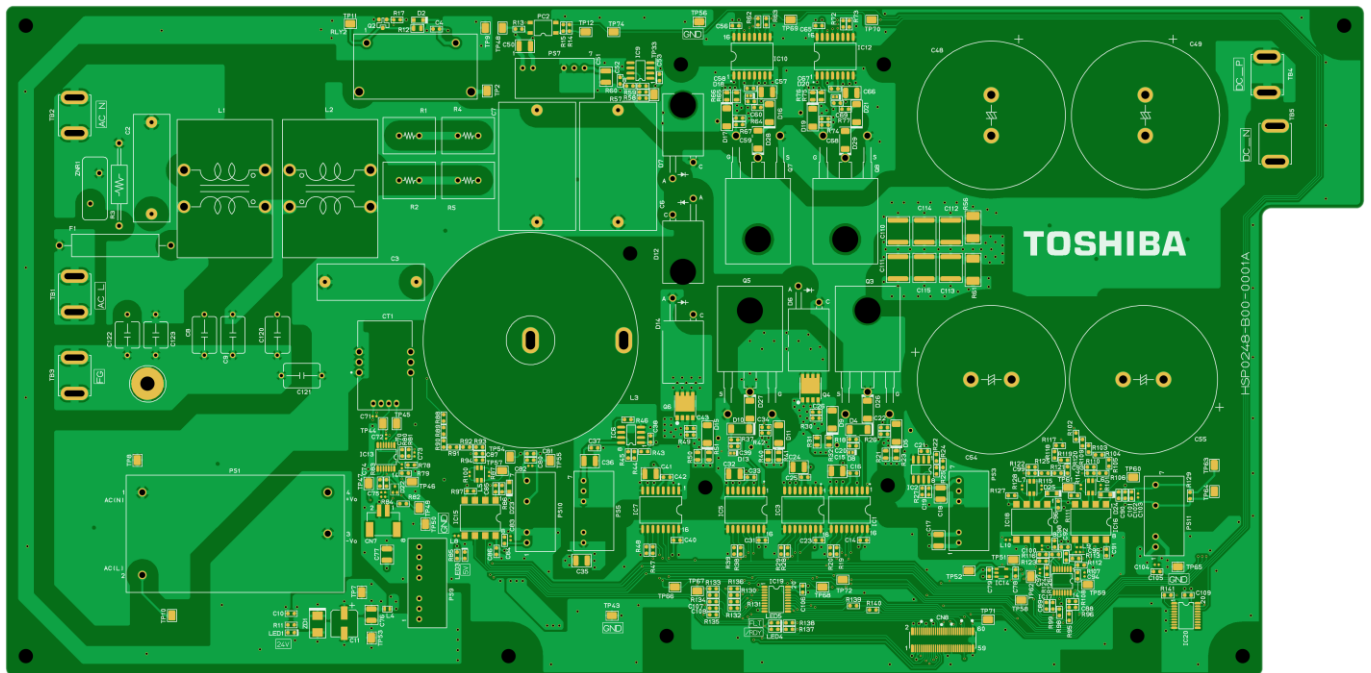


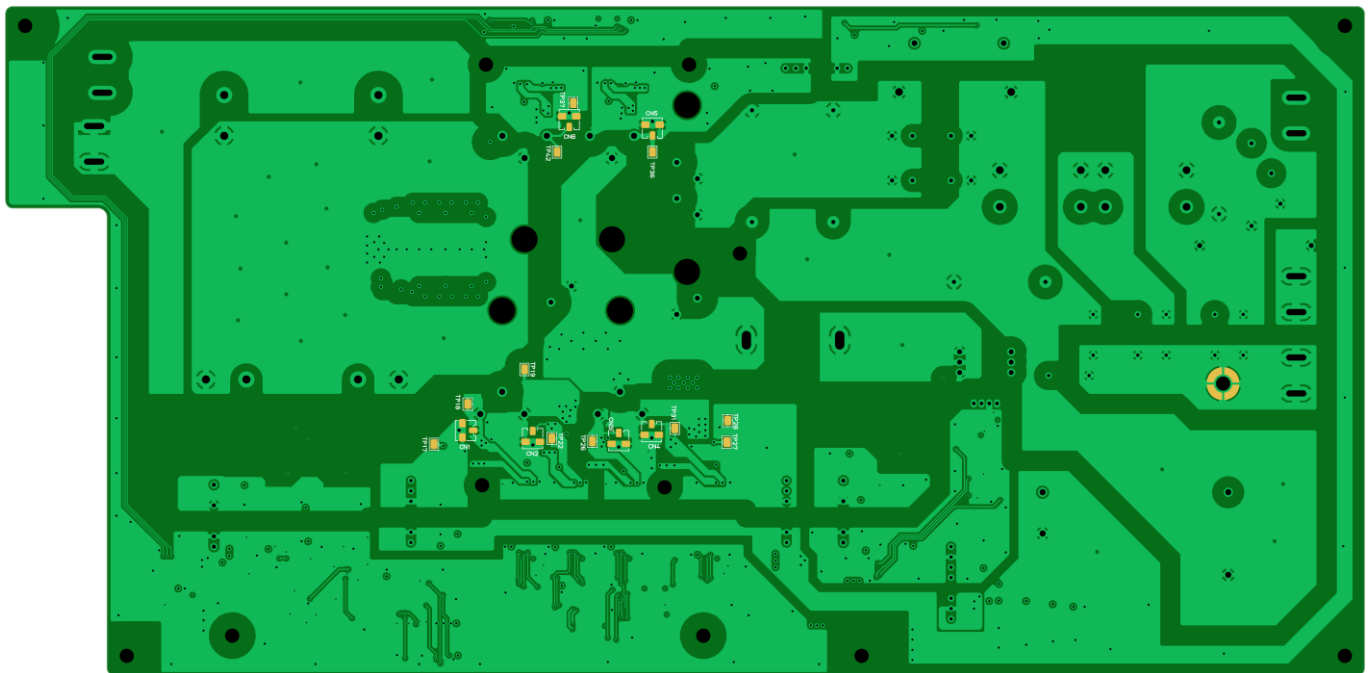
Fig. 2.3 Top view of 1.6 kW T-Type 3-Level PFC Power Supply (Main Circuit Board)

### 2.4. PCB Component Layout

Fig. 2.4 shows the component layout of the main circuit board of this power supply.



<Front>



<Back>

**Fig. 2.4 Component Layout of 1.6 kW T-Type 3-Level PFC Power Supply (Main Circuit Board)**

## **3. Circuit Diagram, Bill of Material, and PCB Pattern Diagram**

### **Diagram**

#### **3.1 Circuit Diagram**

Refer to the following files for the circuit diagram of this power supply.

Main circuit board    RD172-SCHEMATIC-xx.pdf  
Control board        RD044-SCHEMATIC2-xx.pdf  
(xx is the revision number)

#### **3.2. Bill of Material**

Refer to the following files for the Bill of Material (BOM) for this power supply.

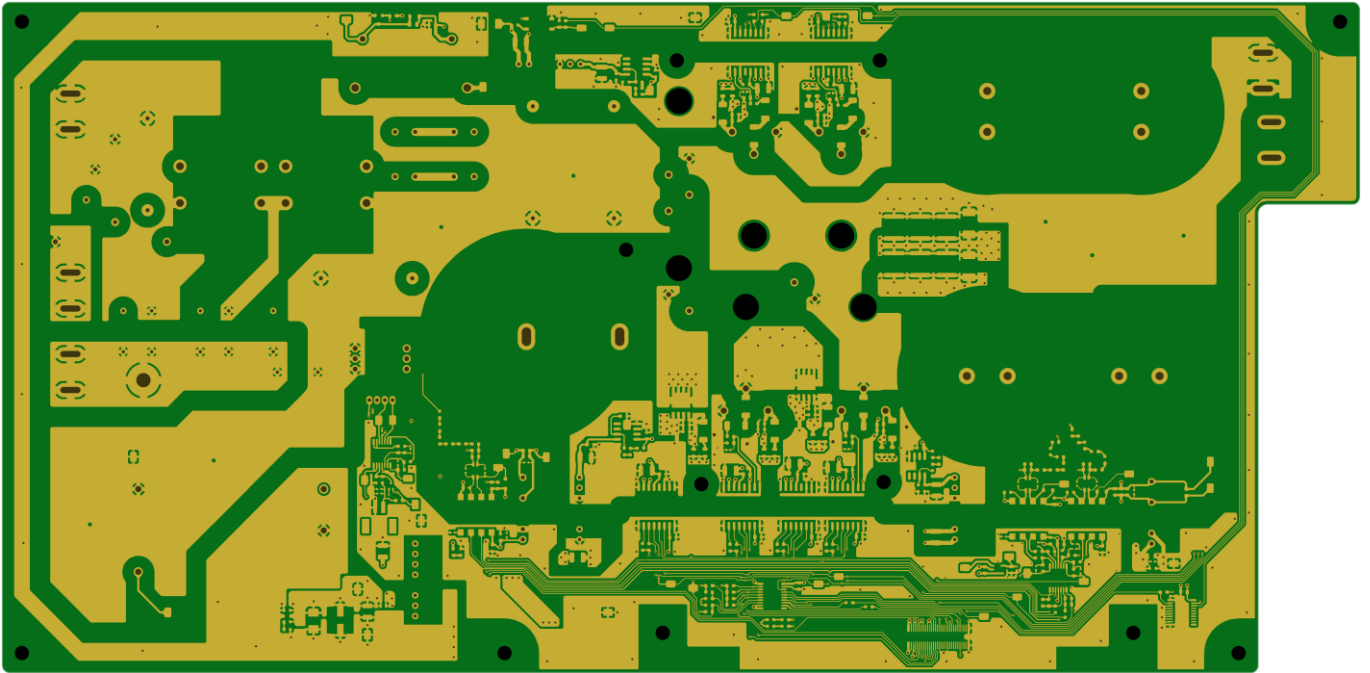
Main circuit board    RD172-BOM-xx.pdf  
Control board        RD044-BOM2-xx.pdf  
(xx is the revision number)

#### **3.3. PCB Pattern Diagram**

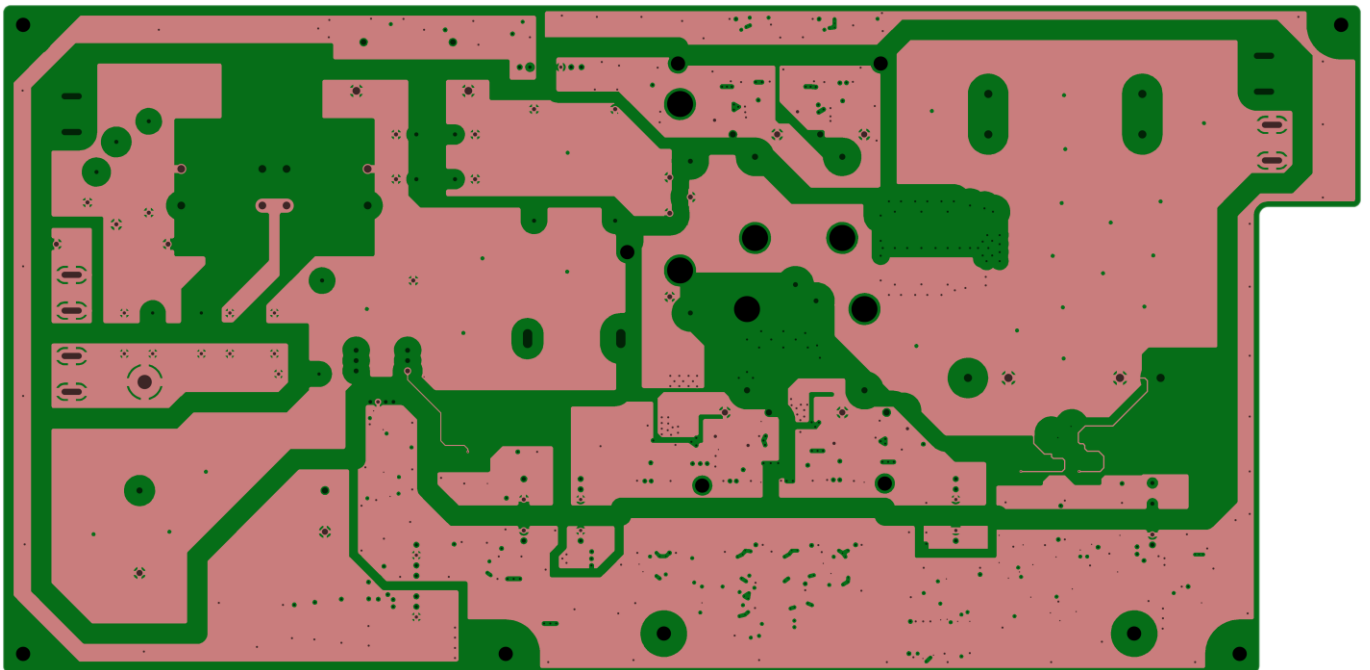
Fig. 3.1 shows the pattern diagram of the main circuit board of this power supply. It is also available in following files:

Main circuit board    RD172-LAYER-xx.pdf  
Control board        RD044-LAYER2-xx.pdf  
(xx is the revision number)

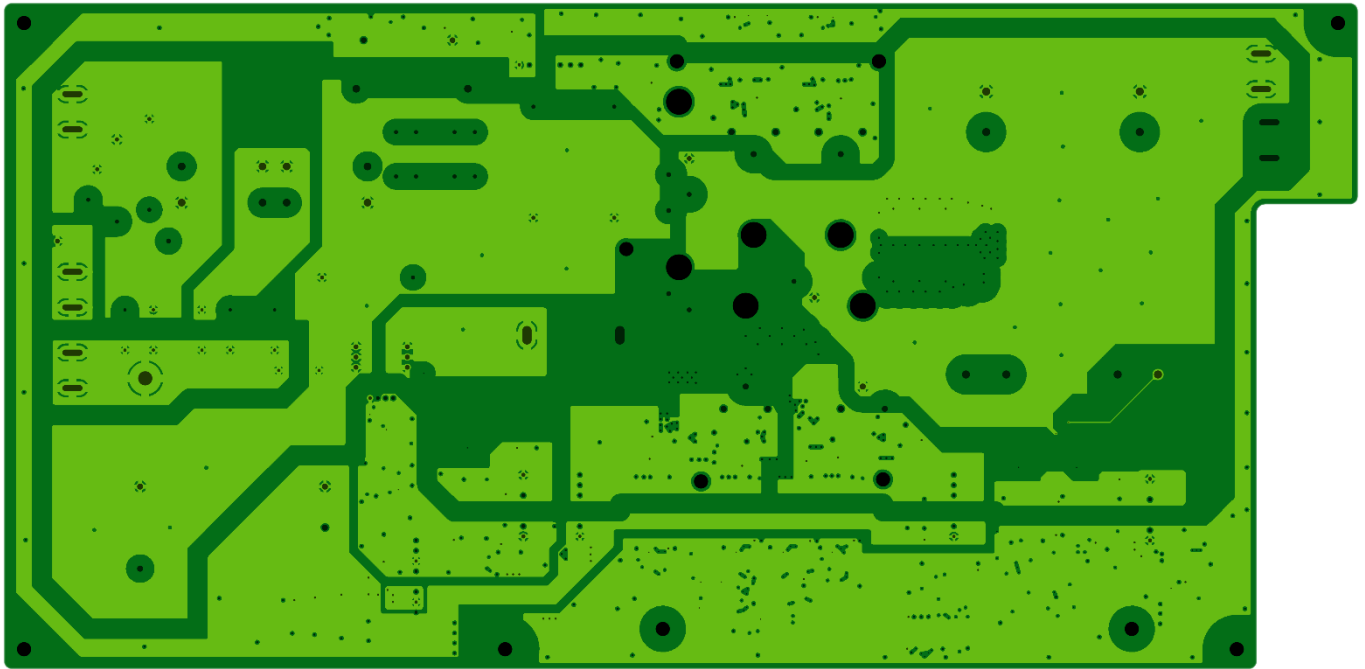




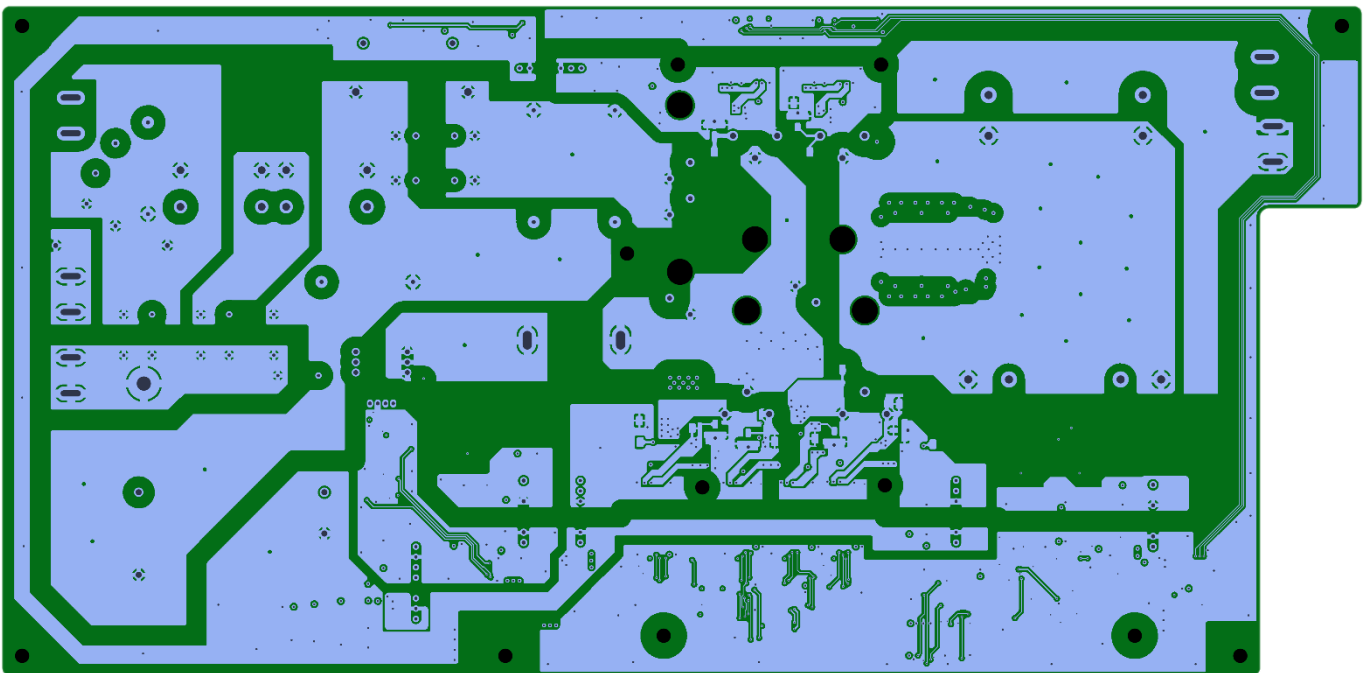
<Layer1 Front side, Front View>



<Layer2, Front View>



<Layer3, Front View>



<Layer4 Back side, Front View>

**Fig. 3.1 Pattern Diagram of 1.6 kW T-Type 3-Level PFC Power Supply (Main Circuit Board)**

## **4. Operating Procedure**

### **4.1. Connections**

Connect this power supply as follows.

- Properly ground the FG terminal (TB3).
- Connect the positive side of the load to the P terminal (TB4) and the negative side of the load to the N terminal (TB5).
- Connect a single-phase AC power supply to the L terminal (TB1) and N terminal (TB2).

### **4.2. Start and Stop Procedures**

Before starting up the power supply, check that all input terminals (L terminal, N terminal) and output terminals (P terminal, N terminal) are 0 V.

[Startup Procedure]

1. Turn on the input power supply (L terminal and N terminal).

[Stop Procedure]

1. Turn off the input power supply (L terminal and N terminal).

### **4.3. Evaluation Precautions (To Prevent Electric Shock, Burn Injury, etc.)**

Be careful of electric shock when connecting the power supply. Do not touch any component of the power supply directly while the power is on. Be very careful when observing the waveforms. Even after this power supply is stopped, there is a risk of electric shock due to the residual charge of various capacitors. Do not touch the board before confirming that the voltage of each component has decreased sufficiently.

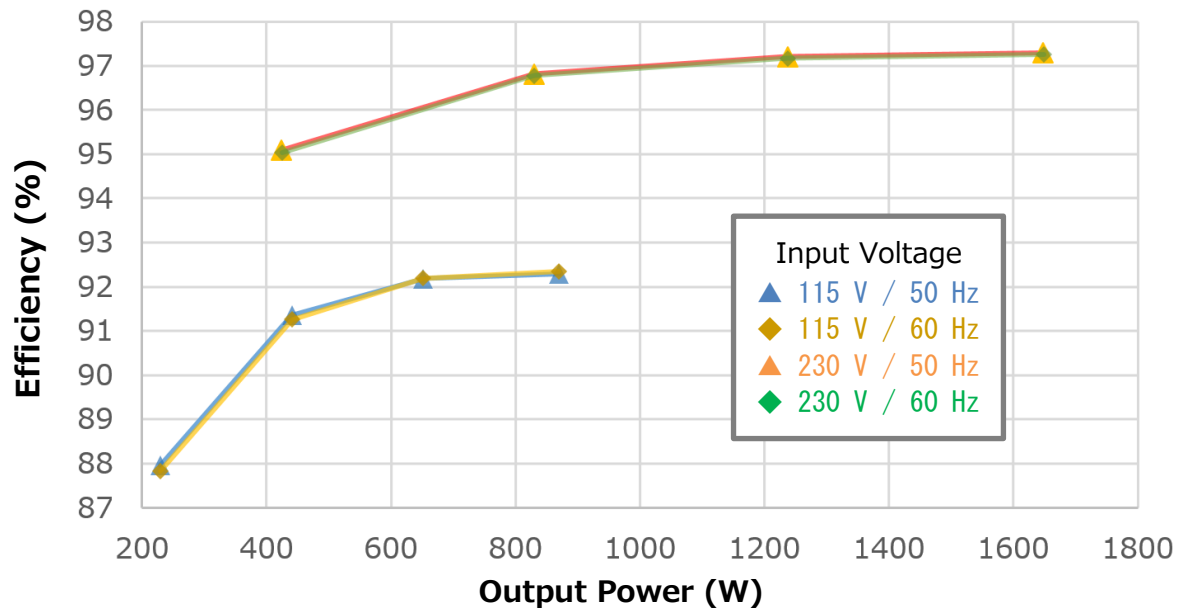
In addition, the semiconductor devices and inductor of this power supply generates heat according to the load current. Do not touch any component of the power supply while the power supply is in operation, as this can cause burns.

### 5. Power Characteristics

The efficiency measurement result of this power supply is described below.

#### 5.1. Efficiency

Fig. 4.1 shows the efficiency measurement result of this power supply. The efficiency is 97.3 % at an input voltage of 230 V and an output of 1600 W.



**Fig. 4.1 Efficiency Measurement Result**

## Terms of Use

This terms of use is made between Toshiba Electronic Devices and Storage Corporation ("We") and customers who use documents and data that are consulted to design electronics applications on which our semiconductor devices are mounted ("this Reference Design"). Customers shall comply with this terms of use. Please note that it is assumed that customers agree to any and all this terms of use if customers download this Reference Design. We may, at its sole and exclusive discretion, change, alter, modify, add, and/or remove any part of this terms of use at any time without any prior notice. We may terminate this terms of use at any time and for any reason. Upon termination of this terms of use, customers shall destroy this Reference Design. In the event of any breach thereof by customers, customers shall destroy this Reference Design, and furnish us a written confirmation to prove such destruction.

### 1. Restrictions on usage

1. This Reference Design is provided solely as reference data for designing electronics applications. Customers shall not use this Reference Design for any other purpose, including without limitation, verification of reliability.

2. This Reference Design is for customer's own use and not for sale, lease or other transfer.

3. Customers shall not use this Reference Design for evaluation in high or low temperature, high humidity, or high electromagnetic environments.

4. This Reference Design shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable laws or regulations.

### 2. Limitations

1. We reserve the right to make changes to this Reference Design without notice.

2. This Reference Design should be treated as a reference only. We are not responsible for any incorrect or incomplete data and information.

3. Semiconductor devices can malfunction or fail. When designing electronics applications by referring to this Reference Design, customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of semiconductor devices could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Customers must also refer to and comply with the latest versions of all relevant our information, including without limitation, specifications, data sheets and application notes for semiconductor devices, as well as the precautions and conditions set forth in the "Semiconductor Reliability Handbook".

4. When designing electronics applications by referring to this Reference Design, customers must evaluate the whole system adequately. Customers are solely responsible for all aspects of their own product design or applications. WE ASSUME NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.

5. No responsibility is assumed by us for any infringement of patents or any other intellectual property rights of third parties that may result from the use of this Reference Design. No license to any intellectual property right is granted by this terms of use, whether express or implied, by estoppel or otherwise.

6. THIS REFERENCE DESIGN IS PROVIDED "AS IS". WE (a) ASSUME NO LIABILITY WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (b) DISCLAIM ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO THIS REFERENCE DESIGN, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.

### 3. Export Control

Customers shall not use or otherwise make available this Reference Design for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). This Reference Design may be controlled under the applicable export laws and regulations including, without limitation, the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export Administration Regulations. Export and re-export of this Reference Design are strictly prohibited except in compliance with all applicable export laws and regulations.

### 4. Governing Laws

This terms of use shall be governed and construed by laws of Japan.