

Notes in use of the TB67S581FNG

Overview

The TB67S581FNG is a two-phase bipolar stepping motor driver IC using a PWM chopper. Fabricated with the BiCD process, the TB67S581FNG rating is 50 V / 2.5 A. The built-in regulator for IC operation allows the motor to be driven by a single VM power supply.

※ The contents described in the application note are reference for evaluating the product. Therefore, the contents described cannot be guaranteed. As for the detailed materials, please check the data sheet.

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1. Power supply voltage

1.1. Operating range of Power supply voltage

In use of the TB67S581FNG, voltage supply to the VM pin and the VREF pin is required. Even though the absolute maximum rating of the VM power supply voltage is 45V (in active), please use within the operating range: 8.2 to 44V. The slew rate at power on should be used 0.05 V/ μ s or less as a guideline.

The VREF pin should be used within the operating range: 1 to 3.6 V..

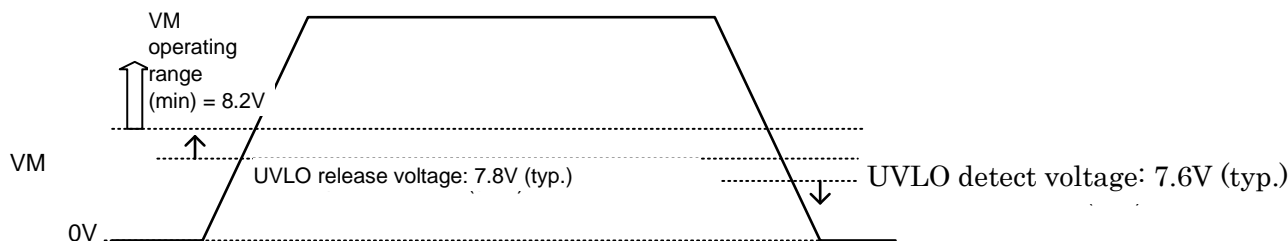


Figure 1.1 VM operating range and UVLO threshold

1.2. Sequence of power supply

Owing to the built-in regulator a single power supply drive is realized in this device. And owing to the built-in low power supply voltage detection (UVLO) malfunctions at low power supply voltage can be prevented.

When the voltage of VM is unstable at turning power on / off (transient area), it is recommended to turn off the motor operation. Please start motor operation by switching the input signal after the power supply voltage becomes stable. And likewise, it is recommended to turn off the power supply after the motor has stopped completely.

2. Output current

Please use this device within the operating range of the motor current: 2.0A or less (per phase). And the maximum current value that can be used actually is limited by application conditions (ambient environment temperature, the board wiring, heat dissipation path, etc.). Please set the optimum current value within the range that does not exceed the allowable loss under operating environment after thermal calculation and actual evaluation.

3. Control input

This device is configured that no electromotive force by the signal input is generated, even though the logic input signal is input during the VM voltage is not supplied. However, it is recommended to set the input signal low level before the power supply is turned on. As the logic input signal is specified as $V_{IN(H)} = 2.2V$ (min.) and $V_{IN(L)} = 0.7V$ (max.), this device can be also controlled with input signal of the 3.3V system. Pull-down resistors of 100 k Ω (typ.) are integrated.

3.1. Function description

3.1.1. SLEEP_X function

SLEEP_X is a function to switch the power saving mode and the normal operation mode. By setting the sleep mode once and then setting the normal operation mode again, it is possible to recover from the output forced OFF state caused by the operation of the overheat detection circuit (TSD) / over-current detection circuit (ISD). By setting SLEEP_X = Low, the power saving mode is set after 100 μs. After inputting SLEEP_X=High, the device returns to normal operation in 10 ms (max.).

Table 3.1 SLEEP_X functions

SLEEP_X	Function
L	Power saving mode (charge pump stopped, VCC Reg stopped)
H	Normal operation mode

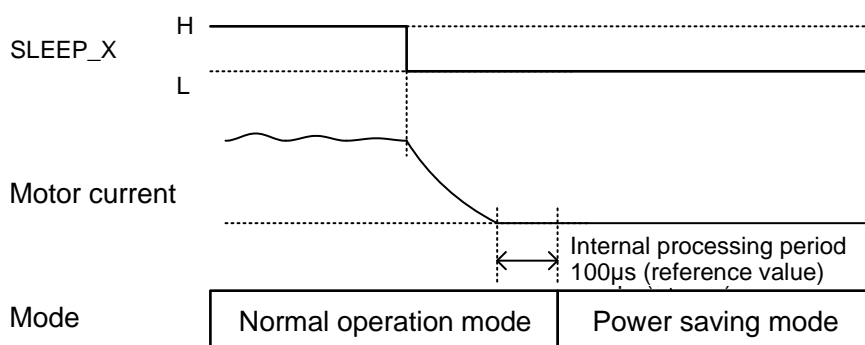


Figure 3.1 SLEEP_X Timing

(Note) Please control the SLEEP_X pin from a logic signal such as the MCU. In particular, when pull - up to a power supply such as 5V or 3.3V through a resistor, please adjust the rise time of the signal to less than 0.1ms.

3.1.2. CLK function

CLK is a function to advance electrical angle. Each CLK advances the electrical angle by one. The signal is captured at the Up edge.

Table 3.2 CLK function

CLK	Function
↑	Shift to the next step at the Up edge
↓	(Maintain the previous state)

3.1.3. ENABLE function

ENABLE is a function to switch the stepping motor drive ON / OFF. When the motor drive is turned ON, normal constant current control is started, and when it is set OFF, the MOSFETs are turned off and the outputs are high impedance.

Table 3.3 ENABLE function

ENABLE	Function
L	Output MOSFET operation: ON (normal operation)
H	Output MOSFET operation: OFF (operation stopped, high impedance)

3.1.4. CW/CCW function

CW / CCW is a function to switch the rotation direction of the stepping motor.

Table 3.4 CW/CCW function

CW/CCW	Function
H	Forward rotation (CW)
L	Reverse rotation (CCW)

3.1.5. DMODE0, DMODE1, and DMODE2 function

DMODE is a function to switch the step resolution. Switching excitation modes even while driving a motor is supported. After inputting DMODE data, the modes are switched at the Up edge of the next external CLK. After switching modes, it is shifted to the nearest electric angle in the direction of rotation.

Table 3.5 DMODE0, DMODE1, and DMODE2 function

DMODE0	DMODE1	DMODE2	Function
L	L	L	Full step (2-phase excitation) with 71 % current
L	L	H	Half step resolution (1-2-phase excitation)
L	H	L	Quarter step resolution (W1-2-phase excitation)
L	H	H	1 / 8 step resolution (2W1-2-phase excitation)
H	L	L	1 / 16 step resolution (4W1-2-phase excitation)
H	L	H	1 / 32 step resolution (8W1-2-phase excitation)
H	H	L	1 / 32 step resolution (8W1-2-phase excitation)
H	H	H	1 / 32 step resolution (8W1-2-phase excitation)

3.1.6. Decay function

The Decay mode in constant current control is selectable by the state of the DECAY pin. In Mixed decay mode, the IC enters to the Fast decay mode. Then, the IC transmits from Fast decay mode to slow decay mode at a fixed period (75 % of PWM cycle). Moreover, after the remainder of the fixed PWM period (25 % of the PWM cycle), the slow decay mode ends. Mixed decay mode occurs only if the output current is decreasing; if the current is increasing, decay operation is forced slow mode.

Table 3.6 Mixed Decay function

DECAY1 pin	Function
L	Slow Decay only
H	Fast Decay only
OPEN	Mixed Decay

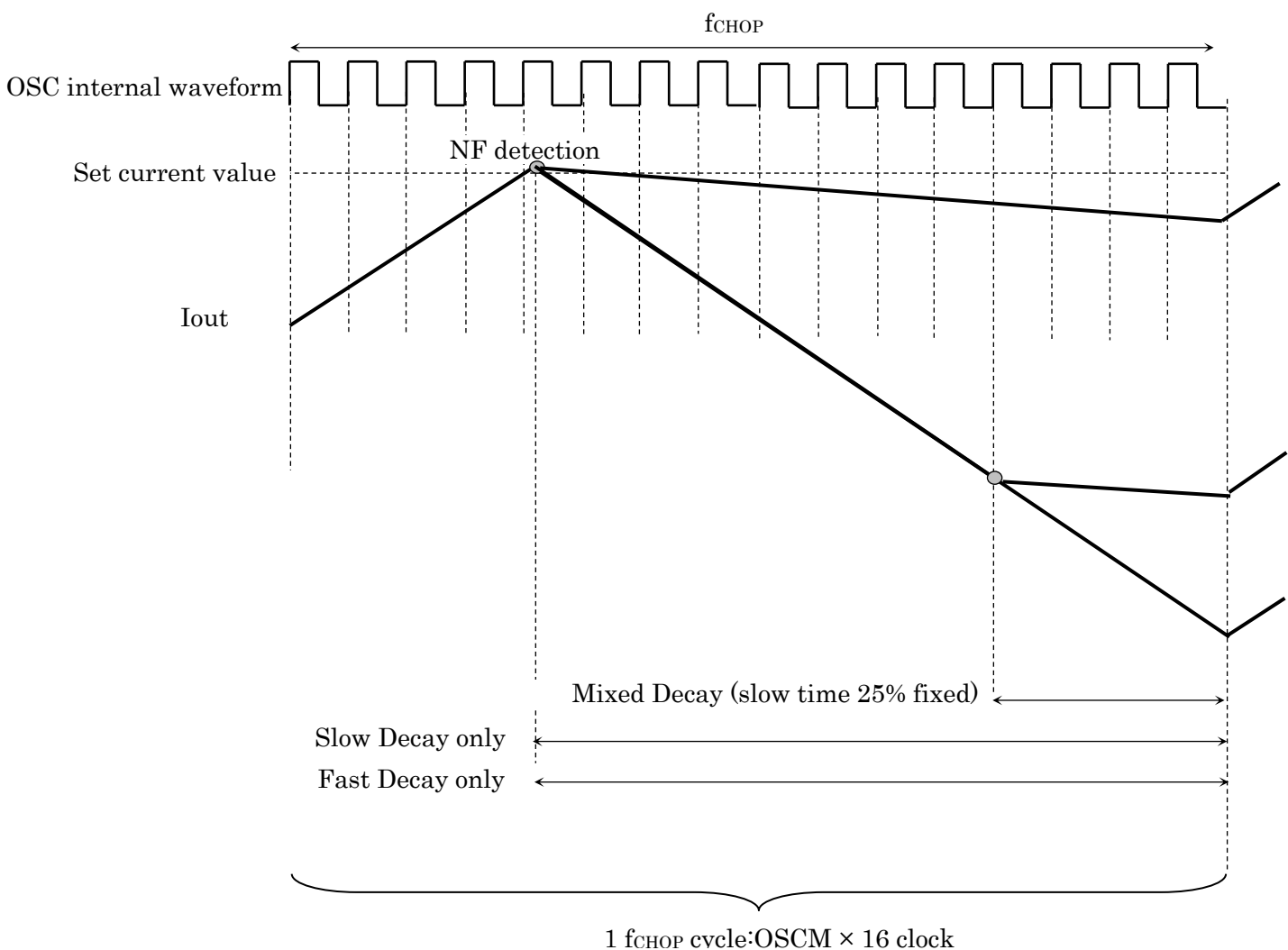


Figure 3.2 Mixed Decay Timing

Note: The timing chart is partially omitted or simplified to explain functions and operation.

3.1.7. RESET function

RESET is a function to initialize the internal electrical angle.

Table 3.7 ESET function

RESET	Function
L	Electrical angle initialization
H	Normal operation

The currents of each phase when the RESET pin is set high are as follows.

Table 3.8 Initial electrical angle after reset of electrical angle

Excitation mode	A-phase current	B-phase current	Initial electrical angle
Full step resolution setting	71%	71%	45°
Half step resolution setting	71%	71%	45°
Quarter step resolution setting	71%	71%	45°
1/8 step resolution setting	71%	71%	45°
1/16 step resolution setting	71%	71%	45°
1/32 step resolution setting	71%	71%	45°
Full step resolution setting	71%	71%	45°

3.1.8. MO function

MO is a function to confirm the internal electric angle. Please connect the output of the MO pin to the potential of 3.3V or 5V with a pull-up resistor of 10 k to 100 kΩ.

Table 3.9 MO function

MO	Function
H (Pull-up)	Other than the initial electrical angle
L	The initial electrical angle

3.1.9. LO (error detection flag output) function

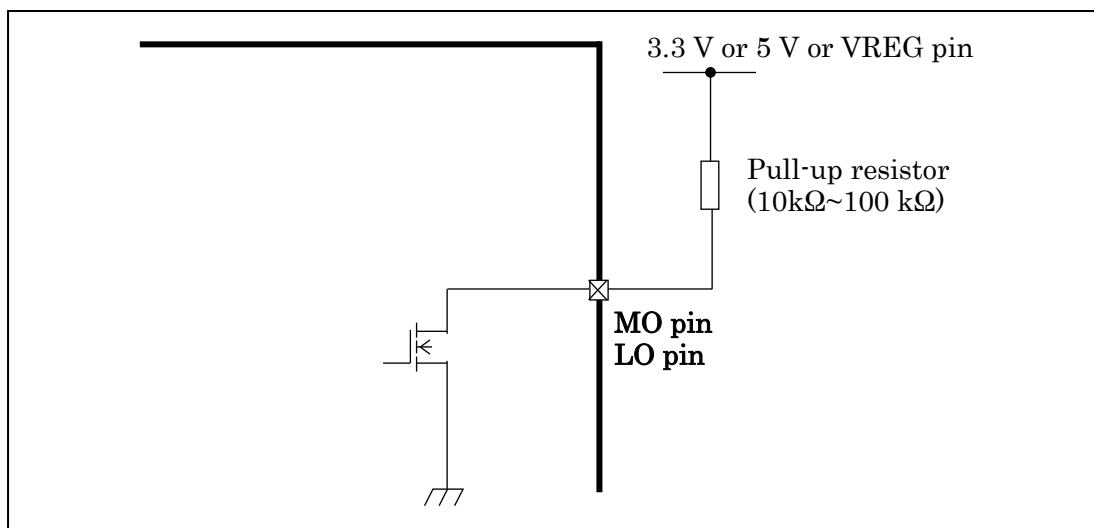
LO is a function to output a signal to the outside when the error detection functions start operation. In use of this function, as the LO pin and are an open drain type those outputs should be connected to the potential of 3.3V or 5V with a pull-up resistor of 10 k to 100 kΩ respectively.

In normal operation, the output level of the LO pin is Hi-Z (internal MOSFETs are OFF). When error detection function (over-temperature (TSD) or over-current (ISD)) starts operation, the output level of the pin is Low (internal MOSFETs are ON).

When the VM power supply is turned on again or error detections are canceled by the sleep mode, the LO pin is returned to "the normal state (normal operation)" again. In case of no use of the LO pins, please leave those pins open.

Table 3.10 LO function

LO pin output	Function
H (Pull-up)	Normal status (Normal operation)
L	Status of over-current (ISD) or over-temperature (TSD) detected



The equivalent circuit may be partially omitted or simplified to explain the circuit.

* Notes in using the LO pins

As a system of this device, the internal regulator is stopped for power saving when SLEEP_X = L. When SLEEP_X = H, the internal regulator is started up, but since there is an internal undefined period before rise up, the internal Logic block is initialized again for stable operation. Therefore, LO is Low for a moment at start-up. When the LO is monitored all the time, please mask the signal for 100μs (max) at switching SLEEP_X from Low to High.

4. Constant current control

4.1. The calculation formula of current value setting

In the TB67S539FTG the constant current value is set only by the input voltage value of the VREF pin without using an external current detection resistor. Setting current value: IOUT (max) is calculated by the following formula.

$$I_{OUT} = V_{REF} / 5 / R_{RS}$$

Example: $V_{REFA} = V_{REFB} = 1.0 \text{ V}$, $R_{SA}, R_{SB} = 0.22 \text{ } \Omega$ $I_{OUT} = 0.91 \text{ A}$

4.2. Correction of output setting current

In use of this device under conditions other than $I_{OUT} = 1.0\text{A}$, 0.91A , 0.82A , an error caused by the circuit off-set can be reduced by using the correction coefficient.

$$V_{REF} = I_{OUT} \times R_{SS} \times 5 \times \text{Correction factor}$$

Table 4.1 VREF correction factor

IOUT(A)	Correction factor
0.09	0.77
0.18	0.78
0.27	0.85
0.36	0.88
0.45	0.88
0.55	0.91
0.64	0.97
0.73	0.98
0.82	1.00
0.91	1.00
1.00	1.00
1.09	1.01
1.18	1.02
1.27	1.03
1.36	1.03

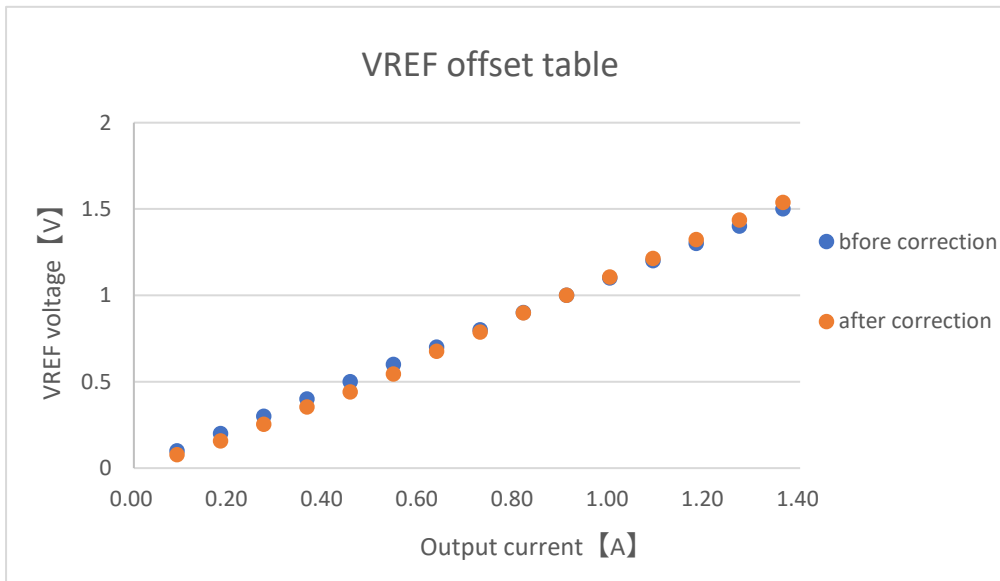


Figure 4.1 VREF offset table

Note: The graph above is not a guaranteed value, but a reference one.

5. Fault detection circuit

▪ Over-temperature detection circuit (TSD)

This function turns off the IC operation temporarily when the over heat of the device is detected. It has a dead band time to avoid error detection occurred by the external noise. When over heat is detected, all channels are turned off. Since the temperature has a hysteresis range, when the junction temperature falls to the return temperature, the operation returns automatically to the normal operation.

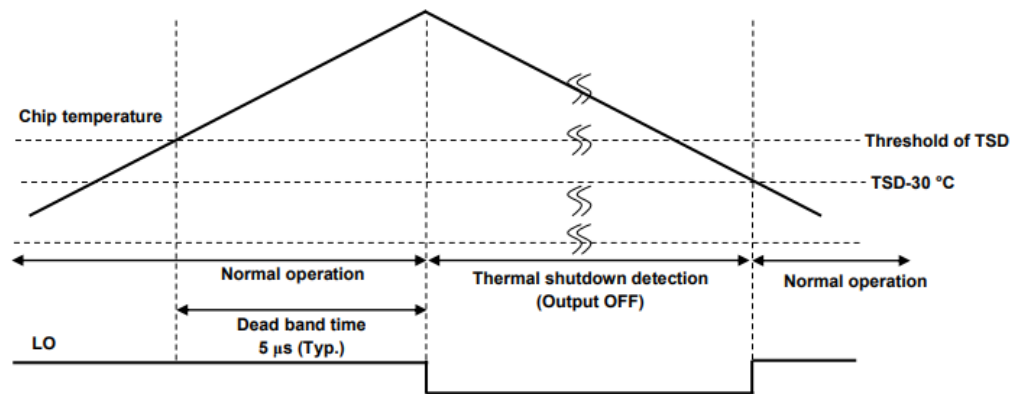


Figure 5.1 Thermal shutdown detection timing chart 1

Timing charts may be simplified for explanatory purposes. The value in the timing chart is the reference value.

TSD function has a detection deadband time. When operating near the TSD detection threshold temperature, when the temperature of the IC falls below the TSD detection threshold immediately after the dead band time ends, normal operation is immediately performed and the LO signal is output momentarily (156 to 312 ns (Typ.)).

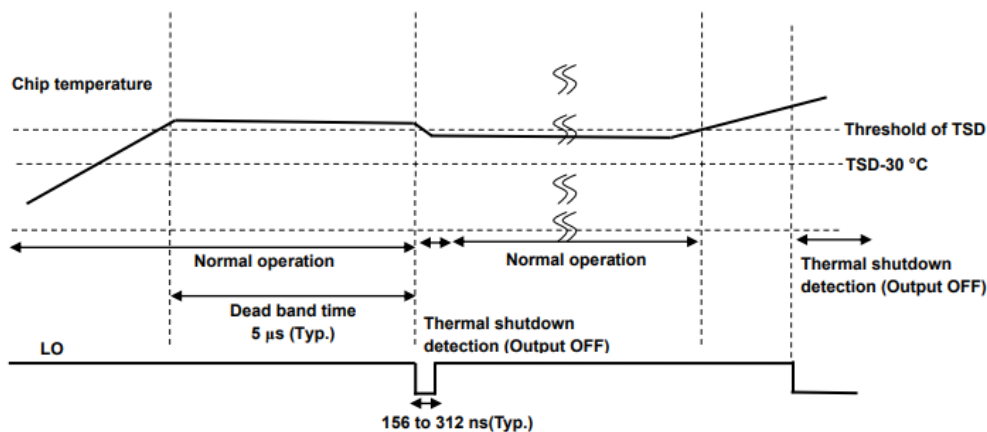


Figure 5.2 Thermal shutdown detection timing chart 2

Timing charts may be simplified for explanatory purposes. The value in the timing chart is the reference value.

▪ Low voltage detection circuit (UVLO)

When the voltage applied to the VM pin reaches 7.6 V (typ.) or less, the internal detection circuit starts operation and puts the output section off state. After the UVLO operation, it is canceled by setting the voltage applied to the VM pin to 7.8 V (typ.) or higher.

• **Over-current detection circuit (ISD)**

This function turns off the IC operation temporarily when the short-circuiting between outputs and the short-circuiting to the power supply or ground occur. It has a dead band time to avoid error detection occurred by the spike current which generates in switching and the external noise. When over current is detected, not only the corresponding channels but both channels are turned off.

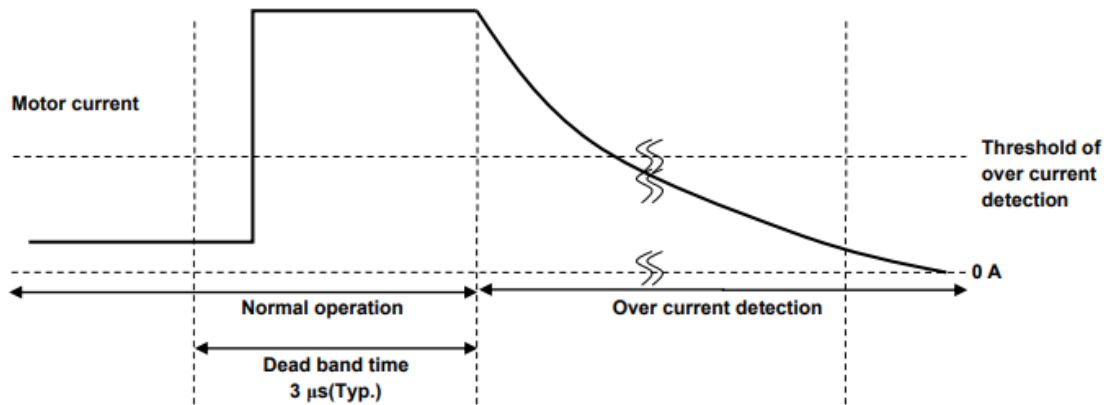


Figure 5.3 Over current detection timing chart

Timing charts may be simplified for explanatory purposes. The value in the timing chart is the reference value.

6. Power consumption of the device

The power consumed in the device can be divided into the power consumed by the output transistors and the power consumed by the internal circuit including the logic section.

$$P_D = P_D(\text{out}) + P_D(\text{bias})$$

- **Power consumption of output transistors**

The power consumption ($P_D(\text{out})$) of the output transistor section is consumed in the high-side and low-side transistors in the H-bridge.

$$P_D(\text{out}) = \text{Number of H-bridges} \times I_{\text{out}}(\text{A}) \times V_{\text{DS}}(\text{V}) = 2(\text{ch}) \times I_{\text{out}}(\text{A}) \times I_{\text{out}}(\text{A}) \times R_{\text{on}}(\Omega) \dots\dots (1)$$

When the current waveform of the motor output is assumed as the ideal one (two-phase excitation / square wave), the average power of the output section can be calculated as follows.

When $R_{\text{on}} = 0.4\Omega$, $I_{\text{out}}(\text{peak: Max}) = 1.0 \text{ A}$, $V_M = 24\text{V}$, it can be calculated as follows.

$$P_D(\text{out}) = 2(\text{ch}) \times 1.0(\text{A}) \times 1.0(\text{A}) \times 0.4(\Omega) = 0.8(\text{W}) \dots\dots\dots (2)$$

- **Power consumption of Logic block and IM system**

The power consumption $P_D(\text{bias})$ of the logic block and IM system is calculated separately during operation and stop.

- $I(\text{IM2}) = 5.0 \text{ mA (typ.): During operation}$
- $I(\text{IM1}) = 10 \mu\text{A (typ.): During stopped}$

The output system is connected to the V_M (12V). (Output system: the sum of the current consumed by the circuit connected to the V_M and the current consumed by switching the output stage)

The power consumption can be calculated as follows.

$$P_D(\text{bias}) = 24(\text{V}) \times 0.005(\text{A}) = 0.12(\text{W}) \dots\dots\dots (3)$$

- **Power consumption**

The total power consumption P_D can be calculated from the values in formulas (2) and (3) as follows.

$$P_D = P_D(\text{out}) + P_D(\text{bias}) = 0.8 + 0.12 = 0.92(\text{W})$$

The power consumption when the motor is not operating (when stopped) can be calculated as follows.

$$P_D = 24(\text{V}) \times 0.00001(\text{A}) = 0.0024(\text{W})$$

And power consumption can be reduced by stopping the operation by using the standby mode.

$$I_{\text{M1}} = 20\mu\text{A (max)}$$

In addition, in actual motor operation, the average current will be lower than the calculated value due to the transition time of the current step and the ripple caused by constant current PWM. However, referring to the above calculated values, please perform the thermal design of the board, etc. with a margin after a sufficient evaluation of the assembled board.

7. Application circuit example

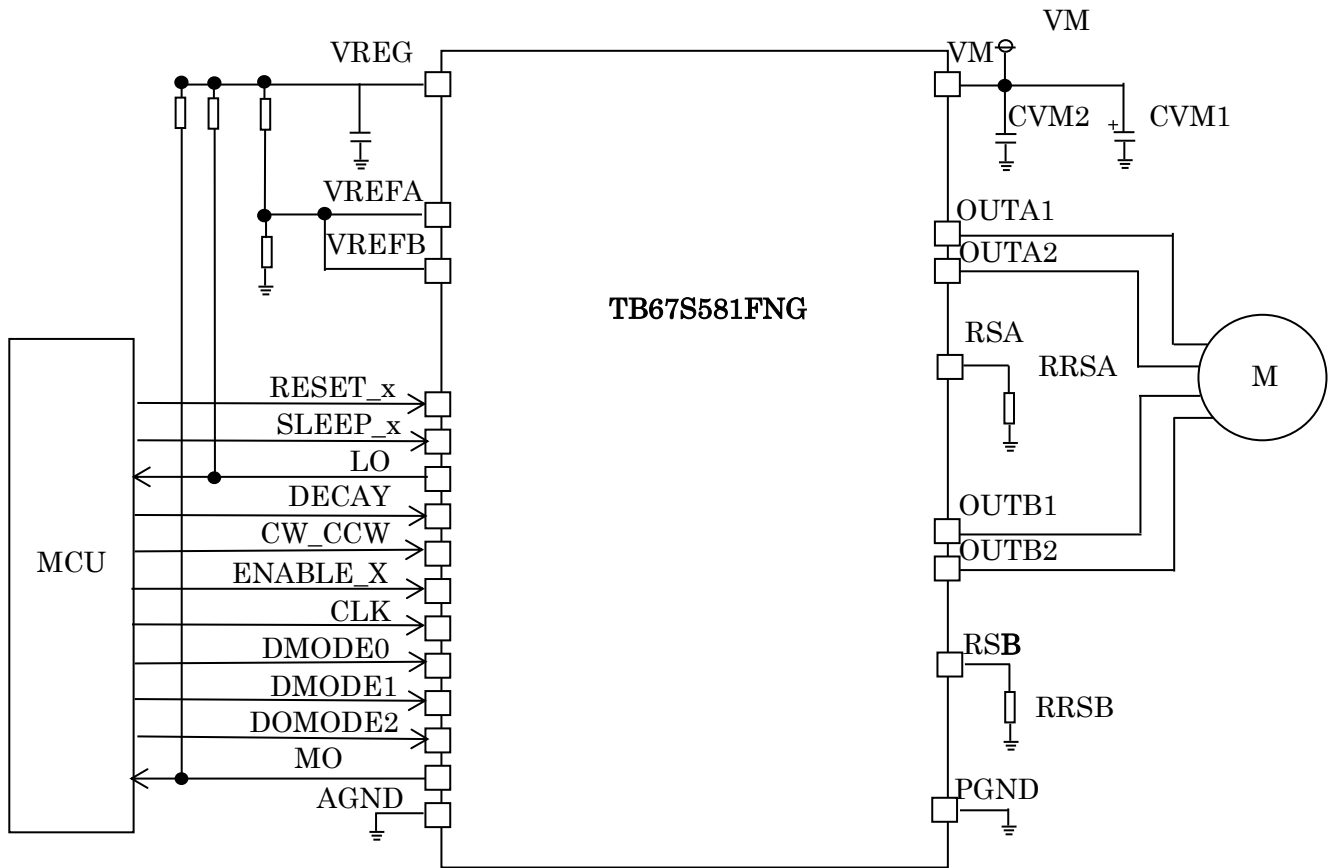


Figure 7.1 Application circuit example

The application circuit example may be simplified or some parts of them may be omitted for explanatory purposes.

7.1. Capacitor for power supply pins

Please connect a capacitor with an appropriate value to each pin for stabilizing the power supply voltage applied to the device and rejecting noises. And it is recommended to connect the capacitor as close to the device as possible. In particular, locating a ceramic capacitor close to the device is effective in suppressing fluctuations of high-frequency power supply and noise.

Table 7.1 Recommended values of capacitor at power supply pins

Item	Parts	Typical value	Recommended value range
Between VM and GND	Electrolytic capacitor	100 μ F	47 to 100 μ F
	Ceramic capacitor	0.1 μ F	0.01 to 1 μ F
Between VREF and GND	Ceramic capacitor	0.1 μ F	0.01 to 1 μ F

* Please consider connecting a capacitor between VREF and GND in accordance with application and environment.

* It is also possible to omit each part or use a capacitor other than the recommended value depending on the motor load conditions and PCB pattern layout.

7.2. sense resistor for current detection

The set current value during constant-current PWM control can be determined by the reference voltage (VREF) and sense resistor value for current detection.

Table 7.2 Recommended values of SENSE resistor for current detection

Item	Part	Typical	Recommended Range
RSA/RSB-GND	SMT resistor	0.22 Ω	0.1 to 0.51 Ω

When using this product, please make sure that the voltage generated by the current value to be used and the value of sense resistor for current detection does not exceed the VR detection voltage in the operating range.

7.3. Wiring pattern for power supply / GND

In this device, a large current is assumed to flow through PCB patterns especially of VM, AGND, PGND_x, OUT_x+, OUT_x- (x = A or B)). Please secure a sufficient wiring pattern in order not to be affected by the wiring impedance, etc. As the TB67S539FTG is surface mount package type, it is extremely important to dissipate heat from the heat sink on the back side of the package to the GND pattern of the board, therefore please design pattern in sufficient consideration of the thermal design.

7.4. Hughes

Please insert an appropriate fuse into the power supply line before use in order to prevent a continue large current flow in the event of an over-current or the device failure. The device may be destroyed caused by usage exceeding the absolute maximum rating, incorrect wiring, and abnormal pulse noise induced by wiring or load. As a result, a continues large current flow through the device might lead to smoke or ignition. Assuming a large current inflow and outflow cause by the device destruction, appropriate settings of fuse such as capacity, blow time, and insertion circuit location are required to minimize the effects.

In this device, the over-current detection circuit (ISD) that detects when an excessive current flows through the outputs and turns the outputs off is a built-in, but it is not guaranteed that the device is protected under all conditions. Please release the over-current state immediately after the fault detection circuit operates. Depending on the usage and conditions such as exceeding the absolute maximum ratings, the over-current detection circuit may not operate normally or the device may be damaged before the ISD operates. In addition, when over-current continues to flow, the device may be destroyed caused by heat generation

depending on the usage and conditions. A secondary destruction is concerned when the over-current state continues, and non-operation of the ISD is concerned depending on the output load conditions because a dead zone period is provided in order to prevent malfunction caused by noise.

As a conclusion, considering the case of emergency please use a fuse in the power supply to prevent abnormal state from continuing.

8. PCBA design dimensions for reference

P-HTSSOP28-0510-0.65-001

Unit: mm

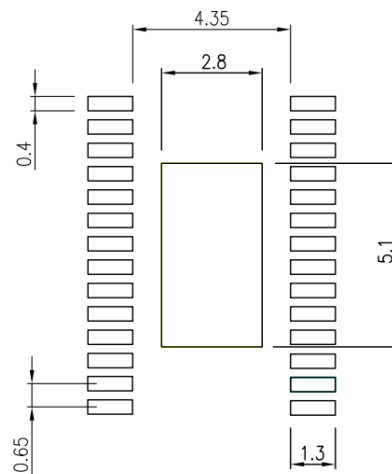


Figure 8.1 PCBA design dimensions for reference

Notes

- All linear dimensions are given in millimeters unless otherwise specified.
- This drawing is based on JEITA ET-7501 Level3 and should be treated as a reference only. TOSHIBA is not responsible for any incorrect or incomplete drawings and information.
- You are solely responsible for all aspects of your own land pattern, including but not limited to soldering processes.
- The drawing shown may not accurately represent the actual shape or dimensions.
- Before creating and producing designs and using, customers must also refer to and comply with the latest versions of all relevant TOSHIBA information and the instructions for the application that Product will be used with or for.

Note on Contents

1. Block Diagram

Some of the functional blocks, circuits, or constants in the block diagram may be omitted or simplified for explanatory purposes.

2. Equivalent Circuit

The equivalent circuit may be partially omitted or simplified to explain the circuit.

3. Timing Charts

Timing charts may be simplified for explanatory purposes.

4. Application Circuit Example

The application circuits shown in this document are provided for reference purposes only. Thorough evaluation is required, especially at the mass production design stage.

Providing these application circuit examples does not grant a license for industrial property rights.

IC Usage Considerations

Notes on handling of ICs

- (1) The absolute maximum ratings of a semiconductor device are a set of ratings that must not be exceeded, even for a moment. Do not exceed any of these ratings.
Exceeding the rating(s) may cause the device breakdown, damage or deterioration, and may result injury by explosion or combustion.
- (2) Do not insert devices in the wrong orientation or incorrectly.
Make sure that the positive and negative terminals of power supplies are connected properly.
Otherwise, the current or power consumption may exceed the absolute maximum rating, and exceeding the rating(s) may cause the device breakdown, damage or deterioration, and may result injury by explosion or combustion.
In addition, do not use any device that is applied the current with inserting in the wrong orientation or incorrectly even just one time.
- (3) Use an appropriate power supply fuse to ensure that a large current does not continuously flow in case of over-current and/or IC failure. The IC will fully break down when used under conditions that exceed its absolute maximum ratings, when the wiring is routed improperly or when an abnormal pulse noise occurs from the wiring or load, causing a large current to continuously flow and the breakdown can lead smoke or ignition. To minimize the effects of the flow of a large current in case of breakdown, appropriate settings, such as fuse capacity, fusing time and insertion circuit location, are required.
- (4) If your design includes an inductive load such as a motor coil, incorporate a protection circuit into the design to prevent device malfunction or breakdown caused by the current resulting from the inrush current at power ON or the negative current resulting from the back electromotive force at power OFF. IC breakdown may cause injury, smoke or ignition.
Use a stable power supply with ICs with built-in protection functions. If the power supply is unstable, the protection function may not operate, causing IC breakdown. IC breakdown may cause injury, smoke or ignition.
- (5) Carefully select external components (such as inputs and negative feedback capacitors) and load components (such as speakers), for example, power amp and regulator.
If there is a large amount of leakage current such as input or negative feedback condenser, the IC output DC voltage will increase. If this output voltage is connected to a speaker with low input withstand voltage, over-current or IC failure can cause smoke or ignition. (The over-current can cause smoke or ignition from the IC itself.) In particular, please pay attention when using a Bridge Tied Load (BTL) connection type IC that inputs output DC voltage to a speaker directly.

Points to remember on handling of ICs

Over-current detection circuit

Over-current detection circuits (referred to as current limiter circuits) do not necessarily protect ICs under all circumstances. If the over-current protection circuits operate against the over-current, clear the over-current status immediately.

Depending on the method of use and usage conditions, such as exceeding absolute maximum ratings can cause the over-current protection circuit to not operate properly or IC breakdown before operation. In addition, depending on the method of use and usage conditions, if over-current continues to flow for a long time after operation, the IC may generate heat resulting in breakdown.

Thermal shutdown circuit

Thermal shutdown circuits do not necessarily protect ICs under all circumstances. If the thermal shutdown circuits operate against the over temperature, clear the heat generation status immediately.

Depending on the method of use and usage conditions, such as exceeding absolute maximum ratings can cause the thermal shutdown circuit to not operate properly or IC breakdown before operation.

Heat radiation design

In using an IC with large current flow such as power amp, regulator or driver, please design the device so that heat is appropriately radiated, not to exceed the specified junction temperature (T_j) at any time and condition. These ICs generate heat even during normal use. An inadequate IC heat radiation design can lead to decrease in IC life, deterioration of IC characteristics or IC breakdown. In addition, please design the device taking into consideration the effect of IC heat radiation with peripheral components.

Back-EMF

When a motor reverses the rotation direction, stops or slows down abruptly, a current flow back to the motor's power supply due to the effect of back-EMF. If the current sink capability of the power supply is small, the device's motor power supply and output pins might be exposed to conditions beyond absolute maximum ratings. To avoid this problem, take the effect of back-EMF into consideration in system design.

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