

High-Power Module Silicon Carbide N-Channel MOSFET, Silicon Carbide SBD

# MG800FXF1JMS3

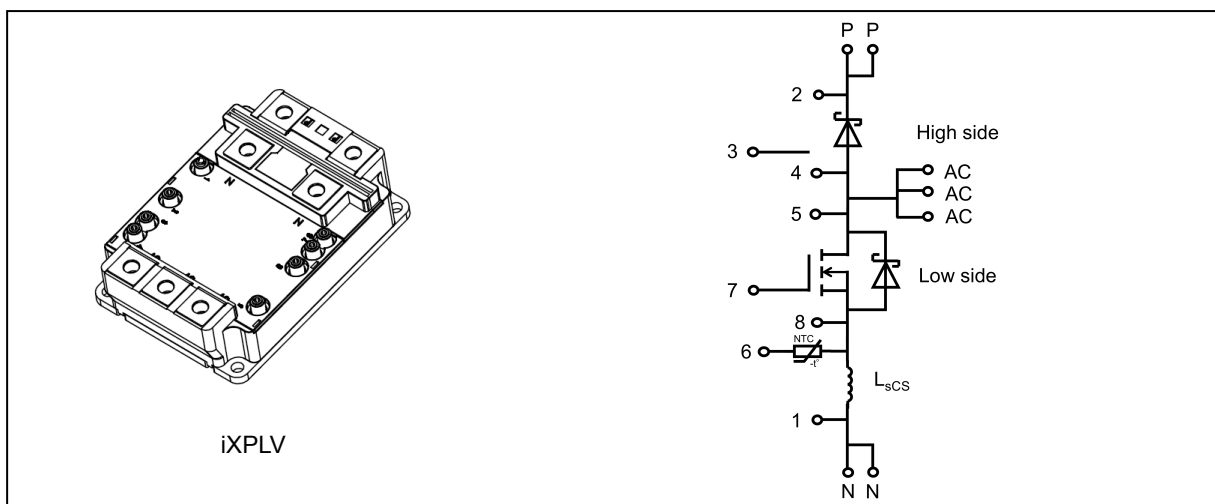
## 1. Applications

- High-Power Switching
- Motor Controllers (including rail traction)

## 2. Features

- (1)  $V_{DSS} = 3300\text{ V}$ ,  $I_D = 800\text{ A}$  All SiC MOSFET Module(Low loss & High speed switching)
- (2) This module is equipped with SiC SBD on the high side and SiC MOSFET on the low side.
- (3) Low stray inductance, low thermal resistance, maximum  $T_{ch} = 175\text{ }^\circ\text{C}$
- (4) New generation standard package(Compact & easily handled by paralleling)
- (5) Electrodes are isolated from metal base plate.

## 3. Packaging and Internal Circuit



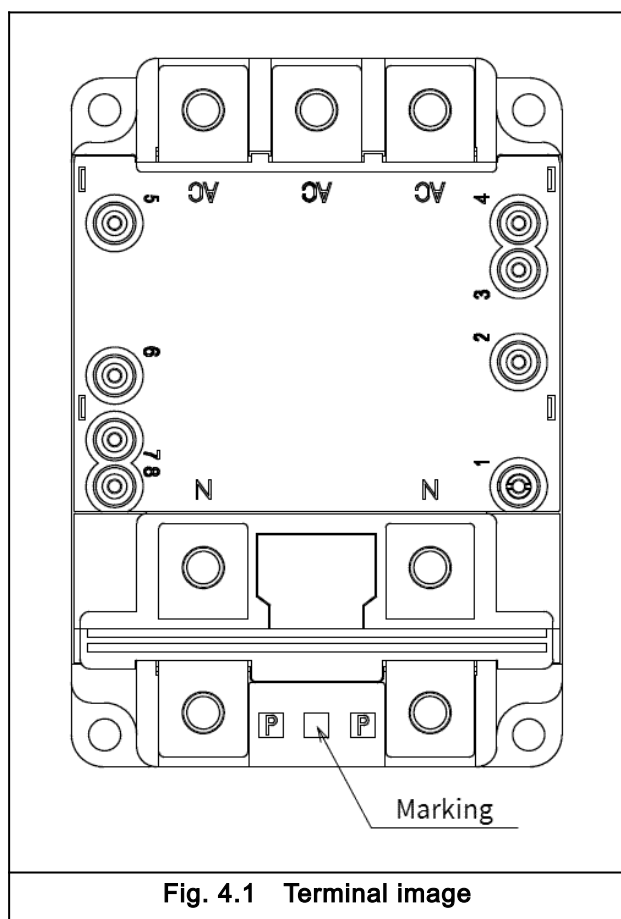
Note : P and N terminal should use two screws fastened in each and AC terminal should use three screws fastened.

Start of commercial production  
2023-10

## 4. Terminal

Terminal No.	Connection
P	P (main terminal)
N	N (main terminal)
AC	AC (main terminal)
1	N (sense) / Current sense
2	P (sense)
3	Non Connection
4	High side anode sense
5	AC (sense)
6	thermistor
7	Low side gate
8	low side source sense

Marking	High side	Low side
C	SBD	MOSFET



**Fig. 4.1 Terminal image**

### 5. Absolute Maximum Ratings (Note,Note 1)

#### 5.1. MOSFET Absolute Maximum Ratings ( $T_c = 25^\circ\text{C}$ , unless otherwise specified)

Characteristics	Symbol	Note	Test Condition	Rating	Unit
Drain-source voltage	$V_{DSS}$			3300	V
Gate-source voltage	$V_{GSS}$			+ 25 / - 10	V
Drain current (DC)	$I_D$	(Note 2)		800	A
Drain current (pulsed)	$I_{DP}$	(Note 2)		1600	A
Drain power dissipation	$P_D$	(Note 2)		4680	W
Source current (DC)	$I_S$	(Note 2)		800	A
Source current (pulsed)	$I_{SP}$	(Note 2)		1600	A
Channel temperature	$T_{ch}$			175	$^\circ\text{C}$

#### 5.2. SBD Absolute Maximum Ratings ( $T_c = 25^\circ\text{C}$ , unless otherwise specified)

Characteristics	Symbol	Note	Test Condition	Rating	Unit
Repetitive peak reverse voltage	$V_{RRM}$			3300	V
Diode forward current (DC)	$I_F$		$T_j = -40$ to $175^\circ\text{C}$	800	A
Diode forward current (pulsed)	$I_{FP}$		$T_j = -40$ to $150^\circ\text{C}$	1600	A
			$T_j = 150$ to $175^\circ\text{C}$	800	A
Junction temperature	$T_j$			175	$^\circ\text{C}$

#### 5.3. Absolute Maximum Ratings (MOSFET,SBD common) ( $T_c = 25^\circ\text{C}$ , unless otherwise specified)

Characteristics	Symbol	Note	Test Condition	Rating	Unit
Storage temperature	$T_{stg}$			-40 to 150	$^\circ\text{C}$
Isolation voltage	$V_{isol}$		AC , 60 s	6000	V
Mounting torque	TOR	(Note 3)	Main terminal : M8	9.1	N · m
		(Note 4)	Signal terminal : M3	1.0	N · m
		(Note 5)	Mounting : M6	5.2	N · m

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Refer to the application notes.

Note 2: Ensure tht th channel temperature does not exceed  $175^\circ\text{C}$ .

Note 3: The recommended tightening torque for the main terminal (M8) is  $7.0 \text{ N} \cdot \text{m}$ .

Note 4: The recommended tightening torque for the signal terminal (M3) is  $0.8 \text{ N} \cdot \text{m}$ .

Note 5: The recommended tightening torque for installation (M6) is  $4.0 \text{ N} \cdot \text{m}$ .

### 6. Thermal Chracteristics

Characteristics	Symbol	Note	Min	Typ.	Max	Unit
Thermal resistance (channel-to-case)	$R_{th(ch-c)}$		—	—	0.032	K/W
Thermal resistance (junction-to-case)	$R_{th(j-c)}$		—	—	0.04	K/W
Thermal resistance (case-to-fin)	$R_{th(c-f)}$	(Note)	—	0.0026	—	K/W

Note: The value per module.

Apply  $50 \mu\text{m}$  of  $3 \text{ W/m} \cdot \text{K}$  grease between the case and fin while taking care not to create a void, and tighten to the recommended torque before use.

### 7. Electrical Characteristics

#### 7.1. MOSFET Electrical Characteristics (T<sub>c</sub> = 25°C, unless otherwise specified)

Characteristics	Symbol	Note	Test Condition	Min	Typ.	Max	Unit	Fig.
Gate-source leakage current	I <sub>GSS</sub>		V <sub>GS</sub> = +25 V / -10 V, V <sub>DS</sub> = 0 V	—	—	±100	nA	—
Drain-source cut-off current	I <sub>DSS</sub>		V <sub>DS</sub> = 3300V, V <sub>GS</sub> = 0 V	—	—	1	mA	—
Gate threshold voltage	V <sub>th</sub>	(Note 1)	I <sub>D</sub> = 0.8 A, V <sub>DS</sub> = 10 V	—	4.8	—	V	—
Drain-source on-voltage (sense)	V <sub>DS(on)</sub> sense		I <sub>D</sub> = 800 A, V <sub>GS</sub> = + 20 V, T <sub>ch</sub> = 25 °C	—	1.3	—	V	—
			I <sub>D</sub> = 800 A, V <sub>GS</sub> = + 20 V, T <sub>ch</sub> = 175 °C	—	3.6	5.2	V	—
Drain-source on-voltage (terminal)	V <sub>DS(on)</sub> terminal		I <sub>D</sub> = 800 A, V <sub>GS</sub> = + 20 V, T <sub>ch</sub> = 25 °C	—	1.4	—	V	—
Input capacitance	C <sub>iss</sub>		V <sub>DS</sub> = 1800 V, V <sub>GS</sub> = 0 V, f = 100 kHz	—	173	—	nF	—
Internal gate resistance	r <sub>ig</sub>		f = 1 MHz	—	2.75	—	Ω	—
Switching time (turn-on delay time)	t <sub>d(on)</sub>	(Note 2)	Inductive load, V <sub>DD</sub> = 1800 V, I <sub>D</sub> = 800 A, V <sub>GS</sub> = + 20 V / - 6 V, R <sub>G(on)</sub> = 1.5 Ω, R <sub>G(off)</sub> = 3.6 Ω, C <sub>GS</sub> = 100nF, T <sub>ch</sub> = 175 °C, L <sub>S</sub> ≈ 70 nH	—	0.58	—	μs	7.1 7.2
Switching time (rise time)	t <sub>r</sub>			—	0.17	—	μs	
Switching time (turn-on time)	t <sub>on</sub>			—	0.75	—	μs	
Switching time (turn-off delay time)	t <sub>d(off)</sub>			—	1.80	—	μs	
Switching time (fall time)	t <sub>f</sub>			—	0.15	—	μs	
Switching time (turn-off time)	t <sub>off</sub>			—	1.95	—	μs	
Turn-on switching loss	E <sub>on</sub>			—	230	—	mJ	
Turn-off switching loss	E <sub>off</sub>			—	230	—	mJ	
Source-drain on-voltage (sense)	V <sub>SD(on)</sub> sense		I <sub>S</sub> = 800 A, V <sub>GS</sub> = + 20 V, T <sub>ch</sub> = 25 °C	—	1.3	—	V	—
			I <sub>S</sub> = 800 A, V <sub>GS</sub> = + 20 V, T <sub>ch</sub> = 175 °C	—	3.5	5.1	V	—
Source-drain on-voltage (terminal)	V <sub>SD(on)</sub> terminal		I <sub>S</sub> = 800 A, V <sub>GS</sub> = + 20 V, T <sub>ch</sub> = 25 °C	—	1.4	—	V	—
Source-drain off-voltage (sense)	V <sub>SD(off)</sub> sense		I <sub>S</sub> = 800 A, V <sub>GS</sub> = - 6 V, T <sub>ch</sub> = 25 °C	—	2.1	—	V	—
			I <sub>S</sub> = 800 A, V <sub>GS</sub> = - 6 V, T <sub>ch</sub> = 175 °C	—	4.3	6.3	V	—
Source-drain off-voltage (terminal)	V <sub>SD(off)</sub> terminal		I <sub>S</sub> = 800 A, V <sub>GS</sub> = - 6 V, T <sub>ch</sub> = 25 °C	—	2.2	—	V	—
Reverse recovery time	t <sub>rr</sub>	(Note 2)	Inductive Load, V <sub>DD</sub> = 1800 V, I <sub>S</sub> = 800 A, V <sub>GS</sub> = - 6 V, Drive side R <sub>G(on)</sub> = 2.2 Ω, C <sub>GS</sub> = 100nF, T <sub>ch</sub> = 175 °C, L <sub>S</sub> ≈ 110 nH	—	0.09	—	μs	7.3 7.5
Reverse recovery loss	E <sub>rr</sub>			—	18	—	mJ	
Stray inductance	L <sub>sPN</sub>		P terminal — N terminal	—	12	—	nH	—
Current sensing inductance	L <sub>sCS</sub>		1 terminal — 8 terminal	—	2.7	—	nH	—
Rated NTC resistance	R		T <sub>c</sub> = 25 °C	3.5	5.0	6.5	kΩ	—
			T <sub>c</sub> = 150 °C	125	165	205	Ω	—
NTC B value	B		T <sub>NTC</sub> = 25 to 150 °C	—	3375	—	K	—

Note 1: Gate-Source Voltage (-10V) is applied 5ms before measurement.

Note 2: L<sub>s</sub> is the sum of the stray inductance between the P and N terminals (L<sub>sPN</sub>) and the stray inductance of external circuitry (L<sub>ext</sub>) .

### 7.2. SBD Electrical Characteristics ( $T_c = 25^\circ\text{C}$ , unless otherwise specified)

Characteristics	Symbol	Note	Test Condition	Min	Typ.	Max	Unit	Fig.
Repetitive peak reverse current	$I_{RRM}$		$V_R = 3300\text{ V}$	—	—	60	$\mu\text{A}$	—
Forward voltage (sense)	$V_F$ sense		$I_F = 800\text{ A}, T_j = 25^\circ\text{C}$	—	2.0	—	V	—
			$I_F = 800\text{ A}, T_j = 175^\circ\text{C}$	—	3.9	5.0	V	—
Forward voltage (terminal)	$V_F$ terminal		$I_F = 800\text{ A}, T_j = 25^\circ\text{C}$	—	2.2	—	V	—
Reverse recovery charge	$Q_{rr}$	(Note)	$V_R = 1800\text{ V}, I_F = 800\text{ A},$ Drive side $R_{G(on)} = 1.5\ \Omega,$ $T_j = 175^\circ\text{C}, L_s \approx 70\text{ nH}$	—	10	—	$\mu\text{C}$	7.4
Reverse recovery time	$t_{rr}$			—	0.08	—	$\mu\text{s}$	7.5
Reverse recovery loss	$E_{rr}$			—	10	—	mJ	

Note:  $L_s$  is the sum of the stray inductance between the P and N terminals ( $L_{sPN}$ ) and the stray inductance of external circuitry ( $L_{ext}$ ).

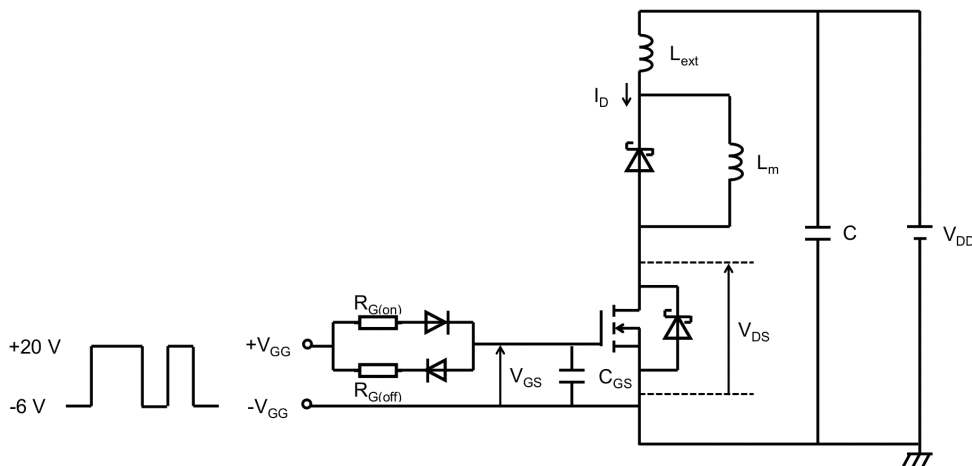


Fig. 7.1 Inductive Load Switching Test Circuit

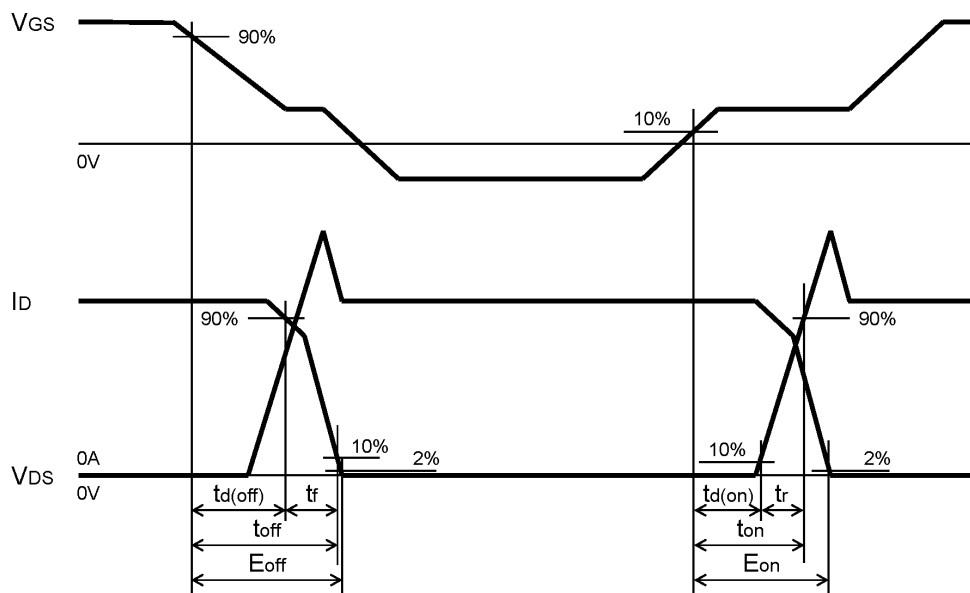


Fig. 7.2 Timing Chart (MOSFET part)

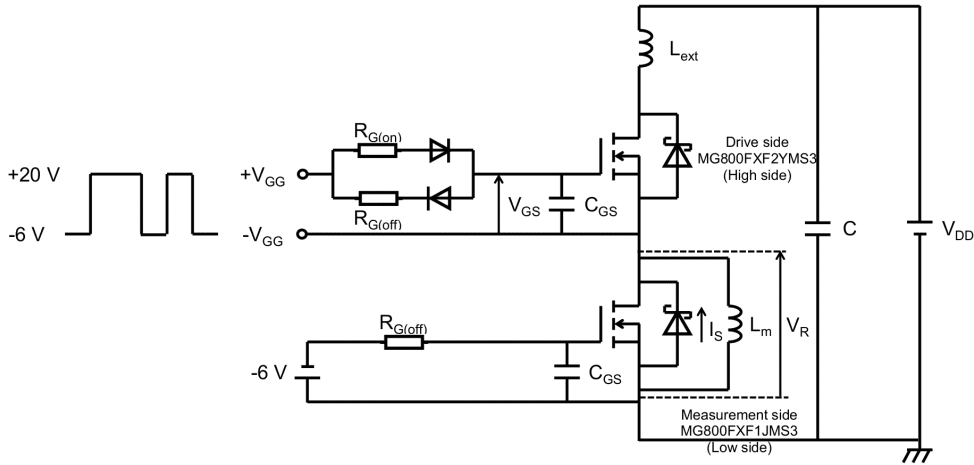


Fig. 7.3 MOSFET Reverse Inductive Load Switching Test Circuit

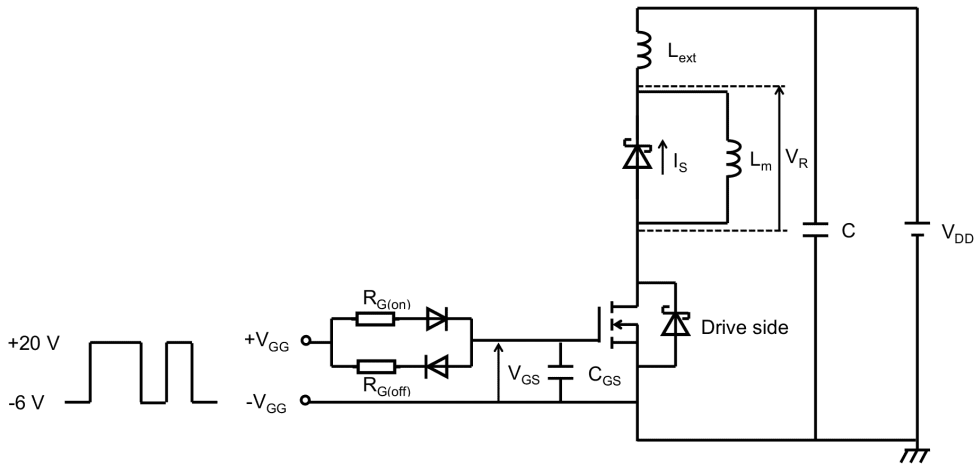


Fig. 7.4 SBD Reverse Inductive Load Switching Test Circuit

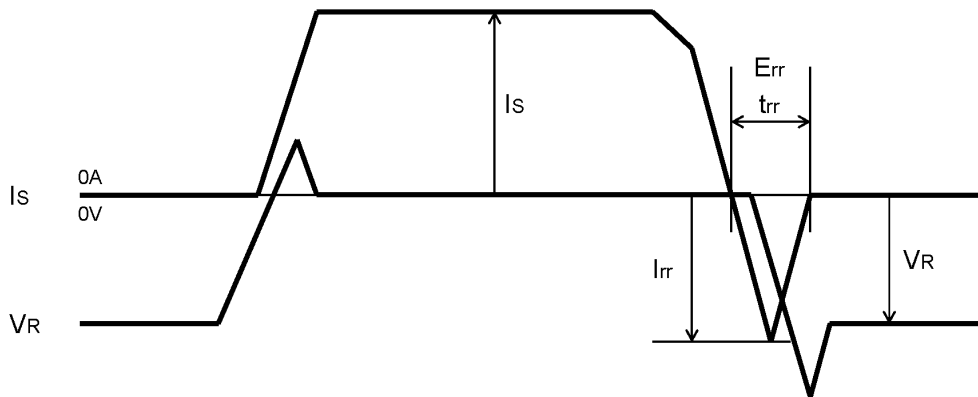
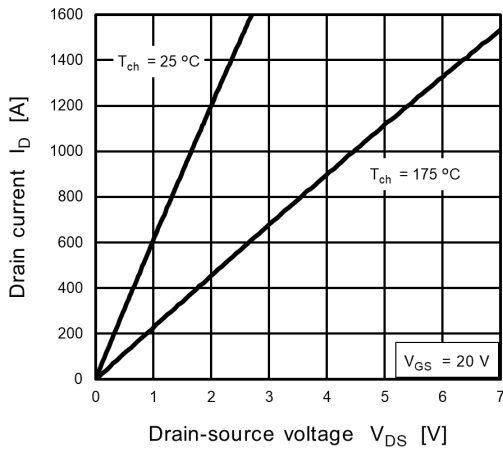
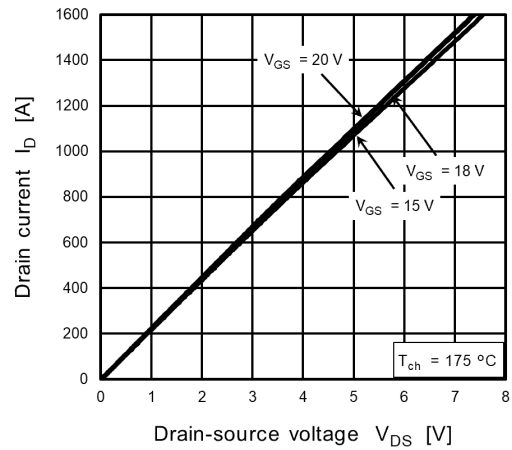


Fig. 7.5 Timing Chart (Diode part)

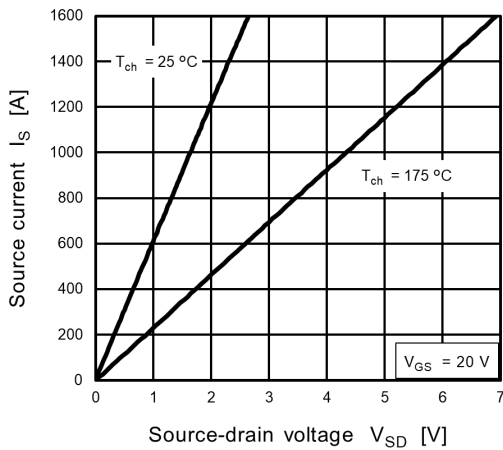
## 8. MOSFET Characteristics Curves (Note)



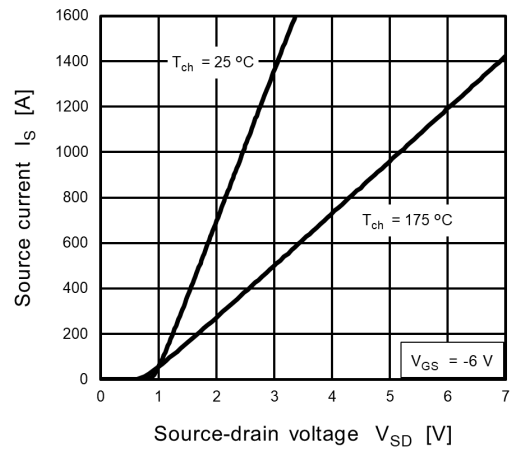
**Fig. 8.1  $I_D - V_{DS}$  (Note 1)**



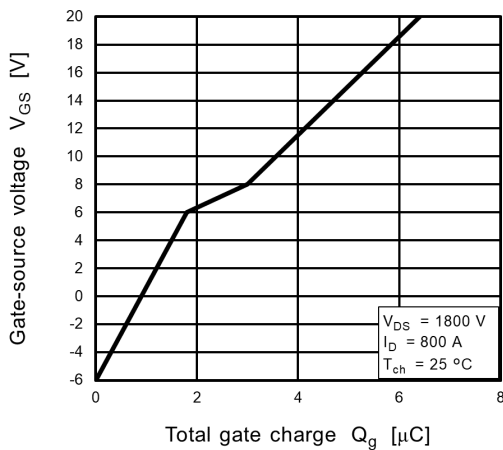
**Fig. 8.2  $I_D - V_{DS}$  (Note 1)**



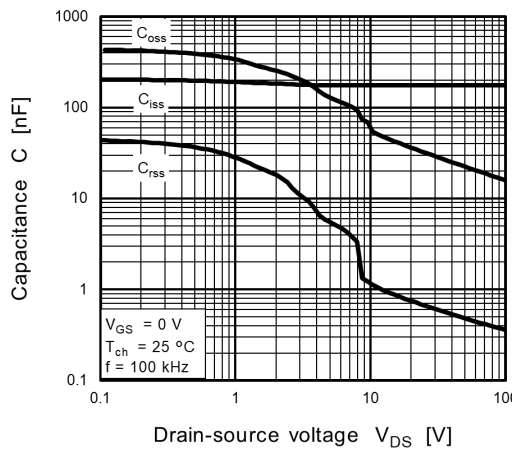
**Fig. 8.3  $I_S - V_{SD}$  (Note 1)**



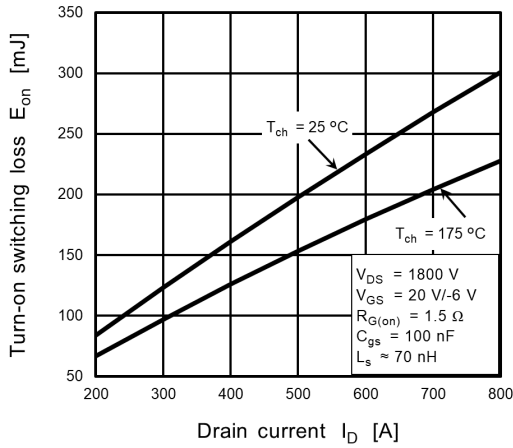
**Fig. 8.4  $I_S - V_{SD}$  (Note 1)**



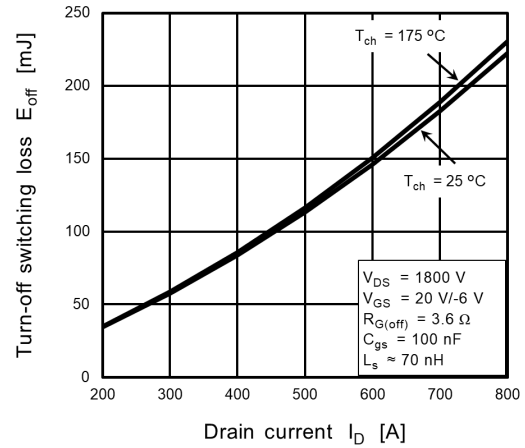
**Fig. 8.5  $V_{GS} - Q_g$**



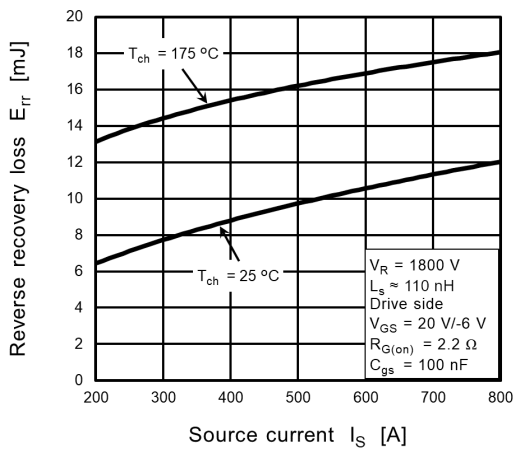
**Fig. 8.6  $C_{iss}, C_{oss}, C_{rss} - V_{DS}$**



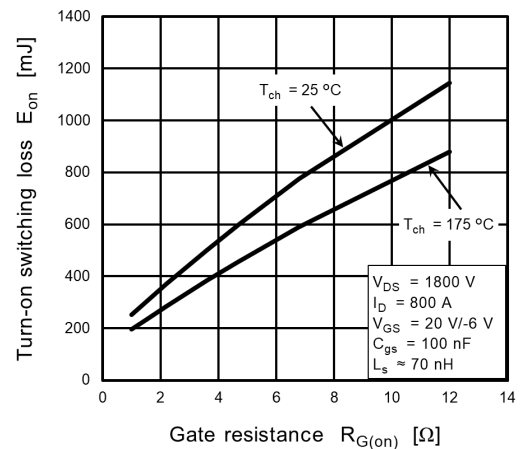
**Fig. 8.7  $E_{on} - I_D$**



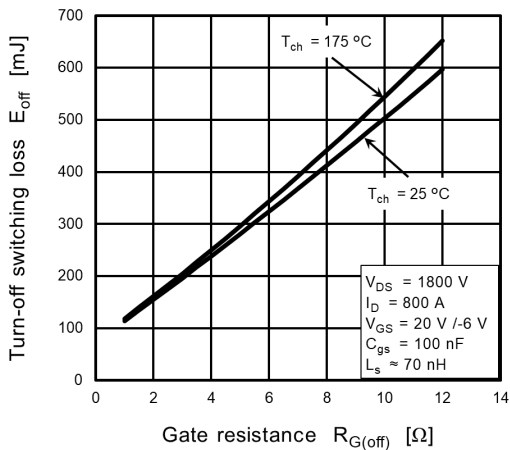
**Fig. 8.8  $E_{off} - I_D$**



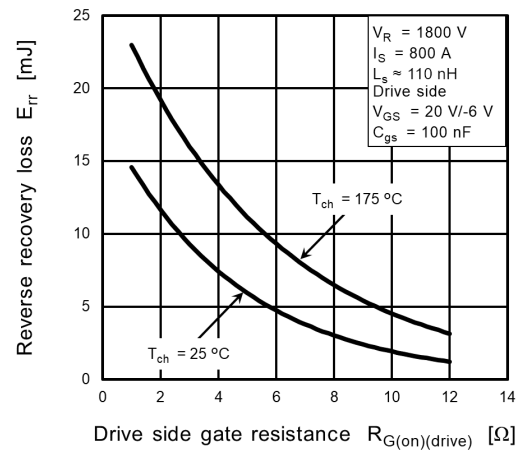
**Fig. 8.9  $E_{rr} - I_S$**



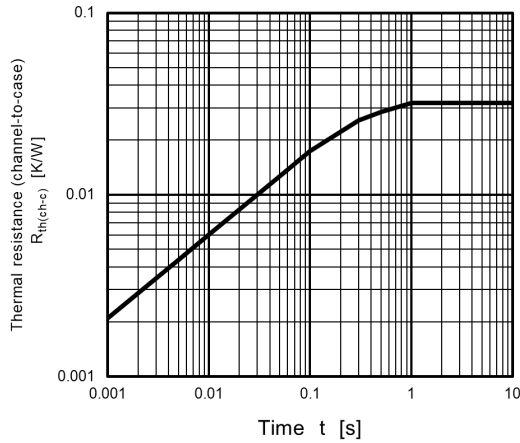
**Fig. 8.10  $E_{on} - R_{G(on)}$**



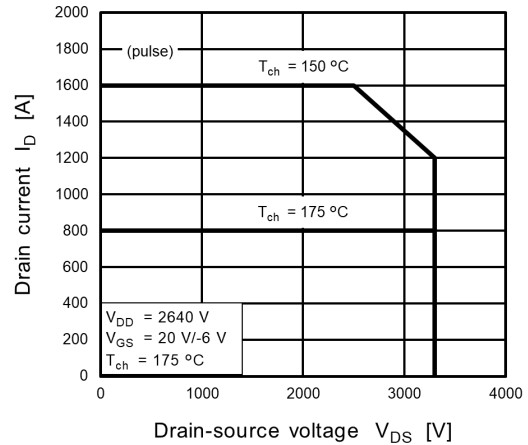
**Fig. 8.11  $E_{off} - R_{G(off)}$**



**Fig. 8.12  $E_{rr} - R_{G(on)}$**



**Fig. 8.13  $R_{th(ch-c)} - t$  (Guaranteed Maximum)**

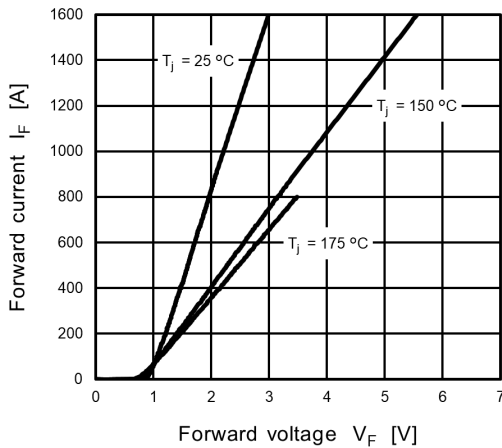


**Fig. 8.14 Reverse bias safe operating area (RBSOA)(Guaranteed Maximum)**

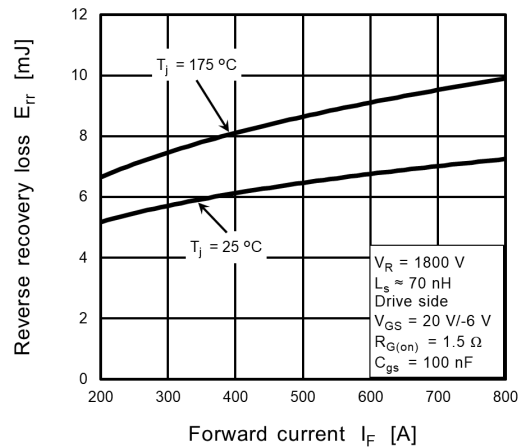
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Note 1: Source - drain voltage and Drain - source voltage are measured at sense terminals.

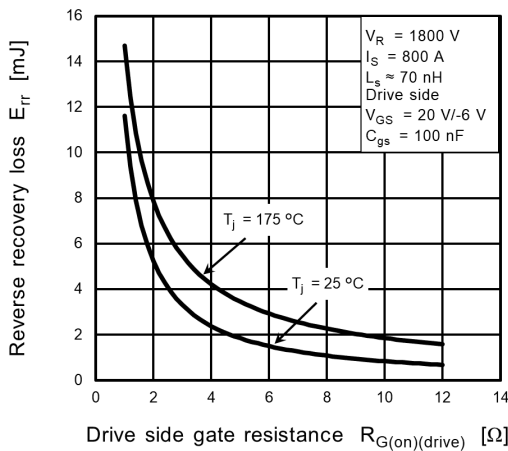
### 9. SBD Characteristics Curves (note)



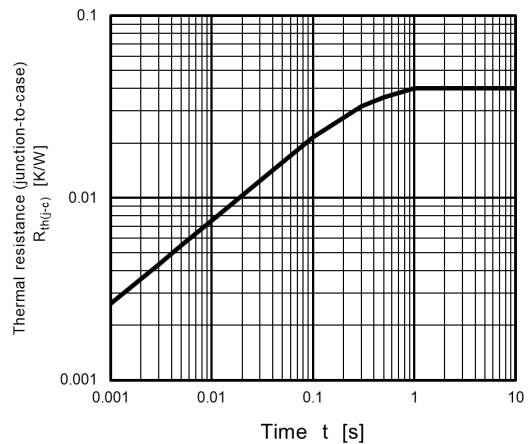
**Fig. 9.1  $I_F - V_F$  (Note 1)**



**Fig. 9.2  $E_{rr} - I_F$**



**Fig. 9.3  $E_{rr} - R_{G(on)}$**



**Fig. 9.4  $R_{th(j-c)} - t$  (Guaranteed Maximum)**

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Note 1: Forward voltage is measured at sense terminals.



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