TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# **TC74HC283AP, TC74HC283AF**

#### 4-Bit Binary Full Adder

The TC74HC283A is a high speed CMOS 4-BIT BINARY FULL ADDER fabricated with silicon gate  $\rm C^2MOS$  technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

Sum  $(\Sigma)$  outputs are provided for each bit and a resultant carry (C4) is obtained from the fourth bit.

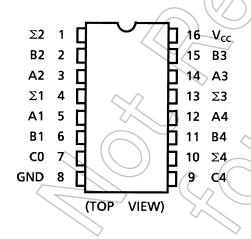
This adder features full internal look-ahead across all four bits.  $A4 \times n$  bit binary adder is easily built up by cascading the HC283A without any additional logic.

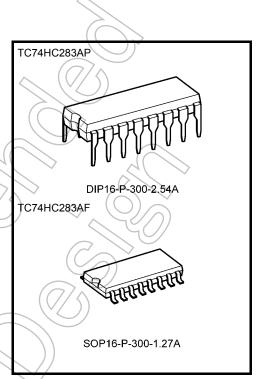
All inputs are equipped with protection circuits against static discharge or transient excess voltage.

#### **Features**

- High speed:  $t_{pd} = 17 \text{ ns (typ.)}$  at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $I_{CC} = 4 \mu A$  (max) at  $T_{a} = 25^{\circ}C$
- High noise immunity:  $V_{NIH} = V_{NIL} = 28\% V_{CC}$  (min)
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: |IOH| = IOL = 4 mA (min)
- Balanced propagation delays:  $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: VCC (opr) = 2 to 6 V
- Pin and function compatible with 74LS283

### Pin Assignment

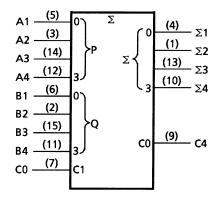




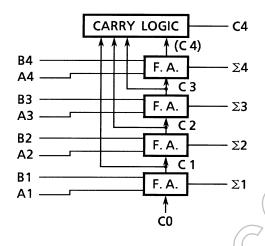
Weight

DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.)

# **IEC Logic Symbol**



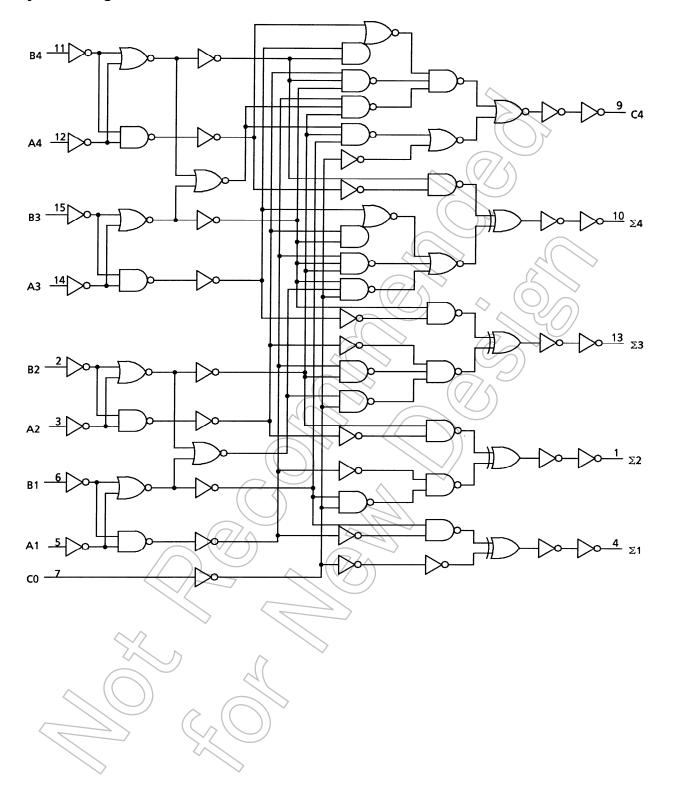
### **Block Diagram**



### Truth Table (1 bit)

	Input	Outputs			
Bn	An	Cn –	Σn	Cn	
L	L	L	Ľ	L	
L	L	Н	Н	\ \	
L	Н	\L\\	Н	L	
L	Н	Ĥ	Z)L	Н	
Н	L (		Н	L	
Н	7	H	L	H	
(H_	H	L	((	)H	
H	=/	Н	Н	Ŧ	

# **System Diagram**



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#### **Absolute Maximum Ratings (Note 1)**

Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	–0.5 to 7	V
DC input voltage	V <sub>IN</sub>	−0.5 to V <sub>CC</sub> + 0.5	V
DC output voltage	Vout	−0.5 to V <sub>CC</sub> + 0.5	⟨v
Input diode current	lıK	±20	mA
Output diode current	lok	±20	mA
DC output current	I <sub>OUT</sub>	±25	mA
DC V <sub>CC</sub> /ground current	Icc	±50	_mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.

### **Operating Ranges (Note)**

Characteristics	Symbol	Rating	Unit
Supply voltage	VCC	2 to 6	V
Input voltage	// ŷ <sub>IN</sub>	0 to V <sub>CC</sub>	٧
Output voltage	Vout	0 to V <sub>CC</sub>	<b>V</b>
Operating temperature	Topr	-40 to 85	°C
		0 to 1000 (V <sub>CC</sub> = 2.0 V)	
Input rise and fall time	t <sub>r</sub> , t <sub>f</sub>	0 to 500 (V <sub>CC</sub> = 4.5 V)	ns
		0 to 400 (V <sub>CC</sub> = 6.0 V)	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V<sub>CC</sub> or GND.

### **Electrical Characteristics**

#### **DC Characteristics**

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
	.,			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	
		_		2.0	1.50	_ <	/	1.50	_	
High-level input voltage	$V_{IH}$			4.5	3.15	_		3.15	_	V
3.0				6.0	4.20		( <del>-</del>	4.20	_	
					_	10	0.50	_	0.50	
Low-level input voltage	V <sub>IL</sub>	_		4.5	4	/ <del>/</del> //	1)35	_	1.35	V
					- 2		1.80	_	1.80	
	Voн	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>		2.0	1.9	2.0	<sup>&gt;</sup> —	1.9	_	
			$I_{OH} = -20 \mu A$	4.5	4.4	4.5	_	4.4	_	
High-level output voltage				6.0 <	5.9	6.0		5.9	$\rightarrow$	V
J			$I_{OH} = -4 \text{ mA}$	4.5	4.18	4.31		4.13	> —	
			$I_{OH} = -5.2 \text{ mA}$	6.0//	5.68	5.80	-(	5.63	_	
	V <sub>OL</sub>	VIN = VIH or VIL	(	2.0		0.0	0.1	4	0.1	
			Ι <sub>ΟL</sub> = 20 μΑ	4.5	_	0.0	⊋0.1	>_	0.1	
Low-level output voltage			40	6.0	_	0.0	(0.1)	_	0.1	V
			I <sub>OL</sub> = 4 mA	4.5	_	0.17	0.26	_	0.33	
			$I_{OL} = 5.2 \text{ mA}$	6.0	1	0.18	0.26	_	0.33	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or	GND	6.0		)	±0.1	_	±1.0	μА
Quiescent supply current	Icc	$V_{IN} = V_{CC}$ or	GND	6.0		//-	4.0		40.0	μΑ

# AC Characteristics ( $C_L = 15 \text{ pF}$ , $V_{CC} = 5 \text{ V}$ , $Ta = 25^{\circ}\text{C}$ , input: $t_r = t_f = 6 \text{ ns}$ )

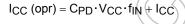
Characteristics	Sŷmbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	T <sub>TLH</sub>	<u> </u>	-	4	8	ns
Propagation delay time	tpLH			17	26	ns
(C0-Σn)	t <sub>pHL</sub>			17	20	113
Propagation delay time (C0-C4)	t <sub>pLH</sub>	_	_	17	26	ns
Propagation delay time (An, Bn-Ση)	t <sub>pLH</sub>	_	_	23	37	ns
Propagation delay time (An, Bn-C4)	t <sub>pLH</sub>	Т	_	21	34	ns

AC Characteristics ( $C_L = 50 \text{ pF}$ , input:  $t_r = t_f = 6 \text{ ns}$ )

Characteristics Symbol		Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	,		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	
	4		2.0	_	30	75	_	95	
Output transition time	t <sub>TLH</sub>	_	4.5	_	8	15	_	19	ns
	t <sub>THL</sub>		6.0	_	7	13	_	16	
Propagation delay	<b>+</b>		2.0	_	60	150	4	190	
time	t <sub>pLH</sub>	_	4.5	_	20	30	<i>J</i> _	38	ns
(C0-Σn)	t <sub>pHL</sub>		6.0	_	17(	26	_	32	
Propagation delay	+		2.0	-	60	150	_	190	
time	t <sub>pLH</sub>	_	4.5	-((	20	30	_	38	ns
(C0-C4)	t <sub>pHL</sub>		6.0		17	26	_	32	
Propagation delay	t		2.0		95	210	7	265	
time	t <sub>pLH</sub>	_	4.5	17	27	42		53	ns
(An, Bn-Σn)	t <sub>pHL</sub>		6.0	/ <del>~</del> ~	22	36	7-/	> 45	
Propagation delay	4		2.0	<i>J</i>	80	195		245	
time	t <sub>pLH</sub>		4.5	_	25	39		49	ns
(An, Bn-C4)	$t_{pHL}$		6.0	_	20	33	>_	42	
Input capacitance	C <sub>IN</sub>	- (		_	5	_10	_	10	pF
Power dissipation capacitance	C <sub>PD</sub> (Note)		>	1	126	) —	_		pF

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

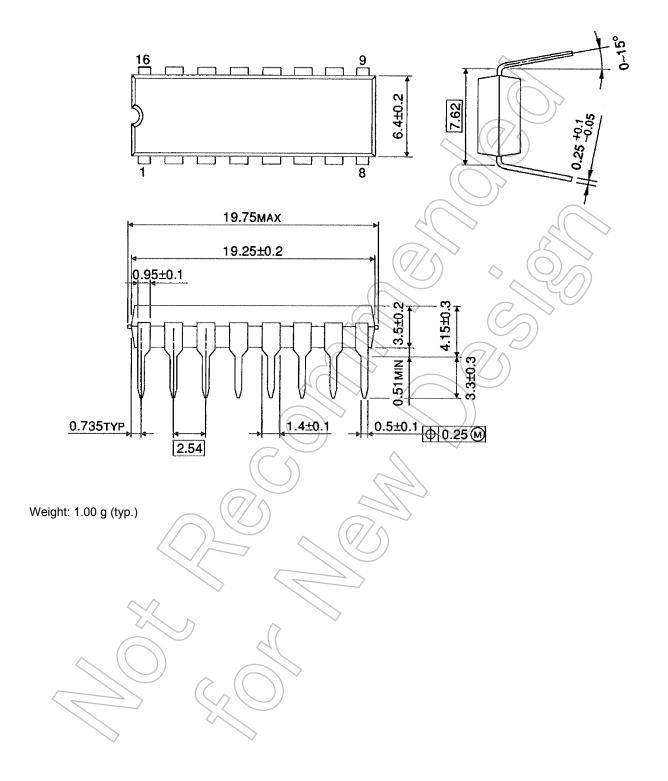
Average operating current can be obtained by the equation:





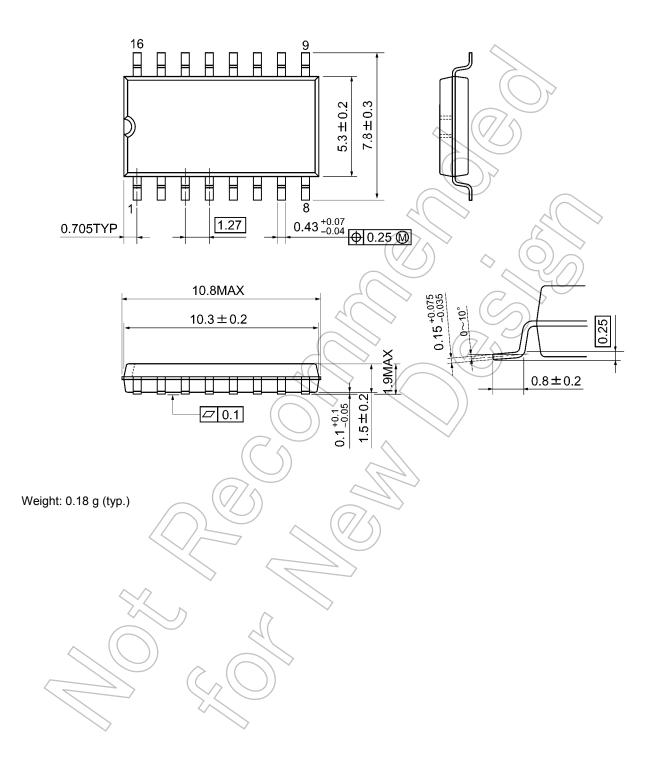
### **Package Dimensions**

DIP16-P-300-2.54A Unit: mm



### **Package Dimensions**

SOP16-P-300-1.27A Unit: mm



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