

RD Number: RD131

RD Title: TC78B025FTG Evaluation circuit BOM

Item No.	Designator	Quantity	Value	Part Number	Manufacturer	Description	Package	Not Mount
1	J1	1	Pin header 2P	—	—	Jumper		
2	J1	1	Jumper socket	—	—	Short pin		
3	J2	1	Pin header 3P	—	—	Jumper		
4	J2	1	Jumper socket	—	—	Short pin		
5	JP3	1	Pin header 3P	—	—	Jumper		
6	JP3	1	Jumper socket	—	—	Short pin		
7	D1	0	Not mount	—	—	Diode		✓
8	D1	0	Socket pin	—	—	Socket pin		✓
9	CVM1	1	10μF to 47μF 50V	—	—	Electrolytic capacitor		
10	CVM2	1	0.1μF 50V	—	—	Chip capacitor		
11	CVM3	0	10μF to 47μF 50V	—	—	Electrolytic capacitor		✓
12	CVM4	0	0.1μF 50V	—	—	Chip capacitor		✓
13	CVCP	1	0.1μF 50V	—	—	Chip capacitor		
14	CCP	1	0.01μF 50V	—	—	Chip capacitor		
15	CVREG	1	0.1μF 50V	—	—	Chip capacitor		
16	CHALL	0	Not mount	—	—	Chip capacitor		✓
17	RLED1	1	24kΩ 0.1W	—	—	Chip resistor		
18	RLED1	0	24kΩ	—	—	Leaded resistor		✓
19	RLED2	1	5.1kΩ 0.1W	—	—	Chip resistor		
20	RLED2	0	5.1kΩ	—	—	Leaded resistor		✓
21	RFG	1	5.1kΩ 0.1W	—	—	Chip resistor		
22	RFG	0	5.1kΩ	—	—	Leaded resistor		✓
23	RAL	1	5.1kΩ 0.1W	—	—	Chip resistor		
24	RAL	0	5.1kΩ	—	—	Leaded resistor		✓
25	RHall	1	1.1kΩ 0.1W	—	—	Chip resistor		
26	RHall	0	1.1kΩ	—	—	Leaded resistor		✓

27	RSPD	1	51kΩ 0.1W	–	–	Chip resistor		
28	RHM1	0	Not mount	–	–	Chip resistor		✓
29	RHM1	0	Not mount	–	–	Leaded resistor		✓
30	RHM2	0	Not mount	–	–	Chip resistor		✓
31	RHM2	0	Not mount	–	–	Leaded resistor		✓
32	RHP	0	Not mount	–	–	Leaded resistor		✓
33	RHP	0	Not mount	–	–	Chip resistor		✓
34	LED1	1	LED	–	–	LED Red		
35	LED2	1	LED	–	–	LED Green		
36	LED3	1	LED	–	–	LED Yellow		
37	LED4	1	LED	–	–	LED Red		
38	VIN	1	Check terminal	–	–	Logic pin		
39	GND	1	Check terminal	–	–	Logic pin		
40	CPM	1	Check terminal	–	–	Logic pin		
41	CPP	1	Check terminal	–	–	Logic pin		
42	VCP	1	Check terminal	–	–	Logic pin		
43	VM	1	Check terminal	–	–	Logic pin		
44	U	1	Check terminal	–	–	Logic pin		
45	V	1	Check terminal	–	–	Logic pin		
46	W	1	Check terminal	–	–	Logic pin		
47	DIR	1	Check terminal	–	–	Logic pin		
48	HM	1	Check terminal	–	–	Logic pin		
49	HP	1	Check terminal	–	–	Logic pin		
50	GND1	1	Check terminal	–	–	Logic pin		
51	VHALL	1	Check terminal	–	–	Logic pin		
52	ALERT	1	Check terminal	–	–	Logic pin		
53	FG	1	Check terminal	–	–	Logic pin		
54	SPD	1	Check terminal	–	–	Logic pin		
55	GND2	1	Check terminal	–	–	Logic pin		
56	VPU	1	Check terminal	–	–	Logic pin		
57	VREG	1	Check terminal	–	–	Logic pin		
58	CON1	1	PCB terminal block 2P	–	–	PCB terminal block 2P		
59	CON2	1	PCB terminal block 2P	–	–	PCB terminal block 2P		
60	CON2	1	PCB terminal block 3P	–	–	PCB terminal block 3P		

Terms of Use

This terms of use is made between Toshiba Electronic Devices and Storage Corporation ("We") and customers who use documents and data that are consulted to design electronics applications on which our semiconductor devices are mounted ("this Reference Design"). Customers shall comply with this terms of use. Please note that it is assumed that customers agree to any and all this terms of use if customers download this Reference Design. We may, at its sole and exclusive discretion, change, alter, modify, add, and/or remove any part of this terms of use at any time without any prior notice. We may terminate this terms of use at any time and for any reason. Upon termination of this terms of use, customers shall destroy this Reference Design. In the event of any breach thereof by customers, customers shall destroy this Reference Design, and furnish us a written confirmation to prove such destruction.

1. Restrictions on usage

1. This Reference Design is provided solely as reference data for designing electronics applications. Customers shall not use this Reference Design for any other purpose, including without limitation, verification of reliability.
2. This Reference Design is for customer's own use and not for sale, lease or other transfer.
3. Customers shall not use this Reference Design for evaluation in high or low temperature, high humidity, or high electromagnetic environments.
4. This Reference Design shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable laws or regulations.

2. Limitations

1. We reserve the right to make changes to this Reference Design without notice.
2. This Reference Design should be treated as a reference only. We are not responsible for any incorrect or incomplete data and information.
3. Semiconductor devices can malfunction or fail. When designing electronics applications by referring to this Reference Design, customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of semiconductor devices could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Customers must also

refer to and comply with the latest versions of all relevant our information, including without limitation, specifications, data sheets and application notes for semiconductor devices, as well as the precautions and conditions set forth in the "Semiconductor Reliability Handbook".

4. When designing electronics applications by referring to this Reference Design, customers must evaluate the whole system adequately. Customers are solely responsible for all aspects of their own product design or applications. WE ASSUME NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.
5. No responsibility is assumed by us for any infringement of patents or any other intellectual property rights of third parties that may result from the use of this Reference Design. No license to any intellectual property right is granted by this terms of use, whether express or implied, by estoppel or otherwise.
6. THIS REFERENCE DESIGN IS PROVIDED "AS IS". WE (a) ASSUME NO LIABILITY WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (b) DISCLAIM ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO THIS REFERENCE DESIGN, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.

3. Export Control

Customers shall not use or otherwise make available this Reference Design for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). This Reference Design may be controlled under the applicable export laws and regulations including, without limitation, the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export Administration Regulations. Export and re-export of this Reference Design are strictly prohibited except in compliance with all applicable export laws and regulations.

4. Governing Laws

This terms of use shall be governed and construed by laws of Japan.