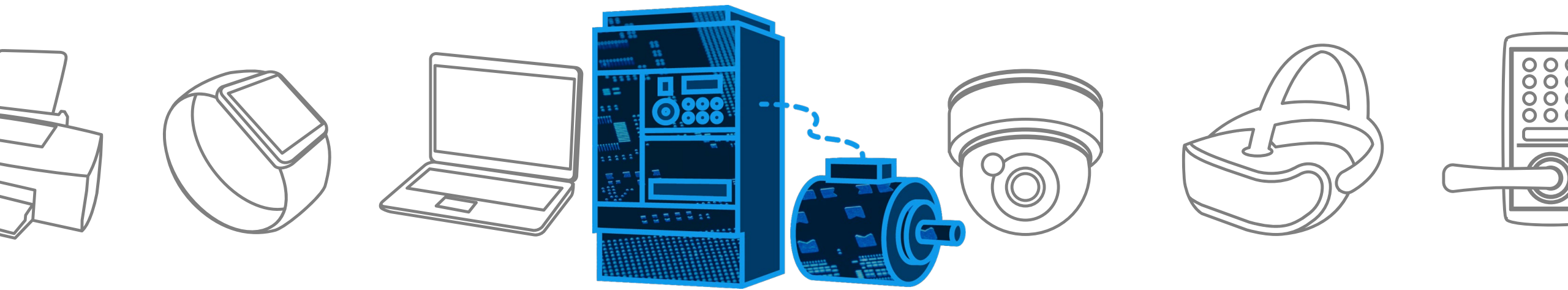
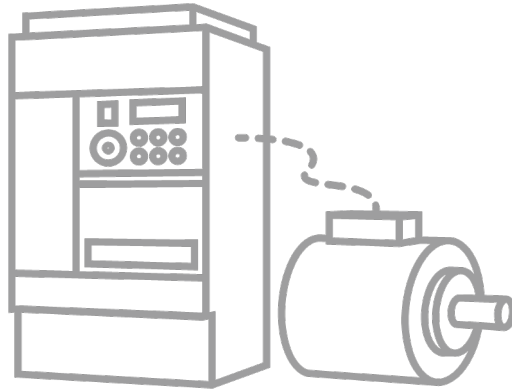
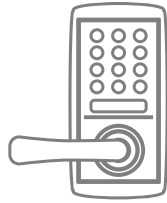
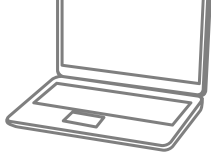


Inverter/Servo

Solution Proposal by Toshiba

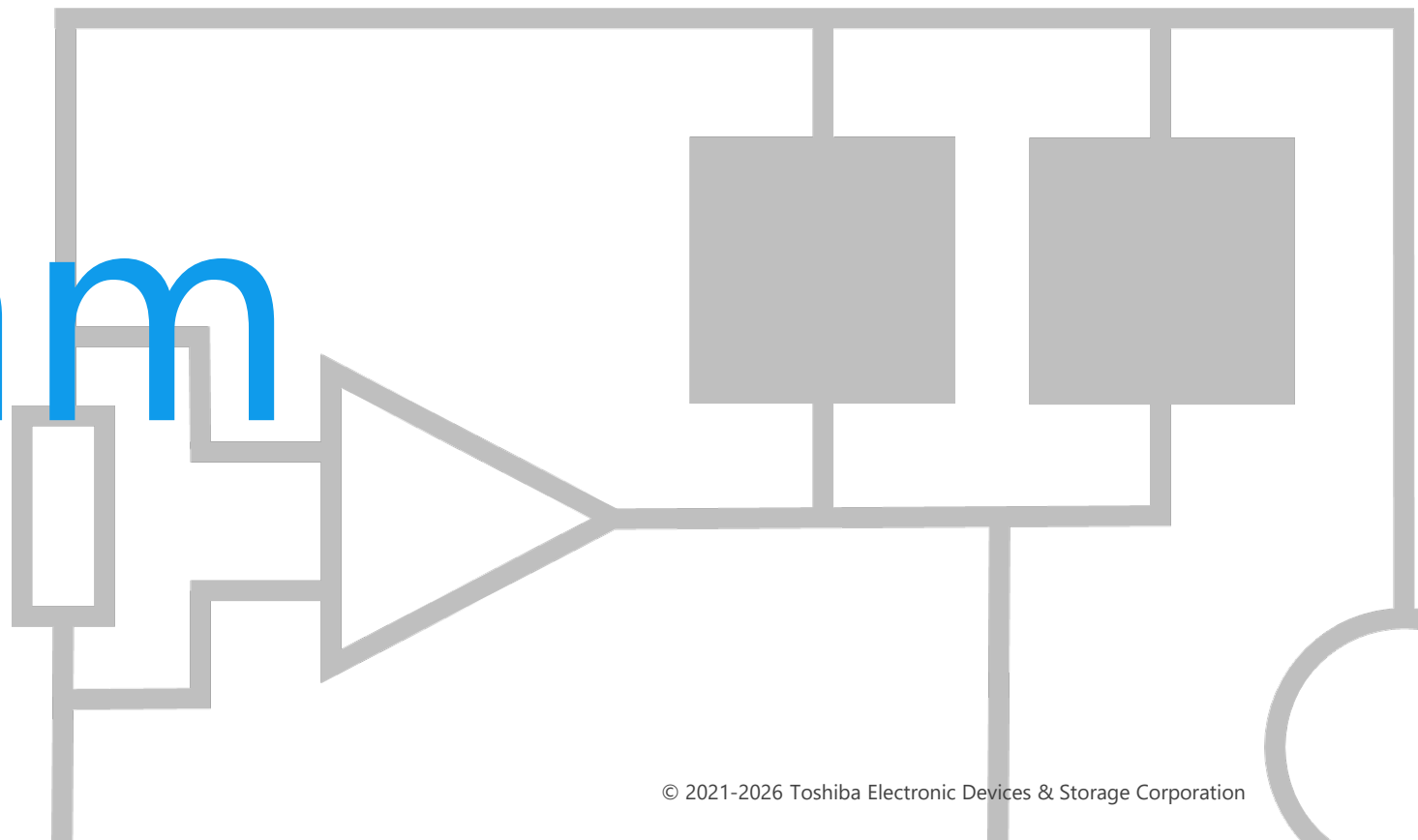




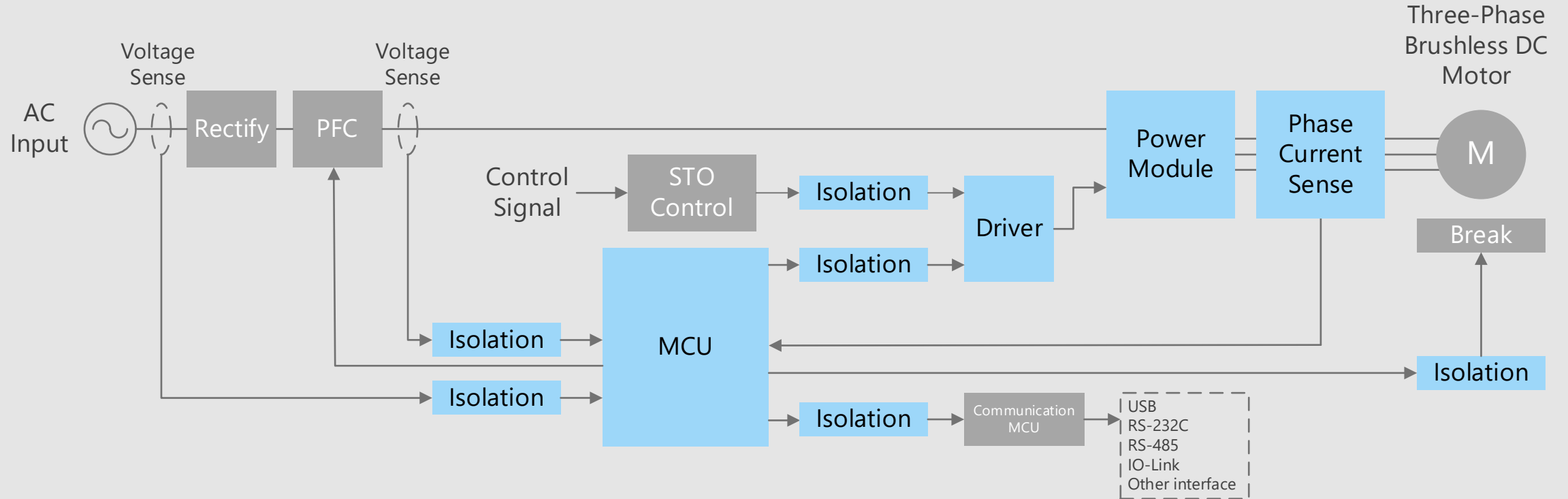
Toshiba Electronic Devices & Storage Corporation provides comprehensive device solutions to customers developing new products by applying its thorough understanding of the systems acquired through the analysis of basic product designs.



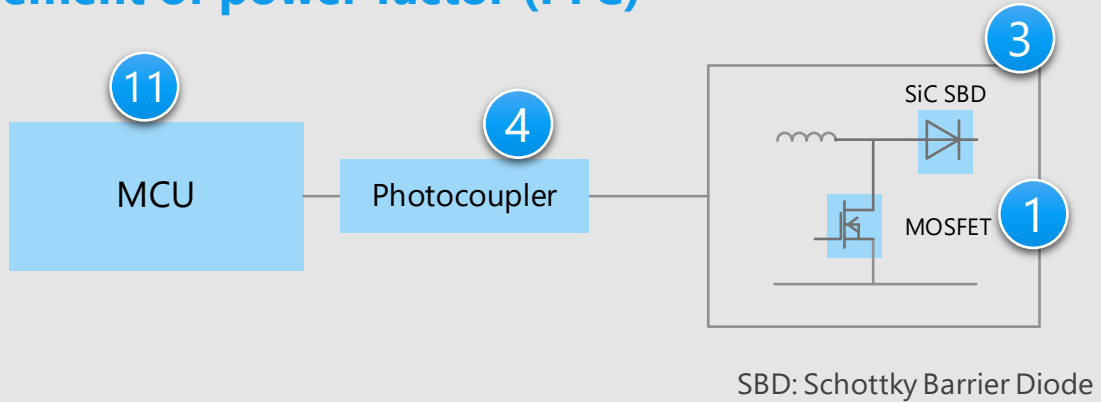
Block Diagram



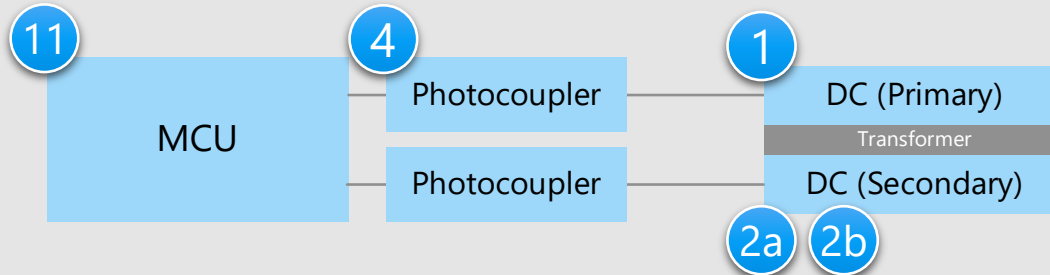
Inverter/Servo Overall block diagram



Improvement of power factor (PFC)



DC-DC converter



Criteria for device selection

- A high voltage MOSFET with high speed recovery diodes is suitable for DC-DC converters.
- SiC type Schottky barrier diodes are suitable for PFC circuits.

Proposals from Toshiba

- **Suitable for high efficiency power supply switching**

DTMOS Series MOSFET

U-MOS Series MOSFET

- **Small $V_F \times Q_C$ and high surge current capability**

SiC Schottky barrier diode

- Photocoupler that is resistant to noise and can operate at high temperature

Gate driver photocoupler (for MOSFET/IPM driving)

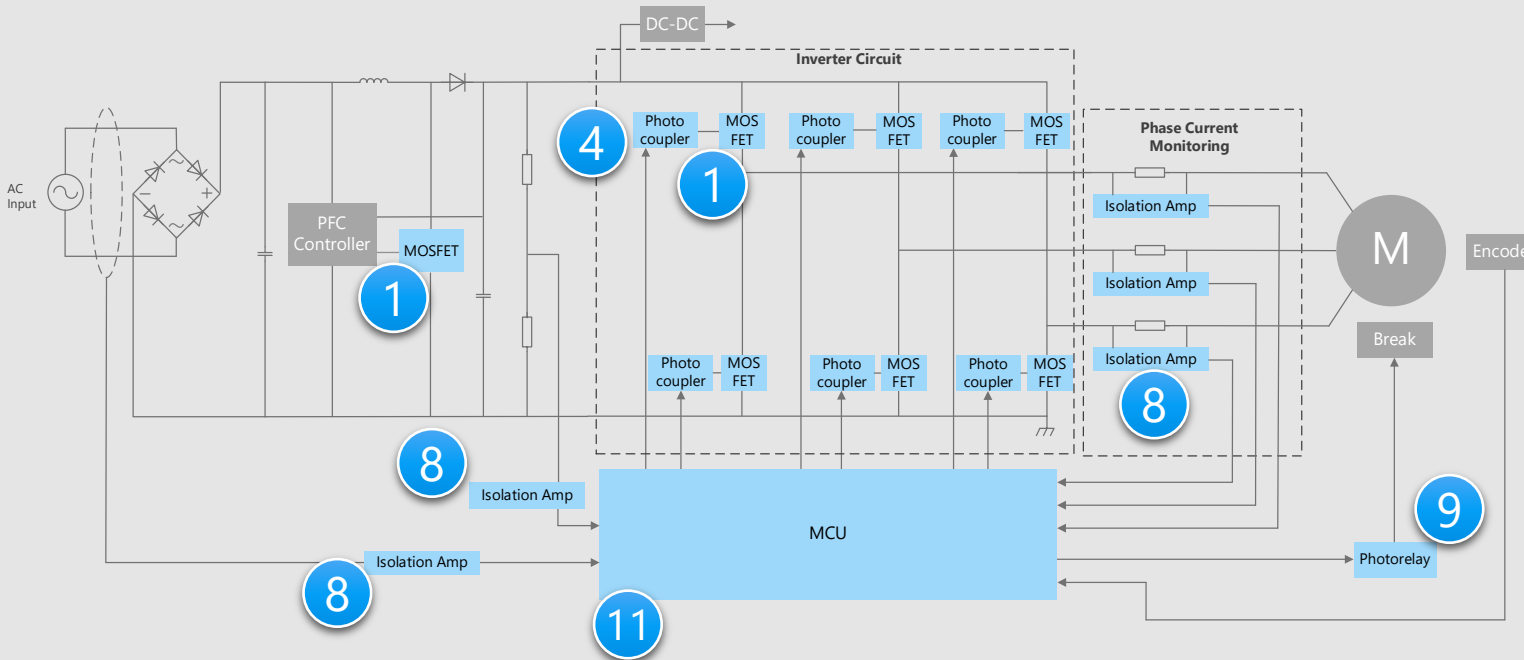
- **Easy software development using general purpose CPU cores**

MCU M4K Group / M470 Group / M370 Group

* Click on the numbers in the circuit diagram to jump to the detailed descriptions page

Inverter/Servo Detail of motor driving circuit (1)

Motor driving circuit (with MOSFETs)



Criteria for device selection

- The use of photocouplers and photorelays realizes the signal transmission between the systems with different voltage levels and suppresses the noise influences.
- The use of photorelays instead of mechanical relays eliminates the life limitation caused by contact wear and welding at the contact points, enabling long life and quieter operation.

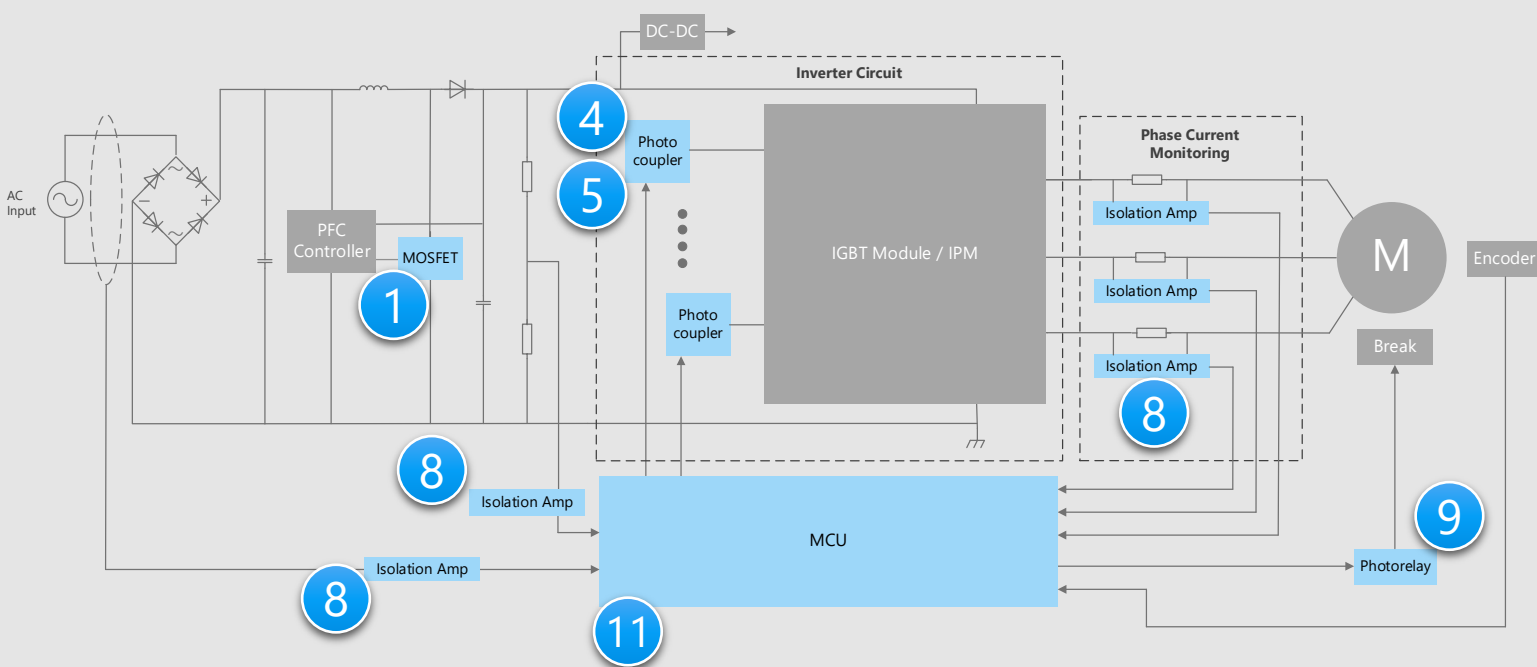
Proposals from Toshiba

- **Suitable for high efficiency power supply switching**
DTMOS Series MOSFET
- **Photocoupler that is resistant to noise and can operate at high temperature**
Gate driver photocoupler (for MOSFET/IPM driving)
- **Isolation amplifiers suitable for current and voltage detection circuits**
Isolation amplifier
- **Photorelays suitable for replacing mechanical relays**
Photorelay
- **Easy software development using general purpose CPU cores**
MCU M4K Group / M470 Group / M370 Group

* Click on the numbers in the circuit diagram to jump to the detailed descriptions page

Inverter/Servo Detail of motor driving circuit (2)

Motor driving circuit (with IGBT Module/IPM)



Criteria for device selection

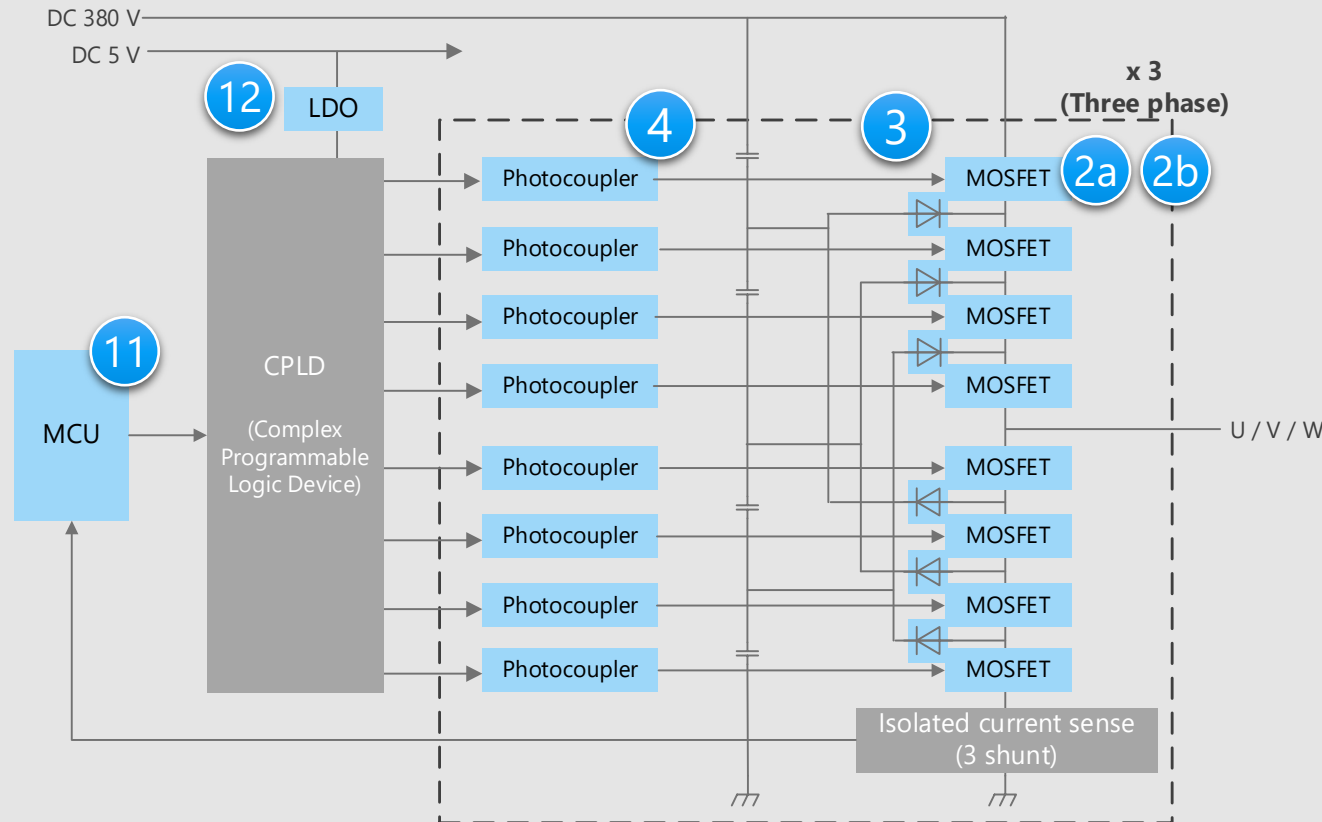
- The use of photocouplers and photorelays realizes the signal transmission between the systems with different voltage levels and suppresses the noise influences.
- The use of photorelays instead of mechanical relays eliminates the life limitation caused by contact wear and welding at the contact points, enabling long life and quieter operation.

Proposals from Toshiba

- **Suitable for high efficiency power supply switching**
DTMOS Series MOSFET
- **Photocouplers that are resistant to noise and can operate at high temperature**
Gate driver photocoupler (for MOSFET/IPM driving)
Gate driver photocoupler (for IGBT driving)
- **Isolation amplifiers suitable for current and voltage detection circuits**
Isolation amplifier
- **Photorelays suitable for replacing mechanical relays**
Photorelay
- **Easy software development using general purpose CPU cores**
MCU M4K Group / M470 Group / M370 Group

* Click on the numbers in the circuit diagram to jump to the detailed descriptions page

Multi-level motor driving circuit (with MOSFETs)



Criteria for device selection

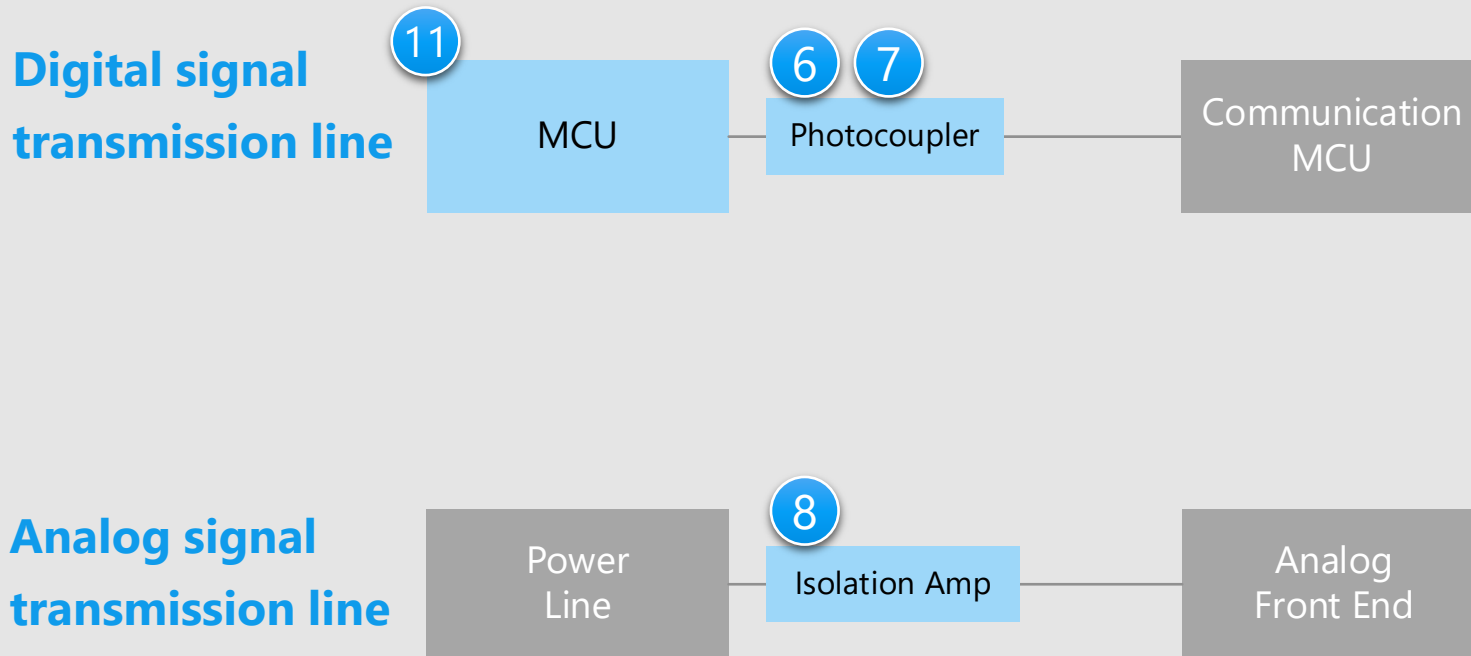
- In a multi-level motor driving circuit, switching devices with low voltage and low on-resistance can be used, enabling efficient power conversion.
- The use of photocouplers and photorelays realizes the signal transmission between the systems with different voltage levels and suppresses the noise influences.

Proposals from Toshiba

- **Suitable for high efficiency power supply switching**
U-MOS Series MOSFET
- **Small $V_F \times Q_C$ and high surge current capability**
SiC Schottky barrier diode
- **Photocoupler that is resistant to noise and can operate at high temperature**
Gate driver photocoupler (for MOSFET/IPM driving)
- **Easy software development using general purpose CPU cores**
MCU M4K Group / M470 Group / M370 Group
- **Supply the power with low noise**
Small surface mount LDO regulator



* Click on the numbers in the circuit diagram to jump to the detailed descriptions page



Criteria for device selection

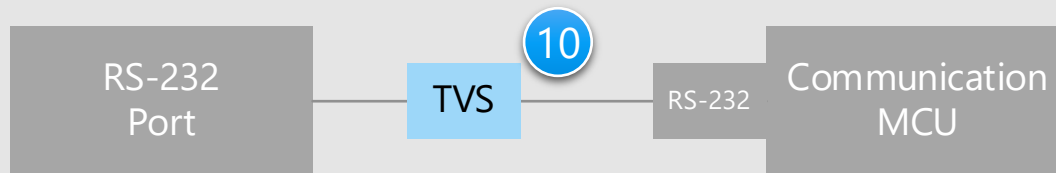
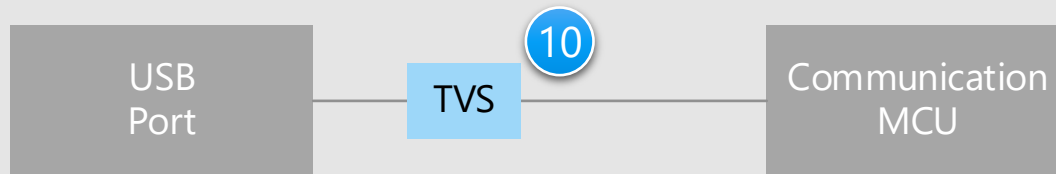
- Photocouplers are suitable for isolation in digital signal transmission lines.
- Isolation amplifiers are suitable for isolation between the high voltage circuit and various detection circuits.

Proposals from Toshiba

- **Photocouplers that are resistant to noise and can operate at high temperature**
Photocoupler for high speed communication
Transistor output photocoupler
- **Isolation amplifiers suitable for current and voltage detection circuits**
Isolation amplifier
- **Easy software development using general purpose CPU cores**
MCU M4K Group / M470 Group / M370 Group

* Click on the numbers in the circuit diagram to jump to the detailed descriptions page

Interface circuits



Criteria for device selection

- TVS diode with a low capacitance is suitable for ESD protecting the USB signal line.

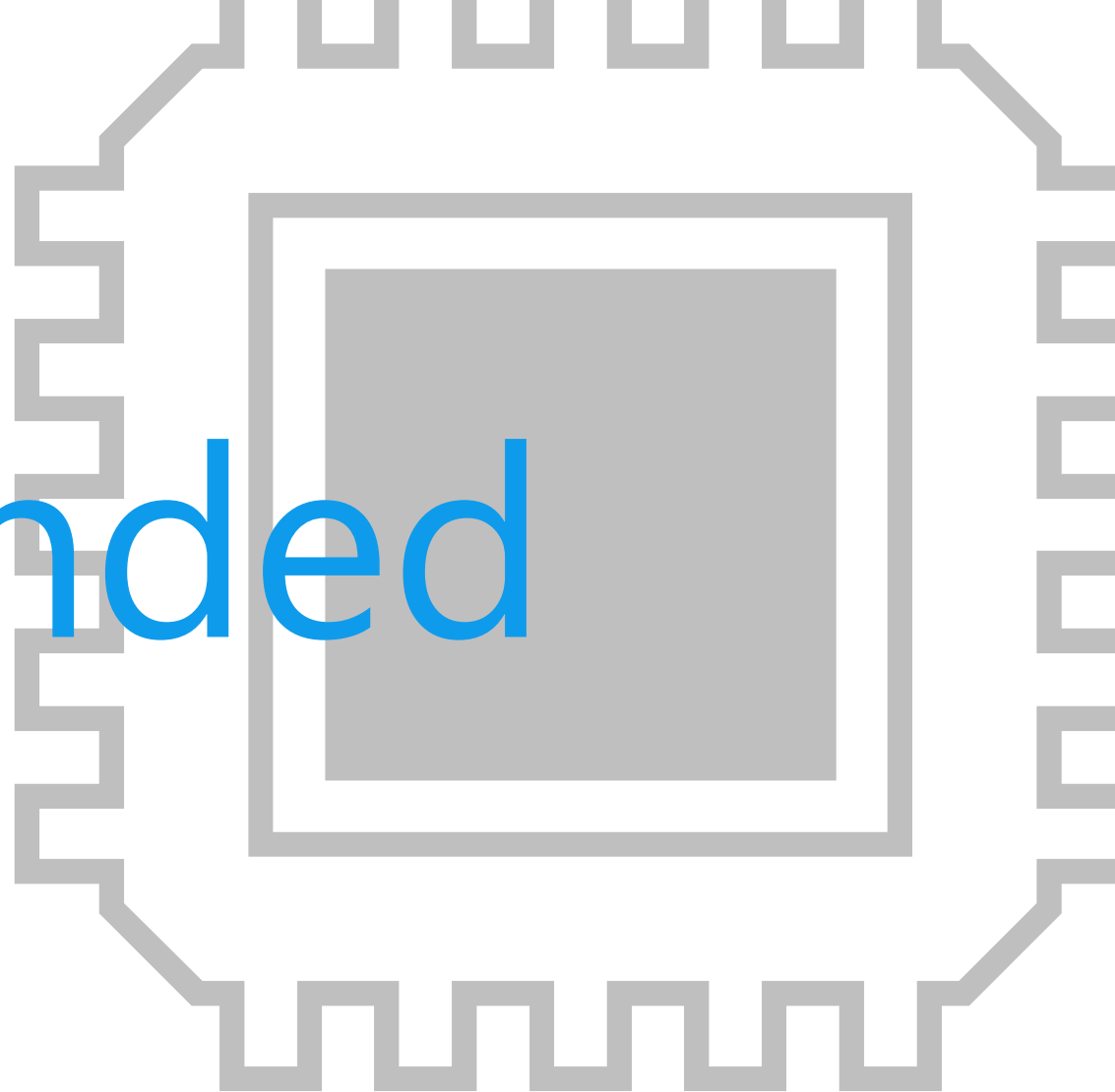
Proposal from Toshiba

- **Prevent circuit malfunctions and protect devices by absorbing electrostatic discharge (ESD) from external terminals**
TVS diode

10

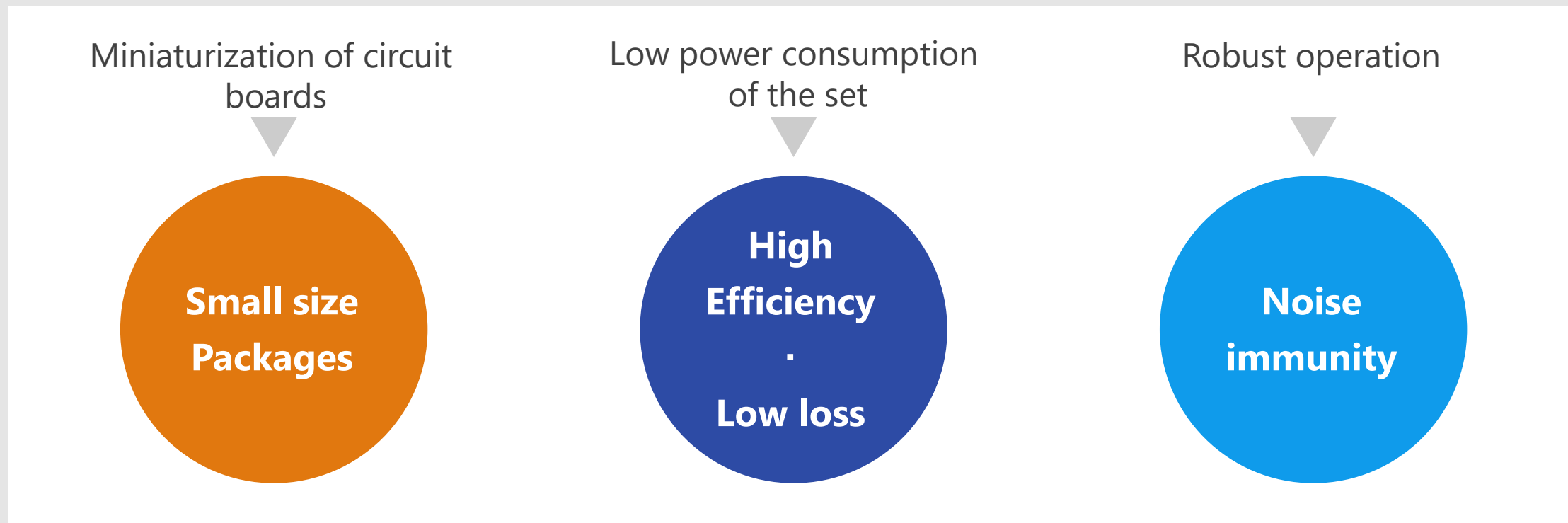
* Click on the numbers in the circuit diagram to jump to the detailed descriptions page

Recommended Devices



Device solutions to address customer needs

As described above, in the design of inverter/servo system, "**Miniaturization of circuit boards**", "**Low power consumption of the set**" and "**Robust operation**" are important factors. Toshiba's proposals are based on these three solution perspectives.



Device solutions to address customer needs

Small size
packages

High
Efficiency
·
Low loss

Noise
immunity

①	DTMOS Series MOSFET	●	●	●
②	U-MOS Series MOSFET	●	●	●
③	SiC Schottky barrier diode	●	●	●
④	Gate driver photocoupler (for MOSFET/IPM driving)	●	●	●
⑤	Gate driver photocoupler (for IGBT driving)	●	●	●
⑥	Photocoupler for high speed communication	●	●	●
⑦	Transistor output photocoupler	●	●	●
⑧	Isolation amplifier	●	●	●
⑨	Photorelay	●	●	●

Device solutions to address customer needs

Small size
packages

High
Efficiency
·
Low loss

Noise
immunity

10	TVS diode	●		●
11	MCU M4K Group / M470 Group / M370 Group	●	●	
12	Small surface mount LDO regulator	●	●	●

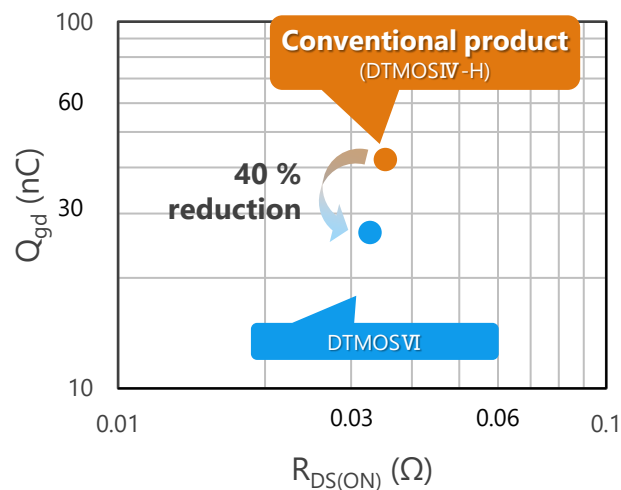
Value provided

Improved performance index $R_{DS(ON)} \times Q_{gd}$, which shows switching characteristic, contribute to improve power supply efficiency and miniaturization of the set.

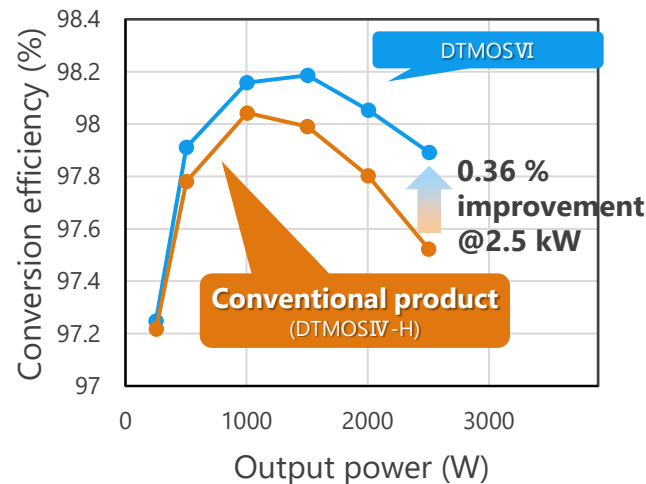
1 $R_{DS(ON)} \times Q_{gd}$ improvement

The $R_{DS(ON)} \times Q_{gd}$ of figure of merit has been reduced by 40 % with gate design and process optimization.

(Comparison of DTMOSIV-H Products: Toshiba internal comparison)



[Note] Toshiba internal comparison



[Note] Toshiba internal comparison

2 Body diode reverse recovery characteristics

High speed body diode reduces recovery loss and contributes to high efficiency of power supply.

(TK16A60W5)

Lineup

Part number		TK25A60X	TK16A60W5	TK110A65Z	TK190A65Z	TK110U65Z	TK190U65Z
Package		TO-220SIS  				TOLL  	
V _{DS} [V]		600	600	650	650	650	650
I _D [A]		25	15.8	24	15	24	15
R _{DS(ON)} [Ω] @V _{GS} = 10 V	Typ.	0.105	0.18	0.092	0.158	0.086	0.149
	Max	0.125	0.23	0.11	0.19	0.11	0.19
Polarity		N-ch	N-ch	N-ch	N-ch	N-ch	N-ch
Generation		DTMOSIV-H	DTMOSIV(HSD)	DTMOSVI	DTMOSVI	DTMOSVI	DTMOSVI

[Return to Block Diagram TOP](#)

Value provided

Contribution to energy saving and efficiency increasing with wide variety of lineup and easy design.

1 High efficiency

Low on-resistance ($R_{DS(ON)}$) has been achieved by fine integration process. Trade off between $R_{DS(ON)}$ and Q_g , Q_{sw} , Q_{oss} have been improved by optimization of cell structure.

2 Wide variety of lineup

Voltage from 20 to 250 V are lined up. Wide variety of packages from surface mount type to through hole type are provided.

3 Easy to design

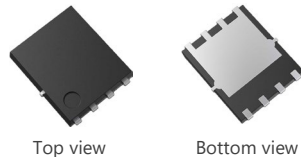
Low V_{DS} spike and low ringing have been realized by parasitic snubber. High avalanche capability.

TSON Advance



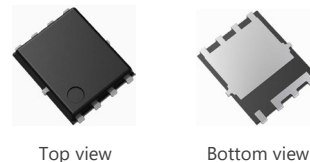
3.1 x 3.3 mm

SOP Advance(N)



4.9 x 6.1 mm

SOP Advance(E)



4.9 x 6.1 mm

Wide variety of packages

Lineup

Part number	TPN19008QM	TPH4R008QM	TPM1R908QM	TPM7R10CQ5
Package	TSON Advance 	SOP Advance(N) 	SOP Advance(E) 	
V_{DS} [V]	80	80	80	150
I_D [A]	34 (38*)	86 (140*)	238	120
$R_{DS(ON)}$ [Ω] @ $V_{GS} = 10$ V	Typ.	0.0147	0.0031	0.0015
	Max	0.019	0.004	0.0019
Polarity	N-ch	N-ch	N-ch	N-ch
Generation	U-MOSX-H	U-MOSX-H	U-MOSX-H	U-MOSX-H

*: Silicon limit

[Return to Block Diagram TOP](#)

Value provided

Contribution to energy saving and efficiency increasing with wide variety of lineup and easy design.

1 High efficiency

Low on-resistance ($R_{DS(ON)}$) has been achieved by fine integration process. Trade off between $R_{DS(ON)}$ and Q_g , Q_{SW} , Q_{OSS} have been improved by optimization of cell structure.

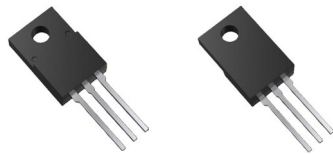
2 Wide variety of lineup

Voltage from 20 to 250 V are lined up. Wide variety of packages from surface mount type to through hole type are provided.

3 Easy to design

Low V_{DS} spike and low ringing have been realized by parasitic snubber. High avalanche capability.

TO-220SIS



Top view

Bottom view

TO-220



Top view

Wide variety of packages

Lineup

Part number		TK2R4A08QM	TK2R4E08QM	TK100E10N1
Package		TO-220SIS 	TO-220 	
V _{DSS} [V]		80	80	100
I _D [A]		100 (116*)	120 (290*)	100 (207*)
R _{DS(ON)} [Ω] @V _{GS} = 10 V	Typ.	0.00188	0.00197	0.0028
	Max	0.00244	0.00244	0.0034
Polarity		N-ch	N-ch	N-ch
Generation		U-MOSX-H	U-MOSX-H	U-MOSVIII-H

*: Silicon limit

[◆Return to Block Diagram TOP](#)

Value provided

SiC SBDs [Note1] with low loss and high efficiency are realized by adopting new metal and optimizing device design.

[Note1] SBD: Schottky barrier diode

1 Low forward voltage (V_F)

For Toshiba's third generation products, new metal and thin wafer technology are introduced. $V_F = 1.2 \text{ V}$ (typ.) is realized as compared with $V_F = 1.45 \text{ V}$ (typ.) of our existing products. V_F is reduced by about 17 %.

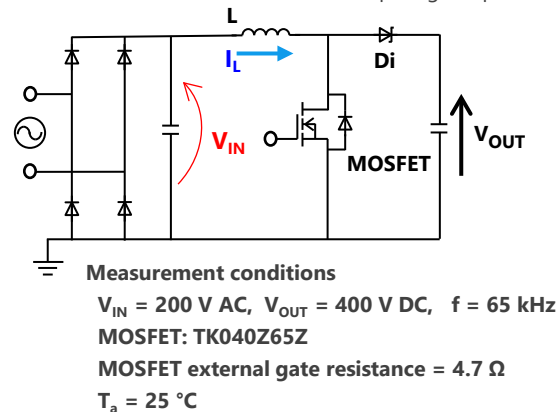
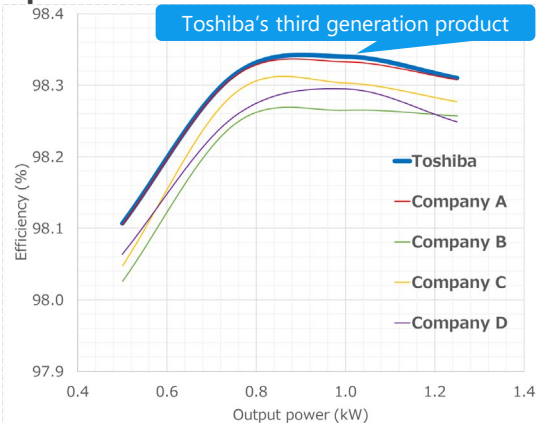
2 Improvement of power supply efficiency

Compared with our existing products, the trade off of $V_F \times Q_C$ [Note2] of Toshiba's third generation products have improved. About 0.1 % of conversion efficiency improvement have also achieved under 800 W output condition in our test.

3 Expansion of package series

In addition to the existing package series, DFN8x8 surface mount package type has prepared. It contributes to miniaturization and high power density of equipment.

Comparison between Toshiba's third generation product and competitor products



(According to Toshiba's research, as of July 2023)

[Note2] The $V_F \times Q_C$ (product of forward voltage and total charge) is an index representing the loss performance of the SiC SBD. When comparing the products with the same current rating, the smaller the index, the lower the loss.

Lineup

Part number	TRS12A65F	TRS24N65FB	TRS2E65H	TRS12E65H	TRS4V65H	TRS12V65H
Package	TO-220F-2L 	TO-247 (Center tap) 	TO-220-2L 	 Top view Bottom view	DFN8x8 	 Top view Bottom view
V_{RRM} [V]	650	650	650	650	650	650
$I_{F(DC)}$ [A]	12	12 / 24 *	2	12	4	12
I_{FSM} [A]	92	92 / 184 *	19	74	28	60
V_F (Typ.) [V]	1.45 @ $I_F = 12 \text{ A}$	1.45 @ $I_F = 12 \text{ A}$	1.2 @ $I_F = 2 \text{ A}$	1.2 @ $I_F = 12 \text{ A}$	1.2 @ $I_F = 4 \text{ A}$	1.2 @ $I_F = 12 \text{ A}$

*: Per Leg / Both Legs

[Return to Block Diagram TOP](#)

[Note 1] IPM: Intelligent Power Module

Value provided

Combines an infrared light emitting diode with high optical output and a light receiving IC chip with high gain and high speed.

1 High noise immunity

Light receiving IC has internal Faraday shield that provides high CMTI (Common Mode Transient Immunity).

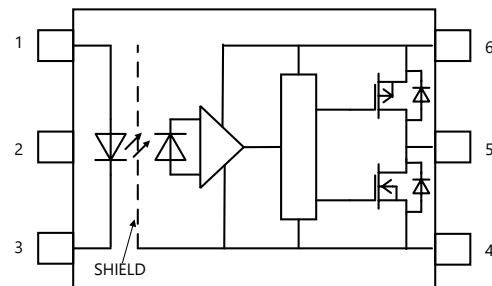
2 High isolation voltage

The isolation voltage BV_S is 5000 Vrms.
($BV_S = 3750$ Vrms for TLP152.)

3 High temperature operation

The products are designed to operate even under severe ambient temperature conditions, such as inverters, robots and machinery, etc.

Internal circuit configuration (TLP5774H)



- 1: Anode
- 2: N.C.
- 3: Cathode
- 4: GND
- 5: V_O (Output)
- 6: V_{CC}

UL-recognized UL1577, File No.E67349

cUL-recognized CSA Component Acceptance Service No.5A File No.E67349

VDE-recognized EN60747-5-5, EN62368-1 [Note 2]

CQC-recognized GB4943.1, GB8898

[Note 2] When a VDE approved type is needed, please designate the Option (D4).

Lineup

Part number	TLP152	TLP5774H	TLP2745	TLP2719
Package	5pin SO6 	SO6L 		
Application	MOSFET driving		IPM driving	
CMTI (Min) [kV/μs]	±20	±35	±30	±10
T_{opr} [°C]	-40 to 100	-40 to 125	-40 to 110	-40 to 100
Propagation delay time (Max) [ns]	190	150	120	2000
Overcurrent protection	-	-	-	-

[◆Return to Block Diagram TOP](#)

Value provided

High isolation by opto-coupling solution and characteristics suitable for gate driving help to simplify circuit design.

1 High noise immunity

Light receiving IC has internal Faraday shield that provides high CMTI (Common Mode Transient Immunity).

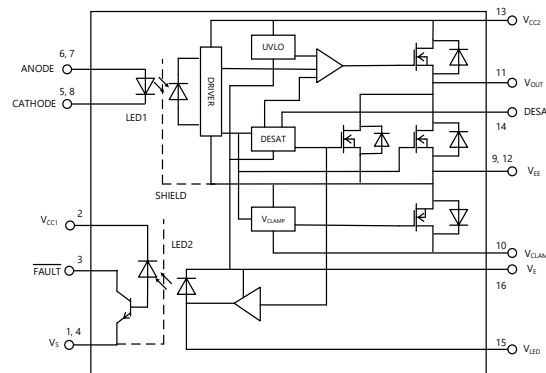
2 High temperature operation

The products are designed to operate even under severe ambient temperature conditions, such as inverters, robots and machinery, etc.

3 Wide output current ratings lineup

Wide product lineup suitable for both gate drive and pre gate drive enables to choose product suitable for each driving.
The products with built-in overcurrent protection function are also provided.

Internal circuit configuration (TLP5212)



UL-recognized UL1577, File No.E67349

cUL-recognized CSA Component Acceptance Service No.5A File No.E67349

VDE-recognized EN60747-5-5, EN62368-1 (TLP5212 approved only for EN60747-5-5) [Note]

CQC-recognized GB4943.1, GB8898

[Note] When a VDE approved type is needed, please designate the Option (D4).

Lineup

Part number	TLP5214A	TLP5212	TLP5222	TLP5231	TLP5754H	TLP5705H
Package	SO16L 				SO6L 	
CMTI (Min) [kV/μs]	±35	±25			±35	±50
T _{opr} [°C]	-40 to 110				-40 to 125	
Peak output current [A]	±4.0	±2.5			±4.0	±5.0
Overcurrent protection	✓				-	

[◆Return to Block Diagram TOP](#)

Value provided

Combines an infrared light emitting diode with high optical output and a light receiving IC chip with high gain and high speed.

1 High noise immunity

Light receiving IC has internal Faraday shield that provides high CMTI (Common Mode Transient Immunity).

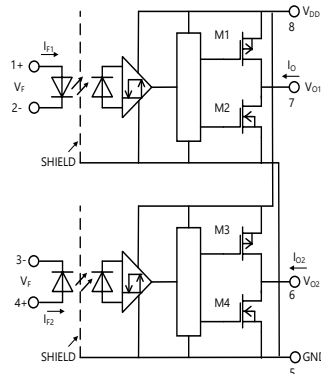
2 High isolation voltage

The isolation voltage BV_S is 5000 Vrms.

3 High temperature operation

The products are designed to operate even under severe ambient temperature conditions, such as inverters, robots and machinery, etc. (-40 to 125 °C)

Internal circuit
configuration
(TLP2210)



- 1: Anode 1
- 2: Cathode 1
- 3: Cathode 2
- 4: Anode 2
- 5: GND
- 6: V_O 2 (output 2)
- 7: V_O 1 (output 1)
- 8: V_{DD}

UL-recognized UL1577, File No.E67349

cUL-recognized CSA Component Acceptance Service No.5A File No.E67349

VDE-recognized EN60747-5-5, EN62368-1 [Note]

CQC-recognized GB4943.1, GB8898

[Note] When a VDE approved type is needed, please designate the Option (D4).

Lineup

Part number	TLP2710	TLP2761	TLP2770	TLP2210	TLP2261	TLP2270
Package	SO6L 			SO8L(LF4) 		
Channel	1			2		
Data rate [Mbps]	5	15	20	5	15	20
T_{opr} [°C]	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125

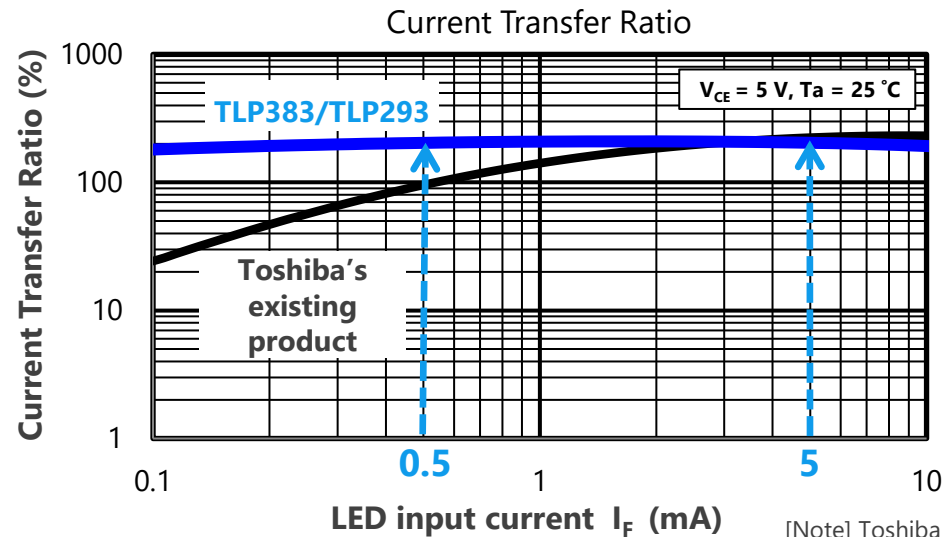
[Return to Block Diagram TOP](#)

Value provided

High CTR (Current Transfer Ratio) is realized even in low input current range ($I_F = 0.5$ mA).

1 High current transfer ratio

The TLP383 and TLP293 are high isolation photocouplers that optically couple a phototransistor and high output infrared LED. Compared to Toshiba's existing product (TLP385), higher CTR (Current Transfer Ratio) in low input current range (@ $I_F = 0.5$ mA) is realized.



2 High temperature operation

The products are designed to operate even under severe ambient temperature conditions, such as inverters, robots and machinery, etc.

Lineup

Part number	TLP383	TLP293	TLP385
Package	4pin SO6L 	SO4 	4pin SO6L 
BV_S [Vrms]	5000	3750	5000
T_{opr} [°C]	-55 to 125	-55 to 125	-55 to 110

[◆Return to Block Diagram TOP](#)

Value provided

This is an isolation amplifier suitable for current and voltage detection of motors and inverters.

1 High isolation performance

This optical coupling type isolation amplifier has a high-precision $\Delta\Sigma$ AD conversion circuit on the input side and a high precision DA conversion circuit on the output side.

2 Support for common mode

Common-mode transient immunity is provided with CMTI [Note 1] = 15 kV/ μ s (min).

[Note 1] CMTI: Common Mode Transient Immunity

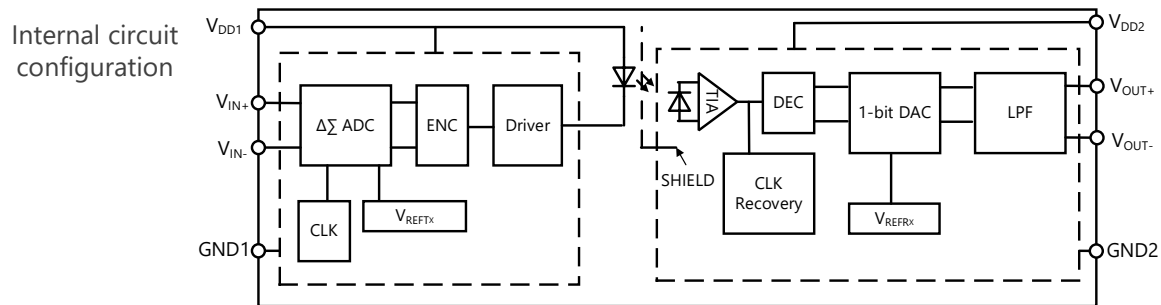
3 5 V system power supply voltages

Input power supply voltage

$$V_{DD1} = 4.5 \text{ to } 5.5 \text{ V}$$

Output power supply voltage

$$V_{DD2} = 3.0 \text{ to } 5.5 \text{ V}$$



[Note 1] Bypass capacitor of 0.1 μ F must be connected between 1 and 4 pins and between 5 and 8 pins.

UL-approved: UL1577, File No.E67349

cUL-approved: CSA Component Acceptance Service No.5A File No.E67349

VDE-approved: EN 60747-5-5, EN 62368-1 [Note 2]

[Note 2] When a VDE approved type is needed, please designate the Option (D4).

Lineup

Part number	TLP7820
Package	SO8L(LF4) 
BV _S [Vrms]	5000
T _{opr} [°C]	-40 to 105
CMTI (Min) [kV/ μ s]	15

[Return to Block Diagram TOP](#)

Value provided

Photorelay consists of an infrared light emitting diode optically coupled to a photo-MOSFET and is suitable for replacing mechanical relays.

1 Low on-resistance

On-resistance $R_{ON} = 0.05 \Omega$ (max)
(TLP3547: A connection) [Note 1]

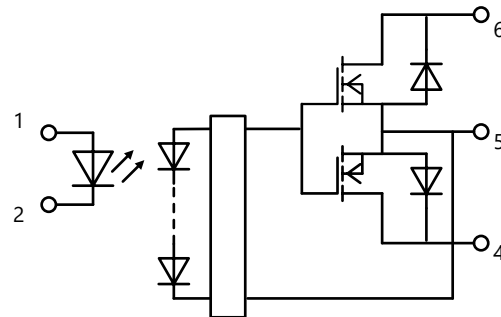
2 Wide current range

Wide range of allowed ON current I_{ON} ,
suitable for power line control.
 $I_{ON} = 5.0 \text{ A}$ (max)
(TLP3547: A connection) [Note 1]

3 Lineup of package and isolation voltage

The lineup of isolation voltage and package for improving design flexibility is provided.

TLP3545A
Internal equivalent circuit



[Note 1] Please refer to the technical data sheet for connection.

Safety Standards

UL approved: UL1577, File No.E67349

cUL approved: CSA Component Acceptance Service No. 5A, File No.E67349

UL-recognized: UL 508, File No.E499232 [Note 2]

VDE-approved: EN 60747-5-5 [Note 3]

[Note 2] Please refer Absolute Maximum Ratings (UL-recognized UL 508) for UL 508 products.

[Note 3] When a VDE approved type is needed, please designate the Option (D4).

Lineup

Part number	TLP3122A	TLP170AM	TLP3545A	TLP3547	TLP240A	TLP241B
Package	4pin SO6 		DIP6 	DIP8 	DIP4 	
I_{ON} [A]	1.4	0.7	4.0	5.0	0.5	2.0
V_{OFF} [V]	60	60	60	60	60	100
R_{ON} (Max) [Ω]	0.25	0.3	0.06	0.05	2.0	0.2
BV_S [Vrms]	3750	3750	2500	2500	5000	5000

[◆Return to Block Diagram TOP](#)

Value provided

This absorbs static electricity (ESD) from external terminals, prevents circuit malfunction and protects devices.

1 High ESD pulse absorption performance

Improved ESD absorption compared to Toshiba's existing products. (50 % reduction in operating resistance)

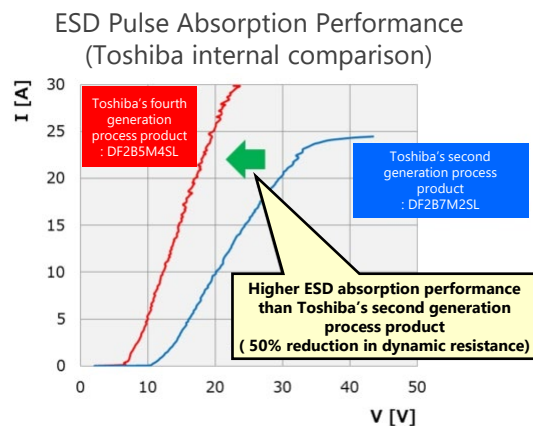
For some products, both low operating resistance and low capacitance are realized and ensures high signal protection performance and signal quality.

2 Suppress ESD energy by low clamp voltage

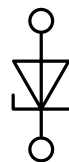
Protect the connected circuits and devices using Toshiba own technology.

3 Suitable for high density mounting

A variety of small size packages are available.



Unidirectional



Suitable for paths such as logic signals. There are lineups of 1in1, 2in1, 4in1, 5in1, 7in1.

Bidirectional



Suitable for paths with both polar signals such as audio signals.

Lineup

Part number	DF2B6M4BSL	DF2B20M4SL	DF2B5PCT	DF2B7PCT
Package	SL2  Top view  Bottom view		CST2  Top view  Bottom view	
V_{ESD} [kV]	±8	±15	±30	±30
V_{RWM} (Max) [V]	5.5	18.5	3.6	5.5
C_t (Typ.) [pF]	0.12	0.2	41	45
R_{DYN} (Typ.) [Ω]	1.05	0.2	0.1	0.1
Purpose	Signal line protection		Power line protection	

[Note] This product is an ESD protection diode and cannot be used for purposes other than ESD protection.

[Return to Block Diagram TOP](#)

Value provided

System cost reduction, higher efficiency and less development work.**1 Equipped with motor control co-processor**

Toshiba's original co-processor vector engine (VE) for motor control reduces CPU load and allows control of multiple motors and peripherals. [Note 1]

[Note 1] VE is integrated into some products

2 Equipped with motor control circuit

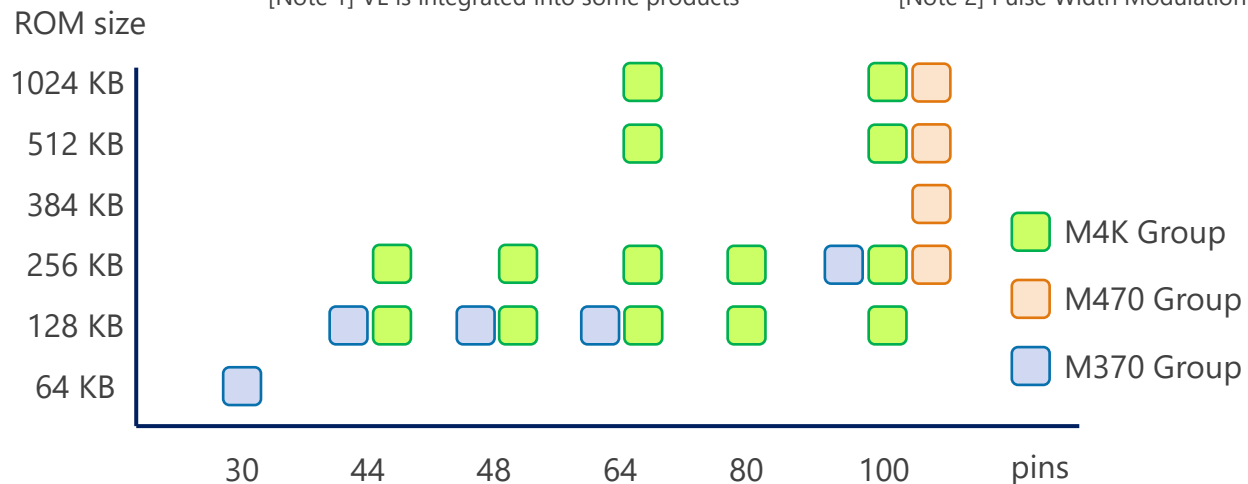
A variety of three-phase PWM [Note 2] waveforms and AD converters enable highly efficient, low noise control. The Advanced Encoder (A-ENC) reduces the load of CPU process in detecting the position performed for each PWM.

[Note 2] Pulse Width Modulation

3 Provide development support tools

Third party evaluation boards and sample programs that can be used to shorten the development time are provided. Toshiba has also offered new, simple, versatile motor control software development kits (MCU Motor Studio). [Note 3]

[Note 3] MCU Motor Studio supports some products and will expand in TXZ+™ family.

**Lineup**

Series	Group	Function
TXZ+™ 4A Series	M4K Group	Arm® Cortex®-M4, Max. 160 MHz operation 4.5 to 5.5 V, 3motor control (Max), Data Flash
TX04 Series	M470 Group	Arm® Cortex®-M4, Max. 160 MHz operation 4.5 to 5.5 V, 2motor control (Max)
TX03 Series	M370 Group	Arm® Cortex®-M3, 80 MHz operation 4.5 to 5.5 V, 2motor control (Max)

[◆Return to Block Diagram TOP](#)

Value provided

Wide lineup from general purpose type to WCSP (Wafer Level Chip Size Package) type are provided. Contribute to realize a stable power supply.

1 Low dropout voltage

Low dropout voltage characteristic has been realized by the originally developed process.

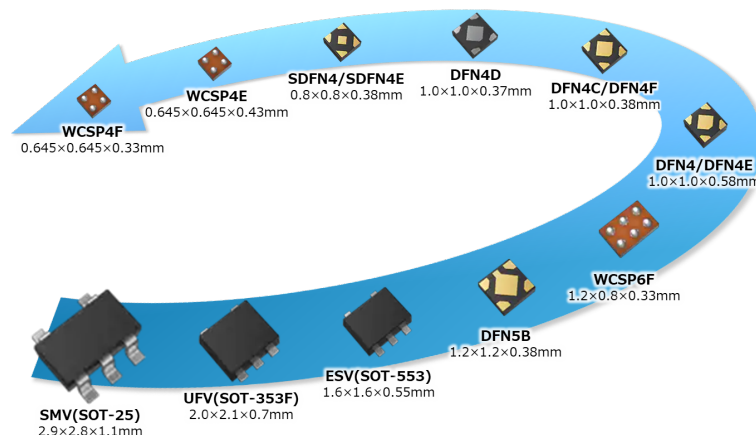
2 High PSRR Low output noise voltage

Many product series that realize both high PSRR (Power Supply Rejection Ratio) and low output noise voltage characteristics are provided. They are suitable for stable power supply for analog circuit.

3 Low current consumption

$I_{B(ON)}$ of 0.34 μA (typ.) @ $I_{OUT} = 0 \text{ mA}$, $V_{OUT} \leq 1.5 \text{ V}$ is realized by utilizing CMOS process and unique circuit technology. (TCR3U Series)

Rich package lineup



Lineup

Part number	TCR15AG Series	TCR8BM Series	TCR5FM Series	TCR5RG Series	TCR3RM Series	TCR3U Series	TCR3LM Series	TCR3D Series	TCR3EM Series	TCR1HF Series
Features	Low dropout voltage High PSRR		High PSRR Low noise Low current consumption			Low current consumption		Standard type		36 V Input voltage
I _{OUT} (Max) [A]	1.5	0.8	0.5			0.3				0.15
PSRR (Typ.) [dB] @f = 1 kHz	95	98	91	100	100	70	-	72	68	70
I _B (Typ.) [μA]	25	20	10	7	7	0.34	1	86	35	170

[Return to Block Diagram TOP](#)

If you are interested in these products and have questions or comments about any of them, please do not hesitate to contact us below:

Contact address: <https://toshiba.semicon-storage.com/ap-en/contact.html>



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