

MOSFETs Silicon N-Channel MOS (DTMOSVI)

# TK095A65Z5

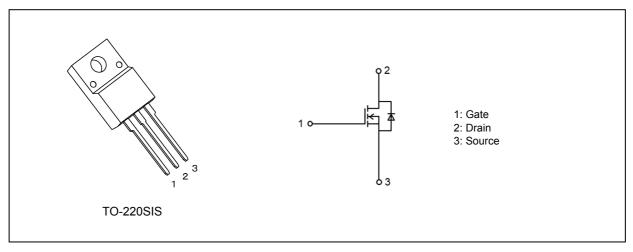
## 1. Applications

• Switching Voltage Regulators

#### 2. Features

- (1) Fast reverse recovery time:  $t_{rr} = 115$  ns (typ.)
- (2) Low drain-source on-resistance:  $R_{DS(ON)} = 0.079 \Omega$  (typ.)
- (3) High-speed switching properties with the lower capacitance.
- (4) Enhancement mode:  $V_{th}$  = 3.5 to 4.5 V ( $V_{DS}$  = 10 V,  $I_{D}$  = 1.27 mA)

## 3. Packaging and Internal Circuit



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## 4. Absolute Maximum Ratings (Note) (Ta = 25 °C unless otherwise specified)

Characteristics	Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	650	V
Gate-source voltage		$V_{GSS}$	±30	
Drain current (DC)	(Note 1)	I <sub>D</sub>	29	Α
Drain current (pulsed)	(Note 1)	I <sub>DP</sub>	116	
Power dissipation	(T <sub>c</sub> = 25 °C)	P <sub>D</sub>	45	W
Single-pulse avalanche energy	(Note 2)	E <sub>AS</sub>	342	mJ
Single-pulse avalanche current		I <sub>AS</sub>	5.8	Α
Reverse drain current (DC)	(Note 1)	I <sub>DR</sub>	29	
Reverse drain current (pulsed)	(Note 1)	I <sub>DRP</sub>	116	
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature		T <sub>stg</sub>	-55 to 150	
Isolation voltage (RMS) (t	= 1.0 s)	V <sub>ISO(RMS)</sub>	2000	V
Mounting torque		TOR	0.6	N · m

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### 5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance		2.77	°C/W
Channel-to-ambient thermal resistance	R <sub>th(ch-a)</sub>	62.5	

Note 1: Ensure that the channel temperature does not exceed 150 °C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25 °C (initial), L = 18.0 mH,  $I_{AS}$  = 5.8 A

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

#### 6. Electrical Characteristics

## 6.1. Static Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 650 V, V <sub>GS</sub> = 0 V	_	_	100	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	650	_	_	V
Gate threshold voltage	$V_{th}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1.27 mA	3.5	_	4.5	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 14.5 A	_	0.079	0.095	Ω

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## 6.2. Dynamic Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance		C <sub>iss</sub>	V <sub>DS</sub> = 300 V, V <sub>GS</sub> = 0 V, f = 100 kHz	_	2880	_	pF
Reverse transfer capacitance		C <sub>rss</sub>		_	2.7	_	
Output capacitance		C <sub>oss</sub>		_	63	_	
Effective output capacitance (energy related)	(Note 3)	C <sub>o(er)</sub>	V <sub>DS</sub> = 0 to 400 V, V <sub>GS</sub> = 0 V	_	105	_	
Effective output capacitance (time related)	(Note 4)	C <sub>o(tr)</sub>		_	723	_	
Gate resistance		r <sub>g</sub>	V <sub>DS</sub> = OPEN , f = 1 MHz	_	3	_	Ω
Switching time (rise time)		t <sub>r</sub>	See Fig. 6.2.1	_	30	_	ns
Switching time (turn-on time)		t <sub>on</sub>		_	62	_	
Switching time (fall time)		t <sub>f</sub>		_	4	_	
Switching time (turn-off time)		t <sub>off</sub>		_	95	_	
MOSFET dv/dt ruggedness		dv/dt	$V_{DS} \le V_{DSS}$ , $I_D \le 14.5 A$	110	_	_	V/ns

Note 3:  $C_{O(er)}$  is a fixed capacitance that gives the same stored energy as  $C_{OSS}$  while  $V_{DS}$  is rising from 0 V to 400 V. Note 4:  $C_{O(tr)}$  is a fixed capacitance that gives the same charging time as  $C_{OSS}$  while  $V_{DS}$  is rising from 0 V to 400 V.

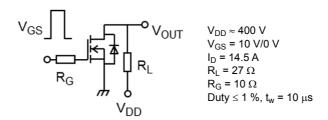


Fig. 6.2.1 Switching Time Test Circuit

## 6.3. Gate Charge Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 29 \text{ A}$	-	50	1	nC
Gate-source charge 1	Q <sub>gs1</sub>			15		
Gate-drain charge	$Q_{gd}$		_	17		

## 6.4. Source-Drain Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Diode forward voltage	V <sub>DSF</sub>	I <sub>DR</sub> = 29 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time (Note 5	t <sub>rr</sub>	V <sub>DD</sub> = 400 V,	_	115	184	ns
Reverse recovery charge	Q <sub>rr</sub>	$I_{DR} = 14.5 \text{ A}, V_{GS} = 0 \text{ V}$ $-dI_{DR}/dt = 100 \text{ A}/\mu\text{s}$	_	0.58	_	μС
Peak reverse recovery current	Irr		_	10	_	Α
Diode dv/dt ruggedness	dv/dt	$V_{DD} \le 400 \text{ V}, I_{DR} \le 14.5 \text{ A}, V_{GS} = 0 \text{ V}$	70	_	_	V/ns

Note 5: Defined by design.



## 7. Marking (Note)

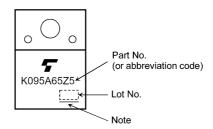


Fig. 7.1 Marking

Note: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.



#### 8. Characteristics Curves (Note)

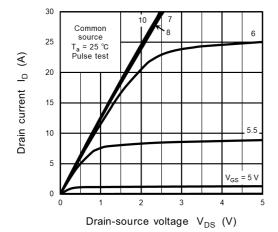


Fig. 8.1 I<sub>D</sub> - V<sub>DS</sub>

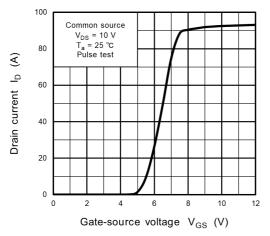


Fig. 8.3 I<sub>D</sub> - V<sub>GS</sub>

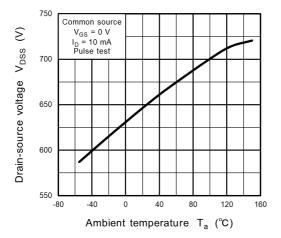


Fig. 8.5 V<sub>DSS</sub> - T<sub>a</sub>

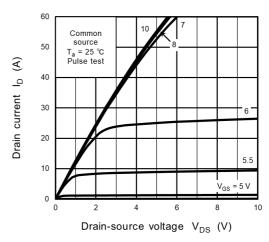


Fig. 8.2 I<sub>D</sub> - V<sub>DS</sub>

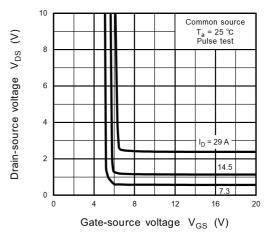


Fig. 8.4 V<sub>DS</sub> - V<sub>GS</sub>

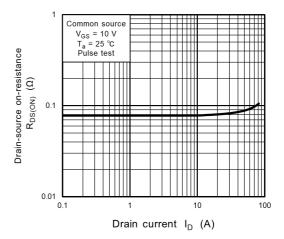


Fig. 8.6 R<sub>DS(ON)</sub> - I<sub>D</sub>



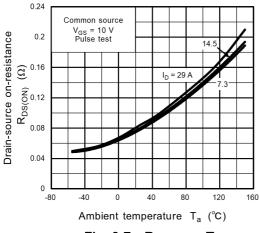
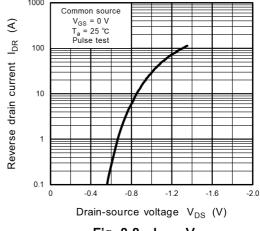


Fig. 8.7 R<sub>DS(ON)</sub> - T<sub>a</sub>



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Fig. 8.8 IDR - VDS

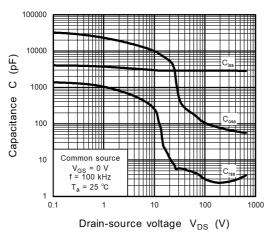


Fig. 8.9 C - V<sub>DS</sub>

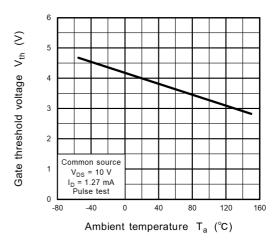
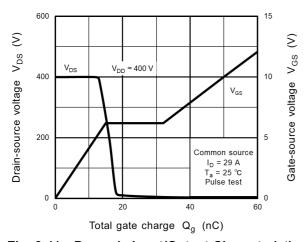


Fig. 8.10 V<sub>th</sub> - T<sub>a</sub>



**Dynamic Input/Output Characteristics** 

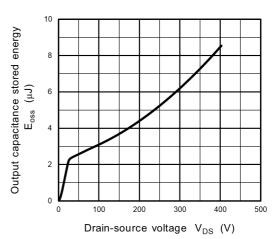


Fig. 8.12 Eoss - V<sub>DS</sub>



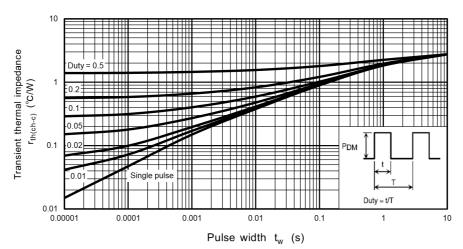


Fig. 8.13 r<sub>th</sub> - t<sub>w</sub> (Guaranteed Maximum)

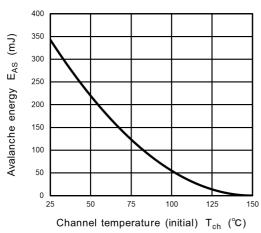
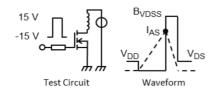


Fig. 8.14 E<sub>AS</sub> - T<sub>ch</sub> (Guaranteed Maximum)



$$V_{DD} = 90 \text{ V}, L = 18.0 \text{ mH}$$
  $E_{AS} = \frac{1}{2} \cdot L \cdot I_{AS}^2 \cdot \left( \frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$ 

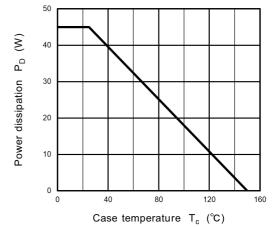


Fig. 8.15 P<sub>D</sub> - T<sub>c</sub> (Guaranteed Maximum)

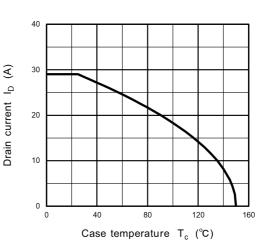


Fig. 8.17 I<sub>D</sub> - T<sub>c</sub> (Guaranteed Maximum)

Fig. 8.16 Test Circuit/Waveform



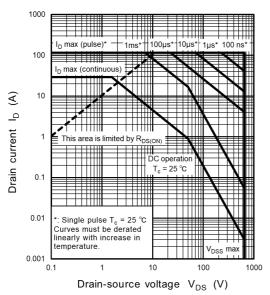


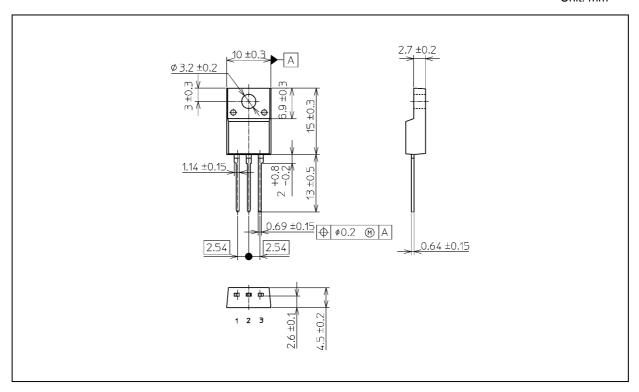
Fig. 8.18 Safe Operating Area (Guaranteed Maximum)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



## **Package Dimensions**

Unit: mm



Weight: 1.7 g (typ.)

Package Name(s)	
TOSHIBA: 2-10U1S	
Nickname: TO-220SIS	



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