TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (L^2 - π -MOSV)

2SK3205

Switching Regulator Applications DC-DC Converter, and Motor Drive Applications

• 4 V gate drive

• Low drain–source ON resistance $: RDS (ON) = 0.36 \Omega (typ.)$

• High forward transfer admittance $|Y_{fs}| = 4.5 \text{ S (typ.)}$

• Low leakage current $: IDSS = 100 \mu A \text{ (max) (VDS} = 150 \text{ V)}$

• Enhancement-mode : $V_{th} = 0.8 \sim 2.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	(Unit)
Drain-source voltage		V_{DSS}	150	A
Drain-gate voltage (Ro	_{SS} = 20 kΩ)	V_{DGR}	150	V
Gate-source voltage		V_{GSS}	±20	> v
Drain current	DC (Note 1)	ΙD	5	Α
	Pulse (Note 1)	I _{DP}	20	^
Drain power dissipation	n (Tc = 25°C)	PD	20	/w
Single pulse avalanche	e energy (Note 2)	EAS	71	æ
Avalanche current		TAR	5	A
Repetitive avalanche e	nergy (Note 3)	(E _{AR}))	2	\/mJ
Channel temperature		T _{ch}	150	°c
Storage temperature ra	ange	T _{stg}	-55~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	6.25	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	125	°C/W

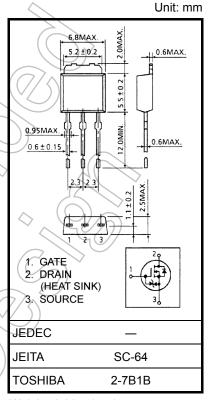
Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: V_{DD} = 50 V, T_{ch} = 25°C (initial), L = 4.2 mH, I_{AR} = 5 A, R_G = 25 Ω

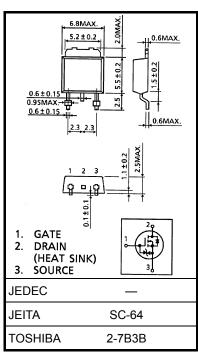
Note 3: Repetitive rating; Pulse width limited by maximum channel temperature.

This transistor is an electrostatic sensitive device.

Please handle with caution.



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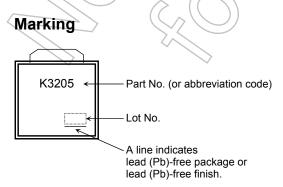


Electrical Characteristics (Ta = 25°C)

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 150 V, V _{GS} = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	150	_	_	V
Gate threshold v	oltage	V_{th}	V _{DS} = 10 V, I _D = 1 mA	0.8	/	2.0	V
Drain-source ON resistance		R _{DS} (ON)	V _{GS} = 4 V, I _D = 2.5 A	1	0.54	0.75	Ω
		R _{DS} (ON)	V _{GS} = 10 V, I _D = 2.5 A) 	0.36	0.5	
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 2.5 A	2.0	4.5	_	S
Input capacitano	е	C _{iss}		_	330	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	50	-	pF
Output capacitance		Coss		_	145		
Switching time	Rise time	tr	$V_{\rm GS}^{10{ m V}}$	- (10	⟩	
	Turn-on time	t _{on}			15) –	
	Fall time	t _f	4, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,		10	_	ns
	Turn-off time	t _{off}	$V_{DD} = 100V$ Duty $\leq 1\%$, $t_{W} = 10\mu s$) –	60	_	
Total gate charg plus gate-drain)		Qg			12	-	
Gate-source charge		Q _{gs}	$V_{DD} \approx 120 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$	_	8	_	nC
Gate-drain ("mil	ler") charge	Q _{gd}		_	4	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IDR		_	_	5	А
Pulse drain reverse current (Note 1)	I _{DRP}	-	_	_	20	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 5 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 5 A, V _{GS} = 0 V, dI _{DR} / dt = 100 A / μs		110	_	ns
Reverse recovery charge	Qrr	IDR - 3 A, VGS - 0 V, diDR / dt - 100 A / μs	_	0.47	_	nC



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