

CMOS Digital Integrated Circuits Silicon Monolithic

74AVC4T245FT

1. Functional Description

2-Bit×2 Dual-Supply Bus Transceiver with Configurable Power Supply

2. General

The 74AVC4T245FT is a dual power supply type high-speed CMOS 2-bit \times 2 bus transceiver that enables interfacing between two systems with power supply voltages from 0.8 V to 3.6 V.

The two supply voltages can be user-configurable within the operating range and the sequence of supply voltage ON/OFF can be freely set.

The Enable input \overline{OE} is H level, both A-bus and B-bus become floating state (high-impedance). When the transmission direction switching input DIR is set to "H", bus A becomes an input and bus B becomes an output, and when set to "L", bus A becomes an output and bus B becomes an input. The input (DIR and \overline{OE}) has a tolerant function that allows input of up to 3.6 V regardless of the supply voltage. When either power supply is at the GND level, the bus terminals are placed in a high impedance mode and a voltage of up to 3.6 V is allowed to be applied.

This function enables application to partial power-down interfaces.

All inputs are equipped with protection circuits to protect the devices from electrostatic discharge damage.

3. Features

- (1) Wide operating temperature range: $T_{opr} = -40$ to 125 °C
- (2) Wide supply voltage value: $V_{CCA} = 0.8$ to 3.6 V, $V_{CCB} = 0.8$ to 3.6 V
- (3) Bidirectional interface
- (4) High-speed operation: $t_{pd} = 3.6 \text{ ns (max)} (V_{CCA} = 3.3 \pm 0.3 \text{ V}, V_{CCB} = 3.3 \pm 0.3 \text{ V})$
- (5) Output current: $|I_{OH}|/I_{OL} = \pm 12 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$

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\begin{split} &|\: I_{OH}\:|\: / I_{OL} = \pm \: 9 \: \text{mA (min)} \: (V_{CC} = 2.3 \: \text{V}) \\ &|\: I_{OH}\:|\: / I_{OL} = \pm \: 6 \: \text{mA (min)} \: (V_{CC} = 1.65 \: \text{V}) \\ &|\: I_{OH}\:|\: / I_{OL} = \pm \: 4 \: \text{mA (min)} \: (V_{CC} = 1.4 \: \text{V}) \end{split}
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 $|I_{OH}|/I_{OL} = \pm 2 \text{ mA (min) (V}_{CC} = 1.1 \text{ V)}$

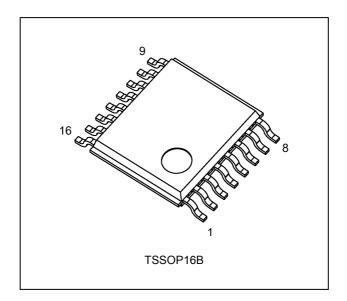
- (6) Small package: TSSOP16B
- (7) Low power dissipation: Suitable for battery-driven applications such as PDAs and cellular phones.

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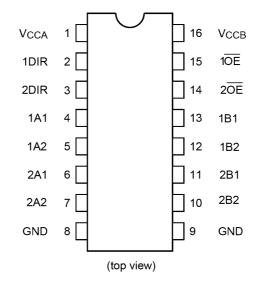
(8) 3.6 V tolerance and power-down protection are provided to all inputs and outputs.



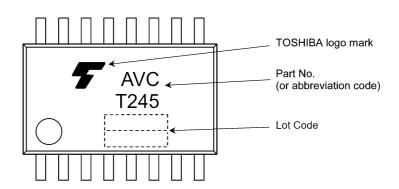
4. Packaging



5. Pin Assignment

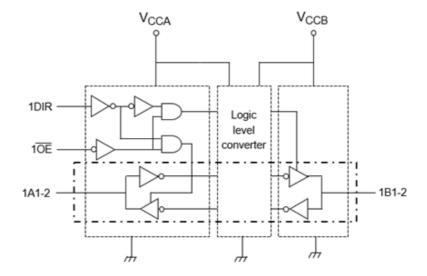


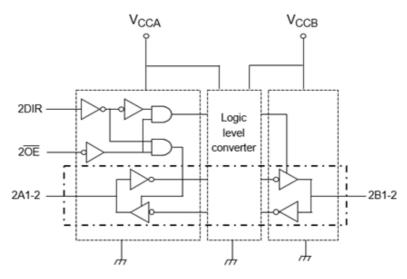
6. Marking





7. Block Diagram







8. Truth Table

Supply voltage V _{CCA} , V _{CCB}	Input nOE	Input nDIR	Input/Output Bus nA1, nA2	Input/Output Bus nB1, nB2	Function
0.8 to 3.6 V	L	L	Output	Input	A = B
0.8 to 3.6 V	L	Н	Input	Output	B = A
0.8 to 3.6 V	Н	X	Z	Z	Z
GND (Note)	X	Х	Z	Z	Z

X: Don't care

Z: High impedance

Note: If either V_{CCA} or V_{CCB} is at GND level, the device enters suspend mode (high impedance mode

for input and output).

9. Absolute Maximum Ratings (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CCA}		-0.5 to 4.6	V
	V _{CCB}		-0.5 to 4.6	
Input voltage (nDIR, nOE)	V _{IN}		-0.5 to 4.6	٧
Bus I/O voltage	V _{I/OA}	(Note 1)	-0.5 to 4.6	V
		(Note 2)	-0.5 to V _{CCA} + 0.5	
	V _{I/OB}	(Note 1)	-0.5 to 4.6	V
		(Note 2)	-0.5 to V _{CCB} + 0.5	
Input diode current	I _{IK}		-50	mA
I/O diode current	I _{I/OK}	(Note 3)	-50	mA
Output current	I _{OUTA}		±50	mA
	I _{OUTB}		±50	
V _{CC} /ground current per supply pin	I _{CCA}		100	mA
	I _{CCB}	1	100	
Power dissipation	P _D	(Note 4)	180	mW
Storage temperature	T _{stg}		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V_{CCA} , $V_{CCB} = 0$ V or output high impedance state.

Note 2: High (H) or Low (L) state. I_{OUT} absolute maximum rating must be observed.

Note 3: V_{OUT} < GND

Note 4: 180 mW in the range of T_a = -40 to 85 °C. From T_a = 85 to 125 °C a derating factor of -3.25 mW/°C shall be applied until 50 mW.



10. Operating Ranges (Note)

Characteristics	Symbol	Note	Test Condition	Rating	Unit
Supply voltage	V _{CCA}		_	0.8 to 3.6	V
	V _{CCB}			0.8 to 3.6	
Input voltage(nDIR, nOE)	V _{IN}		_	0 to 3.6	V
Bus I/O voltage	V _{I/OA}	(Note 1)	_	0 to 3.6	V
		(Note 2)		0 to V _{CCA}	
	V _{I/OB}	(Note 1)		0 to 3.6	
		(Note 2)		0 to V _{CCB}	
Output current	I _{OUTA}		V _{CCA} = 3.0 to 3.6 V	±12	mA
			V _{CCA} = 2.3 to 2.7 V	±9	
			V _{CCA} = 1.65 to 1.95 V	±6	
			V _{CCA} = 1.4 to 1.6 V	±4	
			V _{CCA} = 1.1 to 1.3 V	±2	
	I _{OUTB}		V _{CCB} = 3.0 to 3.6 V	±12	
			V _{CCB} = 2.3 to 2.7 V	±9	1
			V _{CCB} = 1.65 to 1.95 V	±6	1
			V _{CCB} = 1.4 to 1.6 V	±4	
			V _{CCB} = 1.1 to 1.3 V	±2	1
Operating temperature	T _{opr}		_	-40 to 125	°C
Input rise and fall times	dt/dv	(Note 3)	V _{CC} = 0.8 V	0 to 20	ns/V
			V _{CC} = 1.2 V	0 to 20	7 l
			V _{CC} = 1.65 to 1.95 V	0 to 20	
			V _{CC} = 2.3 to 2.7 V	0 to 20	7 l
			V _{CC} = 3.0 to 3.6 V	0 to 10	

Note: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs and bus inputs must be tied to either $V_{\mbox{\footnotesize{CC}}}$ or GND.

Note 1: V_{CCA} , V_{CCB} = 0 V or output high impedance state.

Note 2: High (H) or Low (L) state.

Note 3: $V_{CC} = V_{CCA}$, V_{CCB}



11. Electrical Characteristics

11.1. DC Characteristics (Unless otherwise specified, T_a = -40 to 85 °C)

Characteristics	Symbol	Test Condition		V _{CCA} (V)	V _{CCB} (V)	Min	Max	Unit
High-level input voltage	V _{IHA}	nA, nDIR, nOE		0.8	0.8 to 3.6	$V_{CCA} \times 0.70$		V
				1.1 to 1.95	0.8 to 3.6	$V_{CCA} \times 0.70$	ı	
				2.3 to 2.7	0.8 to 3.6	1.6		
				3.0 to 3.6	0.8 to 3.6	2.0	_	
	V _{IHB}	nB		0.8 to 3.6	0.8	V _{CCB} × 0.70	_	V
				0.8 to 3.6	1.1 to 1.95	V _{CCB} × 0.70	_	
				0.8 to 3.6	2.3 to 2.7	1.6	_	
				0.8 to 3.6	3.0 to 3.6	2.0	_	
Low-level input voltage	V _{ILA}	nA, nDIR, nOE		0.8	0.8 to 3.6	_	$V_{CCA} \times 0.30$	V
	İ			1.1 to 1.95	0.8 to 3.6	_	$V_{CCA} \times 0.30$	
	İ			2.3 to 2.7	0.8 to 3.6	_	0.7	
				3.0 to 3.6	0.8 to 3.6	_	0.9	
	V _{ILB}	nB		0.8 to 3.6	0.8	_	$V_{CCB} \times 0.30$	V
				0.8 to 3.6	1.1 to 1.95	_	$V_{CCB} \times 0.30$	
				0.8 to 3.6	2.3 to 2.7	_	0.7	
				0.8 to 3.6	3.0 to 3.6	_	0.9	
High-level output voltage	V _{OHA}	Output H	I _{OH} = -0.1 mA	0.8 to 3.6	0.8 to 3.6	V _{CCA} -0.1	_	V
			I _{OH} = -2 mA	1.1	1.1	0.85	_	
			I _{OH} = -4 mA	1.4	1.4	1.05	_	
			I _{OH} = -6 mA	1.65	1.65	1.2	_	
			I _{OH} = -9 mA	2.3	2.3	1.75	_	
			I _{OH} = -12 mA	3.0	3.0	2.3	_	
	V _{OHB}	Output H	I _{OH} = -0.1 mA	0.8 to 3.6	0.8 to 3.6	V _{CCB} -0.1	_	
			I _{OH} = -2 mA	1.1	1.1	0.85	_	
			I _{OH} = -4 mA	1.4	1.4	1.05	_	
			I _{OH} = -6 mA	1.65	1.65	1.2	_	
			I _{OH} = -9 mA	2.3	2.3	1.75	_	
			I _{OH} = -12 mA	3.0	3.0	2.3	_	
Low-level output voltage	VOLA	Output L	I _{OL} = 0.1 mA	0.8 to 3.6	0.8 to 3.6	_	0.1	V
, ,	02,1	•	I _{OL} = 2 mA	1.1	1.1	_	0.25	
			I _{OL} = 4 mA	1.4	1.4	_	0.35	
			I _{OL} = 6 mA	1.65	1.65	_	0.45	
			I _{OL} = 9 mA	2.3	2.3	_	0.55	
			I _{OL} = 12 mA	3.0	3.0	_	0.7	
	V _{OLB}	Output L	I _{OL} = 0.1 mA	0.8 to 3.6	0.8 to 3.6	_	0.1	
		,	I _{OL} = 2 mA	1.1	1.1	_	0.25	
			I _{OL} = 4 mA	1.4	1.4	_	0.35	
			I _{OL} = 6 mA	1.65	1.65	_	0.45	
			I _{OL} = 9 mA	2.3	2.3	_	0.55	
			I _{OL} = 12 mA	3.0	3.0	_	0.7	



Characteristics	Symbol	Note	Test Condition	V _{CCA} (V)	V _{CCB} (V)	Min	Max	Unit
3-state output OFF-state leakage current	I _{OZA}	(Note 1)	Function OFF State, V _{IOA} = 0 V or 3.6 V	0.8 to 3.6	0.8 to 3.6		±5	μА
	I _{OZB}		Function OFF State, V _{IOB} = 0 V or 3.6 V	0.8 to 3.6	0.8 to 3.6		±5	
Input leakage current	I _{IN}	(Note 1)	Input = 0 V to 3.6 V	0.8 to 3.6	0 to 3.6	_	±1	μА
Power-OFF leakage	I _{OFFA}	(Note 1)	V _{IOA} = 0 V to 3.6 V	0	0.8 to 3.6	_	±5	μА
current	I _{OFFB}		V _{IOB} = 0 V to 3.6 V	0.8 to 3.6	0	_	±5	
Quiescent supply	I _{CCA}	(Note 1)	_	0.8 to 3.6	0.8 to 3.6	_	8	μА
current				3.6	0	_	8	
	I _{CCB}	(Note 1)	_	0.8 to 3.6	0.8 to 3.6	_	8	μА
				0	3.6	_	8	
	I _{CCTA}		V _{CCA} - 0.6V (per input)	3.0 to 3.6	0.8 to 3.6	_	500	μА
	Ісств		V _{CCB} - 0.6V (per input)	0.8 to 3.6	3.0 to 3.6	_	500	

Note 1: Fix the input terminal to each power supply terminal or 0V.



11.2. DC Characteristics (Unless otherwise specified, T_a = -40 to 125 °C)

Characteristics	Symbol	Test Condition	ı	V _{CCA} (V)	V _{CCB} (V)	Min	Max	Unit
High-level input voltage	V _{IHA}	nA, nDIR, nOE		0.8	0.8 to 3.6	$V_{CCA} \times 0.70$	_	V
				1.1 to 1.95	0.8 to 3.6	$V_{CCA} \times 0.70$	_	
				2.3 to 2.7	0.8 to 3.6	1.6	_	
				3.0 to 3.6	0.8 to 3.6	2.0	_	
	V _{IHB}	nB		0.8 to 3.6	0.8	V _{CCB} × 0.70	_	
				0.8 to 3.6	1.1 to 1.95	V _{CCB} × 0.70	_	
				0.8 to 3.6	2.3 to 2.7	1.6	_	
				0.8 to 3.6	3.0 to 3.6	2.0	_	
Low-level input voltage	V _{ILA}	nA, nDIR, nOE		0.8	0.8 to 3.6	_	$V_{CCA} \times 0.30$	V
				1.1 to 1.95	0.8 to 3.6	_	$V_{CCA} \times 0.30$	
				2.3 to 2.7	0.8 to 3.6	_	0.7	
				3.0 to 3.6	0.8 to 3.6	_	0.9	
	V _{ILB}	nB		0.8 to 3.6	0.8	_	$V_{CCB} \times 0.30$	
				0.8 to 3.6	1.1 to 1.95	_	$V_{CCB} \times 0.30$	
				0.8 to 3.6	2.3 to 2.7	_	0.7	
				0.8 to 3.6	3.0 to 3.6	_	0.9	
High-level output voltage	V _{OHA}	Output H	I _{OH} = -0.1 mA	0.8 to 3.6	0.8 to 3.6	V _{CCA} -0.1	_	V
			I _{OH} = -2 mA	1.1	1.1	0.85	_	
			I _{OH} = -4 mA	1.4	1.4	1.05	_	
			I _{OH} = -6 mA	1.65	1.65	1.2	_	
			I _{OH} = -9 mA	2.3	2.3	1.75	_	
			I _{OH} = -12 mA	3.0	3.0	2.3	_	
	V _{OHB}	Output H	I _{OH} = -0.1 mA	0.8 to 3.6	0.8 to 3.6	V _{CCB} -0.1	_	
			I _{OH} = -2 mA	1.1	1.1	0.85	_	
			I _{OH} = -4 mA	1.4	1.4	1.05	_	
			I _{OH} = -6 mA	1.65	1.65	1.2	_	
			I _{OH} = -9 mA	2.3	2.3	1.75	_	1
			I _{OH} = -12 mA	3.0	3.0	2.3	_	
Low-level output voltage	V _{OLA}	Output L	I _{OL} = 0.1 mA	0.8 to 3.6	0.8 to 3.6	_	0.1	V
			I _{OL} = 2 mA	1.1	1.1	_	0.25	
			I _{OL} = 4 mA	1.4	1.4	_	0.35	
			I _{OL} = 6 mA	1.65	1.65	_	0.45	
			I _{OL} = 9 mA	2.3	2.3	_	0.55	1
			I _{OL} = 12 mA	3.0	3.0	_	0.7	
	V _{OLB}	Output L	I _{OL} = 0.1 mA	0.8 to 3.6	0.8 to 3.6	_	0.1	
			I _{OL} = 2 mA	1.1	1.1	_	0.25	1
			I _{OL} = 4 mA	1.4	1.4	_	0.35	
			I _{OL} = 6 mA	1.65	1.65	_	0.45	
			I _{OL} = 9 mA	2.3	2.3	_	0.55	
			I _{OL} = 12 mA	3.0	3.0	_	0.7	

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Characteristics	Symbol	Note	Test Condition	V _{CCA} (V)	V _{CCB} (V)	Min	Max	Unit
3-state output OFF-state leakage current	I _{OZA}	(Note 1)	Function OFF State, V _{IOA} = 0 V or 3.6 V	0.8 to 3.6	0.8 to 3.6		±7.5	μА
	I _{OZB}		Function OFF State, V _{IOB} = 0 V or 3.6 V	0.8 to 3.6	0.8 to 3.6		±7.5	
Input leakage current	I _{IN}	(Note 1)	Input = 0 V to 3.6 V	0.8 to 3.6	0 to 3.6	_	±5	μΑ
Power-OFF leakage	I _{OFFA}	(Note 1)	V _{IOA} = 0 V to 3.6 V	0	0.8 to 3.6		±10	μΑ
current	I _{OFFB}		V _{IOB} = 0 V to 3.6 V	0.8 to 3.6	0	_	±10	
Quiescent supply	I _{CCA}	(Note 1)	_	0.8 to 3.6	0.8 to 3.6	_	18	μА
current				3.6	0		18	
	I _{CCB}	(Note 1)	_	0.8 to 3.6	0.8 to 3.6	_	18	μА
				0	3.6	_	18	
	I _{CCTA}		V _{CCA} - 0.6 V (per input)	3.0 to 3.6	0.8 to 3.6	_	500	μА
	Ісств		V _{CCB} - 0.6 V (per input)	0.8 to 3.6	3.0 to 3.6	_	500	

Note 1: Fix the input terminal to each power supply terminal or 0V.

11.3. AC Characteristics (Note) (V_{CCA} = 0.8 V, T_a = 25 °C)

Characteristics	Symbol	V _{CCB} (V) 0.8 V typ.	V _{CCB} (V) 1.2 V typ.	V _{CCB} (V) 1.5 V typ.	V _{CCB} (V) 1.8 V typ.	V _{CCB} (V) 2.5 V typ.	V _{CCB} (V) 3.3 V typ.	Unit
Propagation delay time(B \rightarrow A)	t _{PLH} /t _{PHL}	14.0	10.5	9.6	9.1	8.8	8.9	ns
Propagation delay time (B \rightarrow A)		14.0	9.5	8.7	8.3	7.9	7.7	
3-state output disable time $(\overline{OE} \to A)$	t _{PLZ} /t _{PHZ}	20.9	21.1	21.1	21.2	21.4	21.7	
3-state output disable time $(\overline{OE} \to B)$		23.4	14.5	12.6	11.8	10.5	10.1	
3-state output enable time $(\overline{OE} \to A)$	t _{PZL} /t _{PZH}	22.6	22.7	22.8	22.8	22.8	22.7	
3-state output enable time $(\overline{OE} \to B)$		37.6	20.6	17.6	16.2	15.0	14.7	

Note: See Figure 12.1, 13.1, 13.2, table 12.1.1, 12.1.2, 13.1.1 for the measurement circuit.

11.4. AC Characteristics (Note) (V_{CCB} = 0.8 V, T_a = 25 °C)

Characteristics	Symbol	V _{CCA} (V) 0.8 V typ.	V _{CCA} (V) 1.2 V typ.	V _{CCA} (V) 1.5 V typ.	V _{CCA} (V) 1.8 V typ.	V _{CCA} (V) 2.5 V typ.	V _{CCA} (V) 3.3 V typ.	Unit
Propagation delay time $(A \rightarrow B)$	t _{PLH} /t _{PHL}	14.0	9.5	8.7	8.3	7.9	7.7	ns
Propagation delay time (B \rightarrow A)		14.0	10.5	9.6	9.1	8.8	8.9	
3-state output disable time $(\overline{OE} \to A)$	t _{PLZ} /t _{PHZ}	20.9	10.6	7.9	6.3	3.8	2.6	
3-state output disable time $(\overline{OE} \to B)$		23.4	19.4	18.4	18.0	17.4	17.1	
3-state output enable time $(\overline{OE} \to A)$	t _{PZL} /t _{PZH}	22.6	9.4	6.9	5.8	4.7	4.4	
3-state output enable time $(\overline{\sf OE} \to \sf B)$		37.6	32.2	31.4	31.2	31.1	31.1	

Note: See Figure 12.1, 13.1, 13.2, table 12.1.1, 12.1.2, 13.1.1 for the measurement circuit.

11.5. AC Characteristics (Note) (V_{CCA} = 1.2 \pm 0.1 V, T_a = -40 to 85 °C)

Characteristics	Symbol	V _{CCB} (V) 1.2 ± 0.1 V Max	V _{CCB} (V) 1.5 ± 0.1 V Max	V _{CCB} (V) 1.8 ± 0.15 V Max	V _{CCB} (V) 2.5 ± 0.2 V Max	V _{CCB} (V) 3.3 ± 0.3 V Max	Unit
Propagation delay time $(A \rightarrow B)$	t _{PLH} /t _{PHL}	10.2	9.0	8.5	7.6	7.3	ns
Propagation delay time (B \rightarrow A)		10.2	8.2	7.5	6.7	6.4	
3-state output disable time $(\overline{OE} \to A)$	t _{PLZ} /t _{PHZ}	12.2	12.2	12.2	12.2	12.3	
3-state output disable time $(\overline{OE} \to B)$		13.6	10.8	9.5	7.9	7.3	
3-state output enable time $(\overline{OE} \to A)$	t _{PZL} /t _{PZH}	17.7	18.0	18.0	18.0	18.1	
3-state output enable time $(\overline{OE} \to B)$		27.7	20.0	17.0	13.7	12.5	

Note: See Figure 12.1, 13.1, 13.2, table 12.1.1, 12.1.2, 13.1.1 for the measurement circuit.



11.6. AC Characteristics (Note) ($V_{CCA} = 1.5 \pm 0.1 \text{ V}$, $T_a = -40 \text{ to } 85 ^{\circ}\text{C}$)

Characteristics	Symbol	V _{CCB} (V) 1.2 ± 0.1 V Max	V _{CCB} (V) 1.5 ± 0.1 V Max	V _{CCB} (V) 1.8 ± 0.15 V Max	V _{CCB} (V) 2.5 ± 0.2 V Max	V _{CCB} (V) 3.3 ± 0.3 V Max	Unit
Propagation delay time $(A \rightarrow B)$	t _{PLH} /t _{PHL}	8.2	6.9	6.5	5.9	5.5	ns
Propagation delay time $(B \rightarrow A)$		9.0	6.9	6.0	5.2	4.9	
3-state output disable time $(\overline{OE} \to A)$	t _{PLZ} /t _{PHZ}	8.4	8.4	8.4	8.3	8.2	
3-state output disable time $(\overline{OE} \to B)$		12.1	9.1	7.6	6.1	5.4	
3-state output enable time $(\overline{OE} \to A)$	t _{PZL} /t _{PZH}	11.6	11.7	11.9	11.8	11.8	
3-state output enable time $(\overline{OE} \to B)$		25.1	17.4	14.4	11.0	9.7	

Note: See Figure 12.1, 13.1, 13.2, table 12.1.1, 12.1.2, 13.1.1 for the measurement circuit.

11.7. AC Characteristics (Note) ($V_{CCA} = 1.8 \pm 0.15 \text{ V}$, $T_a = -40 \text{ to } 85 ^{\circ}\text{C}$)

Characteristics	Symbol	V _{CCB} (V) 1.2 ± 0.1 V Max	V _{CCB} (V) 1.5 ± 0.1 V Max	V _{CCB} (V) 1.8 ± 0.15 V Max	V _{CCB} (V) 2.5 ± 0.2 V Max	V _{CCB} (V) 3.3 ± 0.3 V Max	Unit
Propagation delay time $(A \rightarrow B)$	t _{PLH} /t _{PHL}	7.5	6.0	5.5	5.1	4.8	ns
Propagation delay time (B \rightarrow A)		8.5	6.5	5.5	4.6	4.3	
3-state output disable time $(\overline{OE} \to A)$	t _{PLZ} /t _{PHZ}	7.3	7.3	7.3	7.3	7.2	
3-state output disable time $(\overline{OE} \to B)$		12.6	9.3	7.7	6.0	5.2	
3-state output enable time $(\overline{OE} \to A)$	t _{PZL} /t _{PZH}	9.2	9.2	9.3	9.3	9.3	
3-state output enable time $(\overline{OE} \to B)$		24.2	16.5	13.4	10.0	8.6	

Note: See Figure 12.1, 13.1, 13.2, table 12.1.1, 12.1.2, 13.1.1 for the measurement circuit.

11.8. AC Characteristics (Note) ($V_{CCA} = 2.5 \pm 0.2 \text{ V}$, $T_a = -40 \text{ to } 85 ^{\circ}\text{C}$)

Characteristics	Symbol	V _{CCB} (V) 1.2 ± 0.1 V Max	V _{CCB} (V) 1.5 ± 0.1 V Max	V _{CCB} (V) 1.8 ± 0.15 V Max	V_{CCB} (V) 2.5 \pm 0.2 V Max	V_{CCB} (V) 3.3 ± 0.3 V Max	Unit
Propagation delay time $(A \rightarrow B)$	t _{PLH} /t _{PHL}	6.7	5.2	4.6	4.0	3.9	ns
Propagation delay time (B \rightarrow A)		7.6	5.9	5.1	4.0	3.5	
3-state output disable time $(\overline{OE} \to A)$	t _{PLZ} /t _{PHZ}	4.9	4.9	4.9	4.9	4.9	
3-state output disable time $(\overline{OE} \to B)$		11.8	8.5	7.0	5.3	4.5	
3-state output enable time $(\overline{OE} \to A)$	t _{PZL} /t _{PZH}	6.7	6.5	6.5	6.5	6.5	
3-state output enable time $(\overline{OE} \to B)$		23.7	15.8	12.5	9.0	7.5	

Note: See Figure 12.1, 13.1, 13.2, table 12.1.1, 12.1.2, 13.1.1 for the measurement circuit.

11.9. AC Characteristics (Note) (V_{CCA} = 3.3 \pm 0.3 V, T_a = -40 to 85 °C)

Characteristics	Symbol	V _{CCB} (V) 1.2 ± 0.1 V Max	V _{CCB} (V) 1.5 ± 0.1 V Max	V _{CCB} (V) 1.8 ± 0.15 V Max	V _{CCB} (V) 2.5 ± 0.2 V Max	$\begin{array}{c} \text{V}_{\text{CCB}}\left(\text{V}\right) \\ 3.3 \pm 0.3 \text{V} \\ \text{Max} \end{array}$	Unit
Propagation delay time $(A \rightarrow B)$	t _{PLH} /t _{PHL}	6.4	4.9	4.3	3.5	3.3	ns
Propagation delay time (B \rightarrow A)		7.3	5.5	4.8	3.9	3.3	
3-state output disable time $(\overline{OE} \to A)$	t _{PLZ} /t _{PHZ}	5.0	5.0	5.0	5.0	5.0	
3-state output disable time $(\overline{OE} \to B)$		15.7	11.4	9.3	6.5	5.3	
3-state output enable time $(\overline{OE} \to A)$	t _{PZL} /t _{PZH}	5.9	5.3	5.3	5.2	5.2	
3-state output enable time $(\overline{OE} \to B)$		23.7	15.5	12.1	8.6	7.1	

Note: See Figure 12.1, 13.1, 13.2, table 12.1.1, 12.1.2, 13.1.1 for the measurement circuit.



11.10. AC Characteristics (Note) (V_{CCA} = 1.2 \pm 0.1 V, T_a = -40 to 125 °C)

Characteristics	Symbol	V _{CCB} (V) 1.2 ± 0.1 V Max	V _{CCB} (V) 1.5 ± 0.1 V Max	V _{CCB} (V) 1.8 ± 0.15 V Max	V _{CCB} (V) 2.5 ± 0.2 V Max	V _{CCB} (V) 3.3 ± 0.3 V Max	Unit
Propagation delay time $(A \rightarrow B)$	t _{PLH} /t _{PHL}	10.5	9.4	8.9	7.9	7.6	ns
Propagation delay time (B \rightarrow A)		10.5	8.6	7.9	7.0	6.7	
3-state output disable time $(\overline{OE} \to A)$	t _{PLZ} /t _{PHZ}	12.5	12.5	12.5	12.5	12.6	
3-state output disable time $(\overline{OE} \to B)$		14.1	11.3	10.0	8.2	7.6	
3-state output enable time $(\overline{OE} \to A)$	t _{PZL} /t _{PZH}	18.3	18.5	18.6	18.6	18.7	
3-state output enable time $(\overline{OE} \to B)$		28.6	21.0	18.0	14.5	13.1	

Note: See Figure 12.1, 13.1, 13.2, table 12.1.1, 12.1.2, 13.1.1 for the measurement circuit.

11.11. AC Characteristics (Note) ($V_{CCA} = 1.5 \pm 0.1 \text{ V}$, $T_a = -40 \text{ to } 125 ^{\circ}\text{C}$)

Characteristics	Symbol	V _{CCB} (V) 1.2 ± 0.1 V Max	V _{CCB} (V) 1.5 ± 0.1 V Max	V _{CCB} (V) 1.8 ± 0.15 V Max	V _{CCB} (V) 2.5 ± 0.2 V Max	V _{CCB} (V) 3.3 ± 0.3 V Max	Unit
Propagation delay time $(A \rightarrow B)$	t _{PLH} /t _{PHL}	8.6	7.3	6.9	6.3	5.8	ns
Propagation delay time (B \rightarrow A)		9.4	7.3	6.4	5.5	5.2	
3-state output disable time $(\overline{OE} \to A)$	t _{PLZ} /t _{PHZ}	8.8	8.9	8.8	8.8	8.7	
3-state output disable time $(\overline{OE} \to B)$		12.8	9.6	8.0	6.4	5.7	
3-state output enable time $(\overline{OE} \to A)$	t _{PZL} /t _{PZH}	12.3	12.5	12.6	12.5	12.5	
3-state output enable time $(\overline{OE} \to B)$		26.2	18.4	15.4	11.8	10.3	

Note: See Figure 12.1, 13.1, 13.2, table 12.1.1, 12.1.2, 13.1.1 for the measurement circuit.

11.12. AC Characteristics (Note) ($V_{CCA} = 1.8 \pm 0.15 \text{ V}$, $T_a = -40 \text{ to } 125 ^{\circ}\text{C}$)

Characteristics	Symbol	V _{CCB} (V) 1.2 ± 0.1 V Max	V _{CCB} (V) 1.5 ± 0.1 V Max	V _{CCB} (V) 1.8 ± 0.15 V Max	V_{CCB} (V) 2.5 \pm 0.2 V Max	V_{CCB} (V) 3.3 ± 0.3 V Max	Unit
Propagation delay time $(A \rightarrow B)$	t _{PLH} /t _{PHL}	7.9	6.4	5.9	5.5	5.1	ns
Propagation delay time (B \rightarrow A)		8.9	6.9	5.9	4.9	4.6	
3-state output disable time $(\overline{OE} \to A)$	t _{PLZ} /t _{PHZ}	7.7	7.7	7.6	7.6	7.5	
3-state output disable time $(\overline{OE} \to B)$		13.4	9.8	8.1	6.3	5.5	
3-state output enable time $(\overline{OE} \to A)$	t _{PZL} /t _{PZH}	9.9	9.9	9.9	10.0	10.0	
3-state output enable time $(\overline{OE} \to B)$		25.1	17.5	14.4	10.7	9.2	

Note: See Figure 12.1, 13.1, 13.2, table 12.1.1, 12.1.2, 13.1.1 for the measurement circuit.

11.13. AC Characteristics (Note) (V_{CCA} = 2.5 \pm 0.2 V, T_a = -40 to 125 °C)

Characteristics	Symbol	V _{CCB} (V) 1.2 ± 0.1 V Max	V _{CCB} (V) 1.5 ± 0.1 V Max	V _{CCB} (V) 1.8 ± 0.15 V Max	V_{CCB} (V) 2.5 \pm 0.2 V Max	V _{CCB} (V) 3.3 ± 0.3 V Max	Unit
Propagation delay time $(A \rightarrow B)$	t _{PLH} /t _{PHL}	7.0	5.5	4.9	4.3	4.2	ns
Propagation delay time (B \rightarrow A)		7.9	6.3	5.5	4.3	3.8	
3-state output disable time $(\overline{OE} \to A)$	t _{PLZ} /t _{PHZ}	5.2	5.2	5.2	5.2	5.2	
3-state output disable time $(\overline{OE} \to B)$		12.6	8.9	7.3	5.6	4.7	
3-state output enable time $(\overline{OE} \to A)$	t _{PZL} /t _{PZH}	7.2	7.0	6.9	6.9	7.0	
3-state output enable time $(\overline{OE} \to B)$		24.5	16.8	13.4	9.6	8.0	

Note: See Figure 12.1, 13.1, 13.2, table 12.1.1, 12.1.2, 13.1.1 for the measurement circuit.



11.14. AC Characteristics (Note) (V_{CCA} = 3.3 \pm 0.3 V, T_a = -40 to 125 °C)

Characteristics	Symbol	V _{CCB} (V) 1.2 ± 0.1 V Max	V _{CCB} (V) 1.5 ± 0.1 V Max	V _{CCB} (V) 1.8 ± 0.15 V Max	V _{CCB} (V) 2.5 ± 0.2 V Max	V _{CCB} (V) 3.3 ± 0.3 V Max	Unit
Propagation delay time $(A \rightarrow B)$	t _{PLH} /t _{PHL}	6.7	5.2	4.6	3.8	3.6	ns
Propagation delay time (B \rightarrow A)		7.6	5.8	5.1	4.2	3.6	
3-state output disable time $(\overline{OE} \to A)$	t _{PLZ} /t _{PHZ}	5.2	5.2	5.2	5.2	5.2	
3-state output disable time $(\overline{OE} \to B)$		16.3	11.8	9.6	6.7	5.5	
3-state output enable time $(\overline{OE} \to A)$	t _{PZL} /t _{PZH}	6.3	5.7	5.7	5.6	5.6	
3-state output enable time $(\overline{OE} \to B)$		24.5	16.5	13.0	9.2	7.6	

Note: See Figure 12.1, 13.1, 13.2, table 12.1.1, 12.1.2, 13.1.1 for the measurement circuit.

11.15. Capacitive Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Note	Test Condition	V _{CCA} , V _{CCB} 0.8 V Typ.	V _{CCA} , V _{CCB} 1.2 V Typ.	V _{CCA} , V _{CCB} 1.5 V Typ.	V _{CCA} , V _{CCB} 1.8 V Typ.	V _{CCA} , V _{CCB} 2.5 V Typ.	V _{CCA} , V _{CCB} 3.3 V Typ.	Unit
Input capacitance	C _{IN}		V _{IN} = 0 V or 3.3 V		_	_	_		4	pF
Bus I/O capacitance	C _{I/OA}		An = OFF, V _{IOA} = 0 V or 3.3 V		_	_	_		5	pF
	C _{I/OB}		Bn = OFF, V _{IOB} = 0 V or 3.3 V		_	_	_	_	5	
Power dissipation	C _{PDA}	(Note 1)	$A \rightarrow B$	1	2	2	2	2	2	pF
capacitance			$B \rightarrow A$	9	11	11	12	14	17	
	C _{PDB}	(Note 1)	$A \rightarrow B$	9	11	11	12	14	17	pF
			$B \rightarrow A$	1	2	2	2	2	2	

Note 1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation. $I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{|N} + I_{CC}/4 \text{ (per bit)}$



12. AC Test Circuit

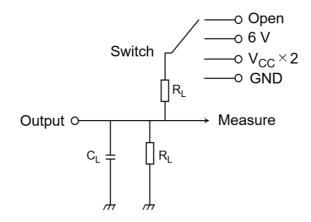


Fig. 12.1 AC Test Circuit

Table 12.1.1 Parameter for AC Test Circuit

Parameter	Switch
t _{PLH} , t _{PHL}	Open
t_{PLZ} , t_{PZL}	V _{CC} × 2
t _{PHZ} , t _{PZH}	GND

Table 12.1.2 Parameter for AC Test Circuit

Symbol	V _{CC} = 3.3 ± 0.3 V	V _{CC} = 2.5 ± 0.2 V	V _{CC} = 1.8 ± 0.15 V	V _{CC} = 1.5 ± 0.1 V	V _{CC} = 1.2 ± 0.1 V	V _{CC} = 0.8 V
R_L	2 kΩ	2 kΩ	2 kΩ	2 kΩ	2 kΩ	10 kΩ
C_{L}	15 pF	15 pF	15 pF	15 pF	15 pF	5 pF



13. AC Waveform

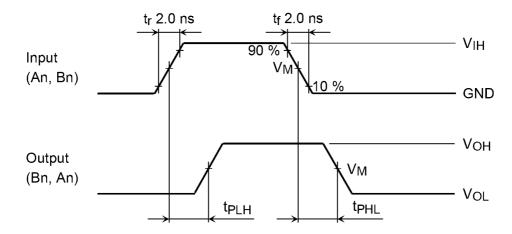


Fig. 13.1 tpLH, tpHL

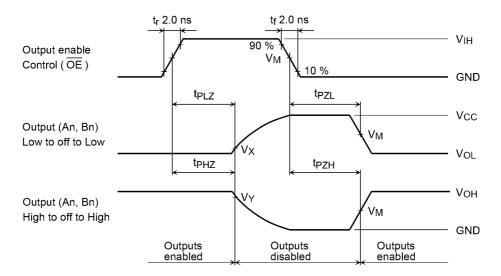


Fig. 13.2 t_{PLZ}, t_{PHZ}, t_{PZL}, t_{PZH}

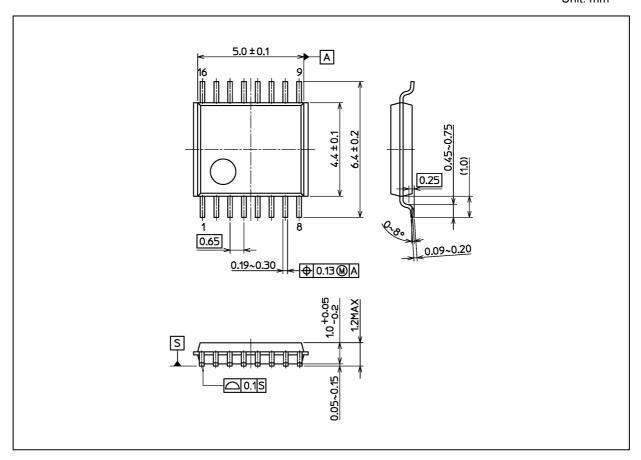
Table 13.1.1 AC Waveform Symbols

Symbol	$V_{CC} = 3.3 \pm 0.3 \text{ V}$	$V_{CC} = 3.3 \pm 0.3 \text{ V}$ $V_{CC} = 2.5 \pm 0.2 \text{ V}$ $V_{CC} = 1.8 \pm 0.15 \text{ V}$ $V_{CC} = 1.8 \pm 0.15 \text{ V}$		V _{CC} = 0.8 V
V_{IH}	V _{CC}	V _{CC}	V_{CC}	V _{CC}
V_{M}	V _{CC} /2	V _{CC} /2	V _{CC} /2	V _{CC} /2
V _X	V _{OL} + 0.3 V	V _{OL} + 0.15 V	V _{OL} + 0.1 V	V _{OL} + 0.1 V
V_{Y}	V _{OH} - 0.3 V	V _{OH} - 0.15 V	V _{OH} - 0.1 V	V _{OH} - 0.1 V



Package Dimensions

Unit: mm



Weight: 0.055 g (typ.)

Package Name(s)
Nickname: TSSOP16B



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