

TLP3640A

1. Applications

- Factory Automation (FA)
- Programmable Logic Controllers (PLCs)
- Security Systems
- Measuring Instruments
- Mechanical relay replacements

2. General

The Toshiba TLP3640A consists of an infrared LED optically coupled to a photo-MOSFET in a SO4 package. This photorelay has higher output current rating than phototransistor-type photocoupler. hence, it is suitable for use as On/Off control for high current.

3. Features

- (1) Halogen-free
For details, see "Devices in Halogen-Free Resin Packages" at the end of this datasheet.
- (2) Operating temperature range: -40 to 110 °C
- (3) Normally opened (1-Form-A)
- (4) OFF-state output terminal voltage: 60 V (min)
- (5) Trigger LED current: 3 mA (max)
- (6) ON-state current: 1 A (max)
- (7) ON-state resistance: 0.3 Ω (max)
- (8) Isolation voltage: 3750 Vrms (min)
- (9) Safety standards
UL-recognized: UL 1577, File No.E67349
cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349
VDE-approved: EN IEC 60747-5-5 (**Note 1**)

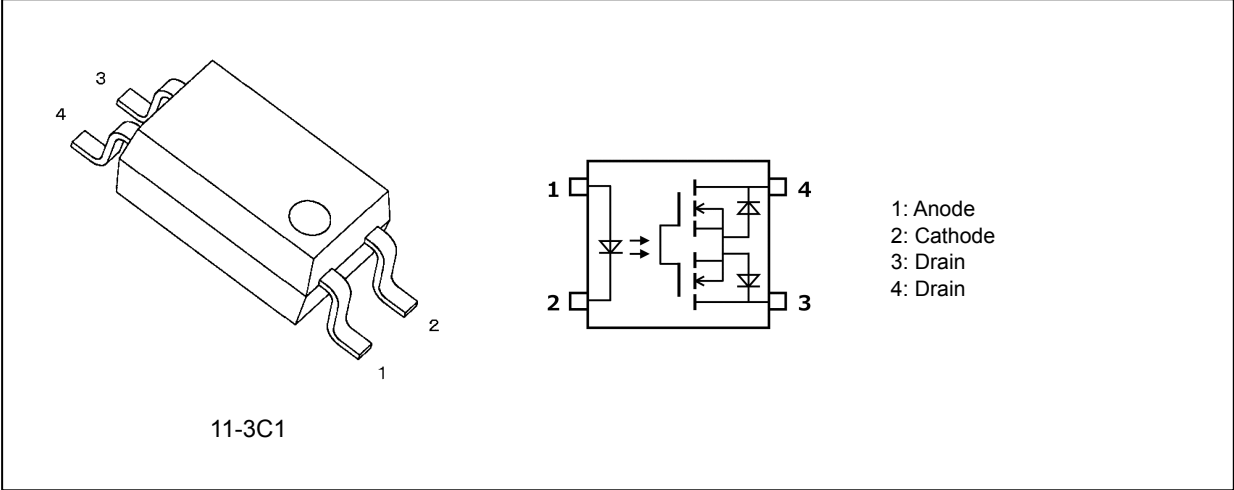
Note 1: When a VDE approved type is needed, please designate the **Option (V4)**.

Table 3.1 Mechanical Parameters

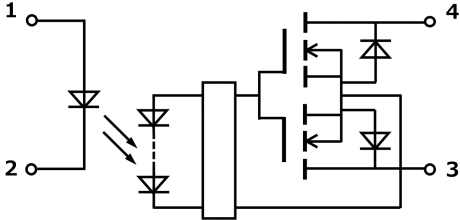
Characteristics	TLP3640A	Unit
Creepage distances	5.0 (min)	mm
Clearance distances	5.0 (min)	
Internal isolation thickness	0.2 (min)	

Start of commercial production
2024-10

4. Packaging and Pin Assignment



5. Internal Circuit



6. Absolute Maximum Ratings (Note) (Unless otherwise specified, $T_a = 25\text{ }^\circ\text{C}$)

	Characteristics	Symbol	Note	Rating	Unit
LED	Input forward current	I_F		30	mA
	Input forward current derating ($T_a \geq 95\text{ }^\circ\text{C}$)	$\Delta I_F/\Delta T_a$		-1	mA/ $^\circ\text{C}$
	Input reverse voltage	V_R		6	V
	Input power dissipation	P_D		60	mW
	Input power dissipation derating ($T_a \geq 95\text{ }^\circ\text{C}$)	$\Delta P_D/\Delta T_a$		-2	mW/ $^\circ\text{C}$
	Junction temperature	T_j		125	$^\circ\text{C}$
Detector	OFF-state output terminal voltage	V_{OFF}		60	V
	ON-state current	I_{ON}		1	A
	ON-state current derating ($T_a \geq 25\text{ }^\circ\text{C}$)	$\Delta I_{ON}/\Delta T_a$		-10	mA/ $^\circ\text{C}$
	ON-state current (pulsed) ($t = 100\text{ ms}$, duty = 1/10)	I_{ONP}		3	A
	Output power dissipation	P_O		300	mW
	Output power dissipation derating ($T_a \geq 25\text{ }^\circ\text{C}$)	$\Delta P_O/\Delta T_a$		-3	mW/ $^\circ\text{C}$
	Junction temperature	T_j		125	$^\circ\text{C}$
Common	Storage temperature	T_{stg}		-55 to 125	$^\circ\text{C}$
	Operating temperature	T_{opr}		-40 to 110	$^\circ\text{C}$
	Lead soldering temperature (10 s)	T_{sol}		260	$^\circ\text{C}$
	Isolation voltage (AC, 60 s, R.H. $\leq 60\%$)	BV_S	(Note 1)	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 1: This device is considered as a two-terminal device: Pins 1 and 2 are shorted together, and pins 3 and 4 are shorted together.

7. Electrical Characteristics (Unless otherwise specified, $T_a = 25\text{ }^\circ\text{C}$)

	Characteristics	Symbol	Note	Test Condition	Min	Typ.	Max	Unit
LED	Input forward voltage	V_F		$I_F = 10\text{ mA}$	1.50	1.65	1.80	V
	Input reverse current	I_R		$V_R = 6\text{ V}$	—	—	10	μA
	Input capacitance	C_t		$V = 0\text{ V}$, $f = 1\text{ MHz}$	—	40	—	pF
Detector	OFF-state current	I_{OFF}		$V_{OFF} = 60\text{ V}$	—	0.001	1	μA
	Output capacitance	C_{OFF}		$V = 0\text{ V}$, $f = 1\text{ MHz}$	—	95	—	pF

8. Coupled Electrical Characteristics (Unless otherwise specified, $T_a = 25\text{ }^\circ\text{C}$)

Characteristics	Symbol	Note	Test Condition	Min	Typ.	Max	Unit
Trigger LED current	I_{FT}		$I_{ON} = 1\text{ A}$	—	0.6	3	mA
Return LED current	I_{FC}		$I_{OFF} = 10\text{ }\mu\text{A}$	0.1	—	—	mA
ON-state resistance	R_{ON}		$I_{ON} = 1\text{ A}$, $I_F = 5\text{ mA}$, $t < 1\text{ s}$	—	0.14	0.3	Ω

9. Isolation Characteristics (Unless otherwise specified, $T_a = 25\text{ }^\circ\text{C}$)

Characteristics	Symbol	Note	Test Condition	Min	Typ.	Max	Unit
Total capacitance (input to output)	C_S	(Note 1)	$V_S = 0\text{ V}$, $f = 1\text{ MHz}$	—	0.4	—	pF
Isolation resistance	R_S	(Note 1)	$V_S = 500\text{ V}$, R.H. $\leq 60\%$	10^{12}	10^{14}	—	Ω
Isolation voltage	BV_S	(Note 1)	AC, 60 s	3750	—	—	Vrms

Note 1: This device is considered as a two-terminal device: Pins 1 and 2 are shorted together, and pins 3 and 4 are shorted together.

10. Switching Characteristics (Unless otherwise specified, $T_a = 25\text{ }^\circ\text{C}$)

Characteristics	Symbol	Note	Test Condition	Min	Typ.	Max	Unit
Turn-on time	t_{ON}		See Fig. 10.1	—	0.13	0.5	ms
Turn-off time	t_{OFF}		$R_L = 200\ \Omega$, $V_{DD} = 20\text{ V}$, $I_F = 5\text{ mA}$	—	0.04	0.2	

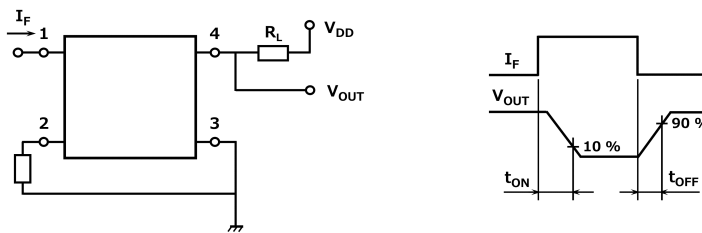


Fig. 10.1 Switching Time Test Circuit and Waveform

11. Usage Considerations

When the LED side is OFF, and time-varying voltage, such as pulse wave, is applied to the detector side, the change rate of voltage ($|dV/dt|$) shall be set to $0.1\text{ kV}/\mu\text{s}$ or less for stable operation.

12. Characteristics Curves and Circuit Connections

12.1. Characteristics Curves (Note)

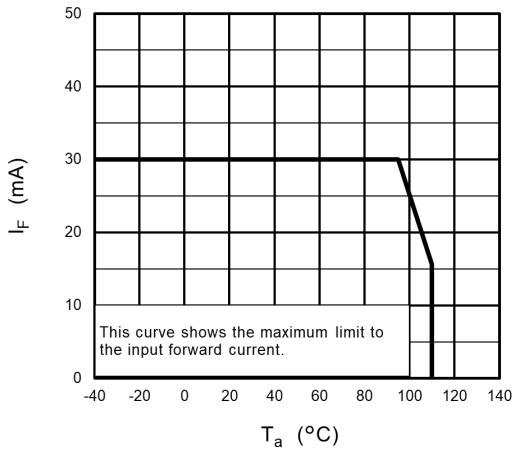


Fig. 12.1.1 $I_F - T_a$

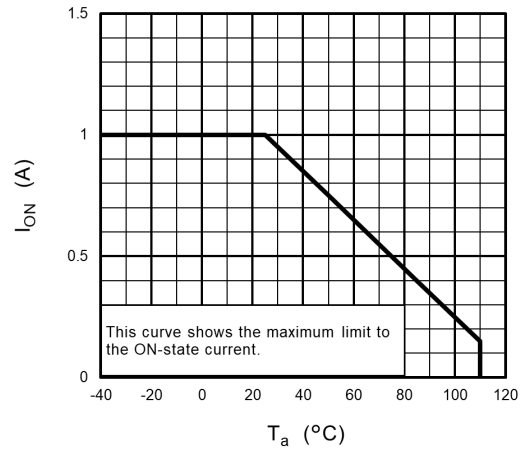


Fig. 12.1.2 $I_{ON} - T_a$

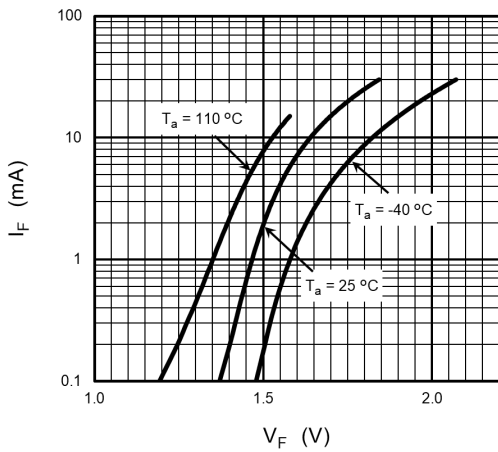


Fig. 12.1.3 $I_F - V_F$

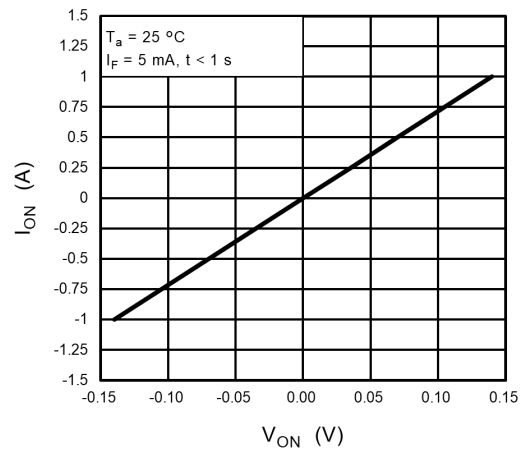


Fig. 12.1.4 $I_{ON} - V_{ON}$

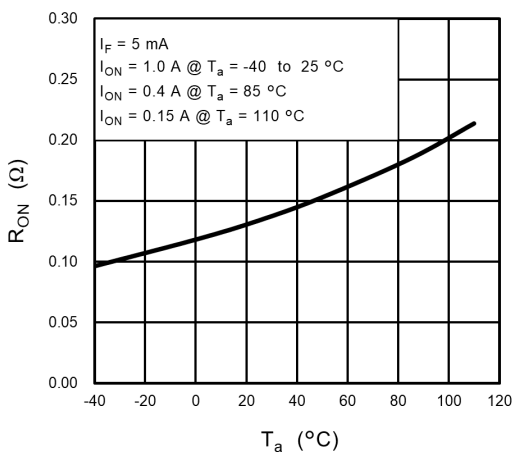


Fig. 12.1.5 $R_{ON} - T_a$

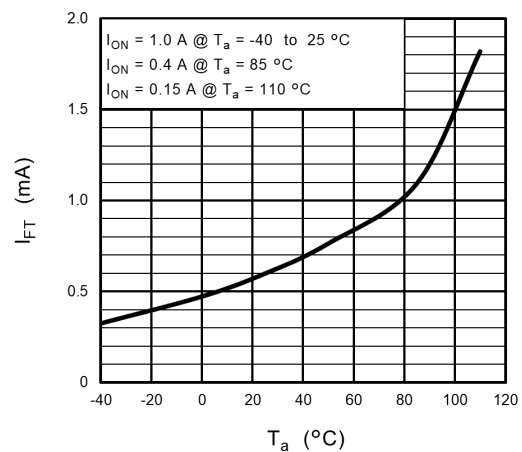


Fig. 12.1.6 $I_{FT} - T_a$

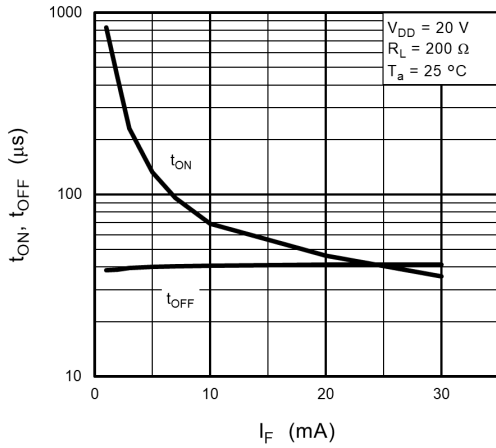


Fig. 12.1.7 t_{ON} , t_{OFF} - I_F

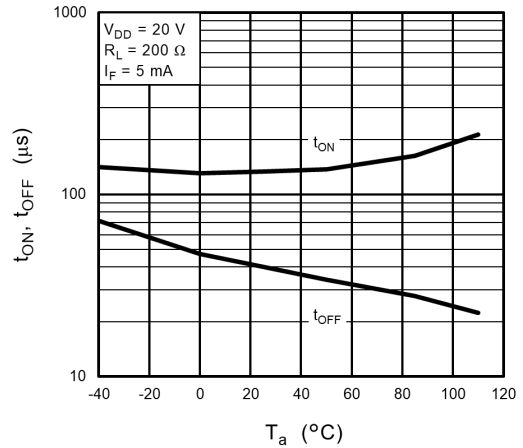


Fig. 12.1.8 t_{ON} , t_{OFF} - T_a

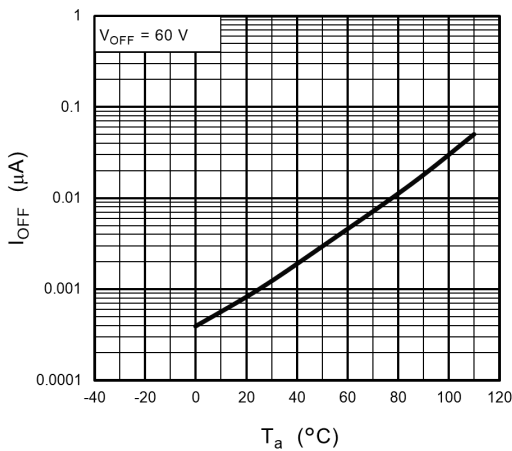


Fig. 12.1.9 I_{OFF} - T_a

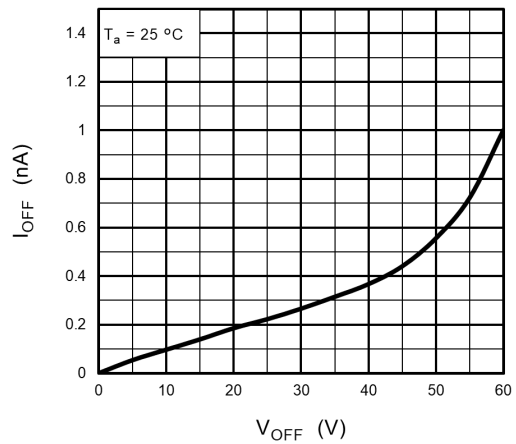


Fig. 12.1.10 I_{OFF} - V_{OFF}

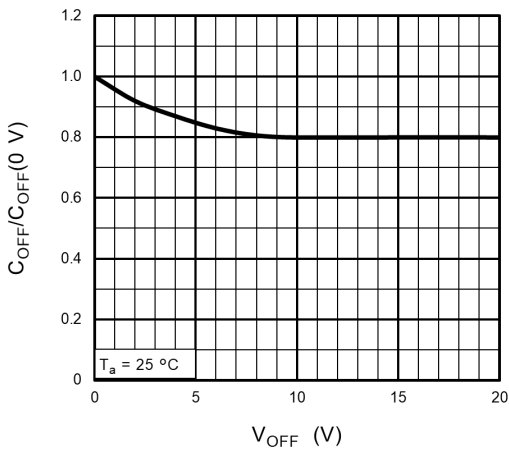


Fig. 12.1.11 $C_{OFF}/C_{OFF}(0\text{ V})$ - V_{OFF}

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

13. Soldering and Storage

13.1. Precautions for Soldering

The soldering temperature should be controlled as closely as possible to the conditions shown below, irrespective of whether a soldering iron or a reflow soldering method is used.

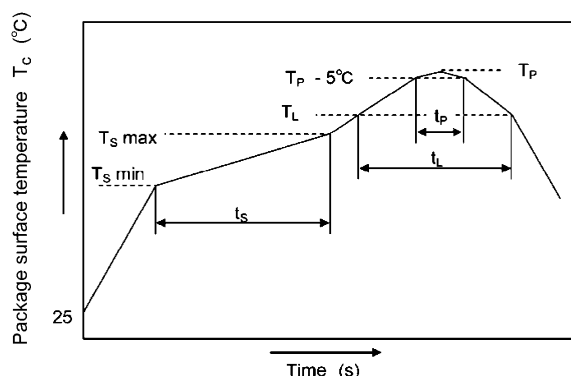
- When using soldering reflow.

The soldering temperature profile is based on the package surface temperature.

(See the figure shown below, which is based on the package surface temperature.)

Reflow soldering must be performed once or twice.

The mounting should be completed with the interval from the first to the last mountings being 2 weeks.



	Symbol	Min	Max	Unit
Preheat temperature	T_s	150	200	°C
Preheat time	t_s	60	120	s
Ramp-up rate (T_L to T_P)			3	°C/s
Liquidus temperature	T_L	217		°C
Time above T_L	t_L	60	150	s
Peak temperature	T_P		260	°C
Time during which T_c is between ($T_P - 5$) and T_P	t_p		30	s
Ramp-down rate (T_P to T_L)			6	°C/s

An Example of a Temperature Profile When Lead(Pb)-Free Solder Is Used

- When using soldering flow
Preheat the device at a temperature of 150 °C (package surface temperature) for 60 to 120 seconds.
Mounting condition of 260 °C within 10 seconds is recommended.
Flow soldering must be performed once.
- When using soldering Iron
Complete soldering within 10 seconds for lead temperature not exceeding 260 °C or within 3 seconds not exceeding 350 °C
Heating by soldering iron must be done only once per lead.

13.2. Precautions for General Storage

- Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- Follow the precautions printed on the packing label of the device for transportation and storage.
- Keep the storage location temperature and humidity within a range of 5 °C to 35 °C and 45 % to 75 %, respectively.
- Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- When restoring devices after removal from their packing, use anti-static containers.
- Do not allow loads to be applied directly to devices while they are in storage.
- If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

14. Land Pattern Dimensions (for reference only)

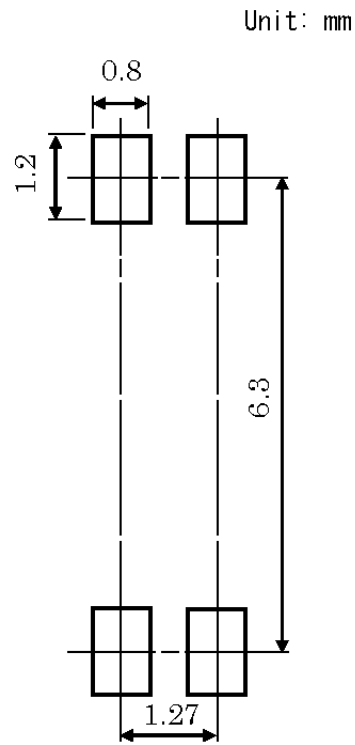


Fig. 14.1 TLP3640A Land Pattern Dimensions (for reference only)

15. Marking

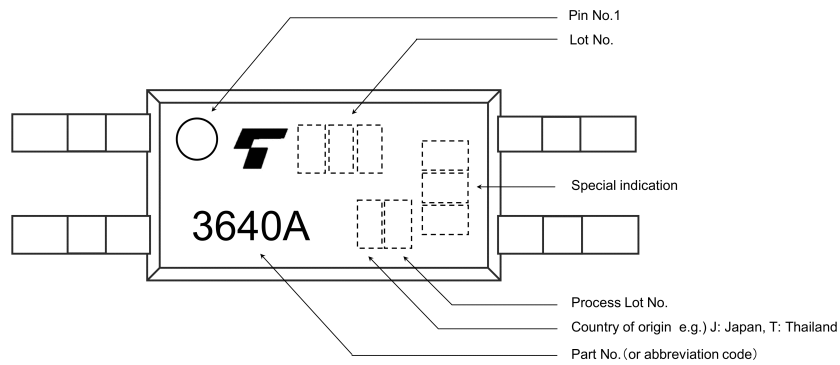


Fig. 15.1 TLP3640A Marking Example

16. EN IEC 60747-5-5 Option (V4) Specification

- Part number: TLP3640A (Note 1)
- The following part naming conventions are used for the devices that have been qualified according to option (V4) of EN IEC 60747.

Example: TLP3640A(V4TPL,E(T

V4: EN IEC 60747 option

TPL: Tape type

E: [[G]]/RoHS COMPATIBLE (Note 2)

(T:Factory production in Thailand

Note 1: Use TOSHIBA standard type number for safety standard application.

e.g., TLP3640A(V4TPL,E(T → TLP3640A

Note 2: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Description	Symbol	Rating	Unit
Application classification for rated mains voltage ≤ 150 Vrms for rated mains voltage ≤ 600 Vrms		I-IV I-III	—
Climatic classification		40 / 110 / 21	—
Pollution degree		2	—
Maximum operating insulation voltage	VIORM	707	Vpeak
Input to output test voltage, Method A V _{pr} = 1.6 × VIORM, type and sample test t _p = 10 s, partial discharge < 5 pC	V _{pr}	1131	Vpeak
Input to output test voltage, Method B V _{pr} = 1.875 × VIORM, 100 % production test t _p = 1 s, partial discharge < 5 pC	V _{pr}	1330	Vpeak
Highest permissible overvoltage (transient overvoltage, t _{pr} = 60 s)	VTR	6000	Vpeak
Safety limiting values (max. permissible ratings in case of fault, also refer to thermal derating curve) current (input current I _F , P _{so} = 0) power (output or total power dissipation) temperature	I _{si} P _{so} T _s	250 400 150	mA mW °C
Insulation resistance V _{IO} = 500 V, T _a = 25 °C V _{IO} = 500 V, T _a = 100 °C V _{IO} = 500 V, T _a = T _s	R _{si}	≥ 10 ¹² ≥ 10 ¹¹ ≥ 10 ⁹	Ω

Fig. 16.1 EN IEC 60747 Insulation Characteristics

17. Insulation Related Specifications

Table Insulation Related Specifications (Note)

Insulation Related Parameters	Symbol	TLP3640A
Minimum creepage distance	Cr	5.0 mm
Minimum clearance	Cl	5.0 mm
Minimum insulation thickness	ti	0.2 mm
Comparative tracking index	CTI	500

Note: If a printed circuit is incorporated, the creepage distance and clearance may be reduced below this value. (e.g., at a standard distance between soldering eye centers of 7.5 mm). If this is not permissible, the user shall take suitable measures.

Note: This photocoupler is suitable for **safe electrical isolation** only within the safety limit data. Maintenance of the safety data shall be ensured by means of protective circuits.



Fig. 17.1 Marking on Packing for EN IEC 60747

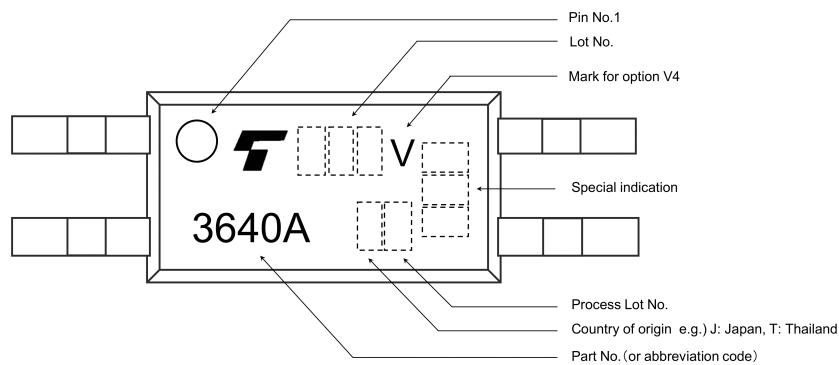


Fig. 17.2 Marking Example (Note)

Note: The above marking is applied to the photocouplers that have been qualified according to option (V4) of EN IEC 60747.

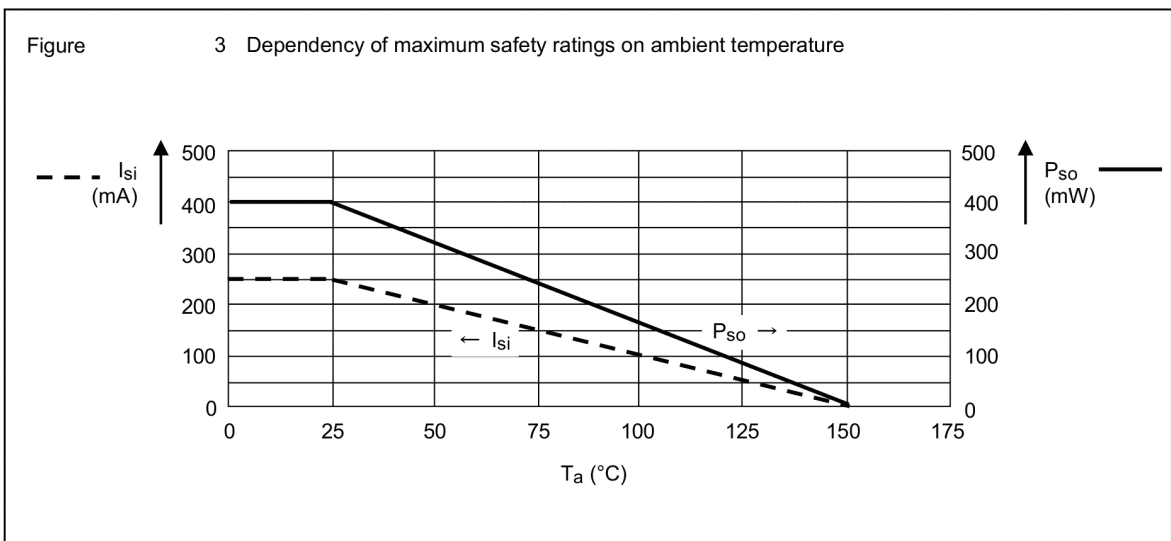
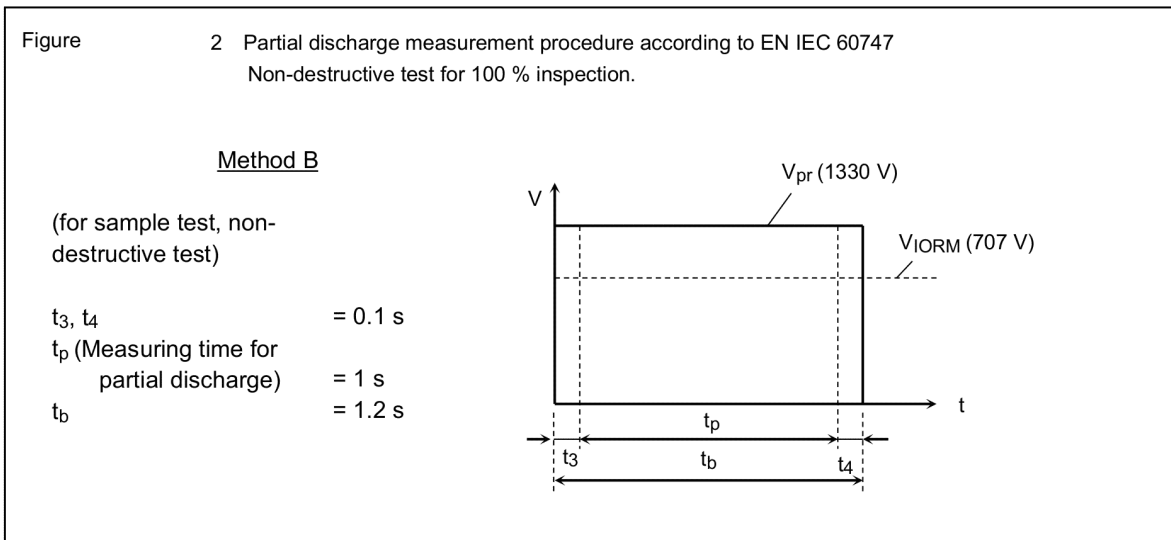
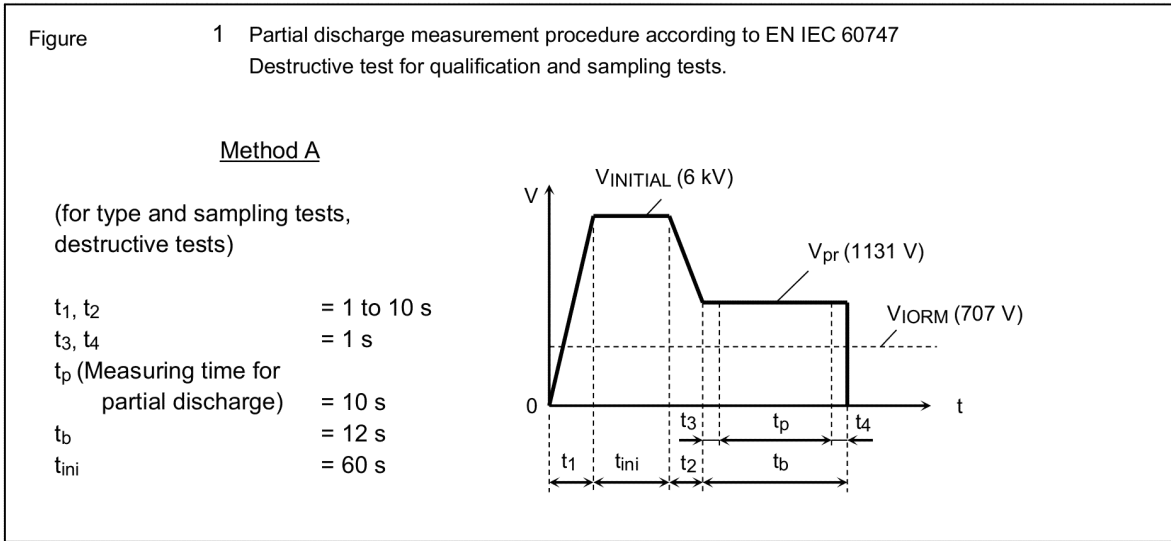


Fig. 17.3 Measurement Procedure

18. Devices in Halogen-Free Resin Packages

- This product is Halogen-Free

Toshiba Electronic Devices & Storage Corporation ("Toshiba") defines a "Halogen-Free resin semiconductor product" as a semiconductor product in which:

- (1) the encapsulating resins do not contain any of the following elements: bromine (Br), chlorine (Cl) and antimony (Sb), respectively, in an amount exceeding 0.09 weight percent, and do not contain chlorine and bromine in an aggregate amount exceeding 0.15 weight percent of the encapsulating resins, and/or
- (2) the resin portion(s) in printed circuit boards do not contain any of the following elements: bromine, chlorine and antimony, respectively, in an amount exceeding 0.09 weight percent, and do not contain chlorine and bromine in an aggregate amount exceeding 0.15 weight percent of the each resin portion(s) in printed circuit boards.

For avoidance of doubt, "Halogen-Free resin semiconductor product" does not mean, and Toshiba does not make any warranty of any kind, that said semiconductor product is entirely free of antimony or of any of the following elements of the halogen family: bromine, chlorine, iodine (I), fluorine (F) and astatine (At).

In addition, a Halogen-Free resin semiconductor product may contain antimony and/or any of the elements of the halogen family as mentioned in the above paragraph in one or more portion(s) of the semiconductor product other than the encapsulating resins and the resin portion(s) in printed circuit boards.

The information provided herein is accurate as of the date that it was provided, to the best of the knowledge and belief of the Toshiba Electronic Devices & Storage Corporation ("Toshiba"), Toshiba bases such knowledge and belief on information provided by third parties, and Toshiba makes no representation or warranty as to the accuracy of such third party information. Toshiba has taken and will continue to take, reasonable steps to provide accurate information to its customers, but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals.

19. Ordering Information (Example of Item Name)

Item Name	Packaging (Note 1)	VDE Option	Packing (MOQ)
TLP3640A(E(T	SMD		Magazine (175 pcs)
TLP3640A(TPL,E(T	SMD		Tape and reel (2500 pcs)
TLP3640A(TPR,E(T	SMD		Tape and reel (2500 pcs)
TLP3640A(V4,E(TE	SMD	EN IEC 60747	Magazine (175 pcs)
TLP3640A(V4TPL,E(T	SMD	EN IEC 60747	Tape and reel (2500 pcs)
TLP3640A(V4TPR,E(T	SMD	EN IEC 60747	Tape and reel (2500 pcs)

Note 1: SMD: Surface Mount Device

20. Ordering Information

When placing an order, please specify the part number, tape type and quantity as shown in the following example.

Example: TLP3640A(TPL,E(T 2500 pcs

Part number: TLP3640A

TPL: Tape type(L direction)

E: [[G]]/RoHS COMPATIBLE (Note 1)

(T:Factory production in Thailand

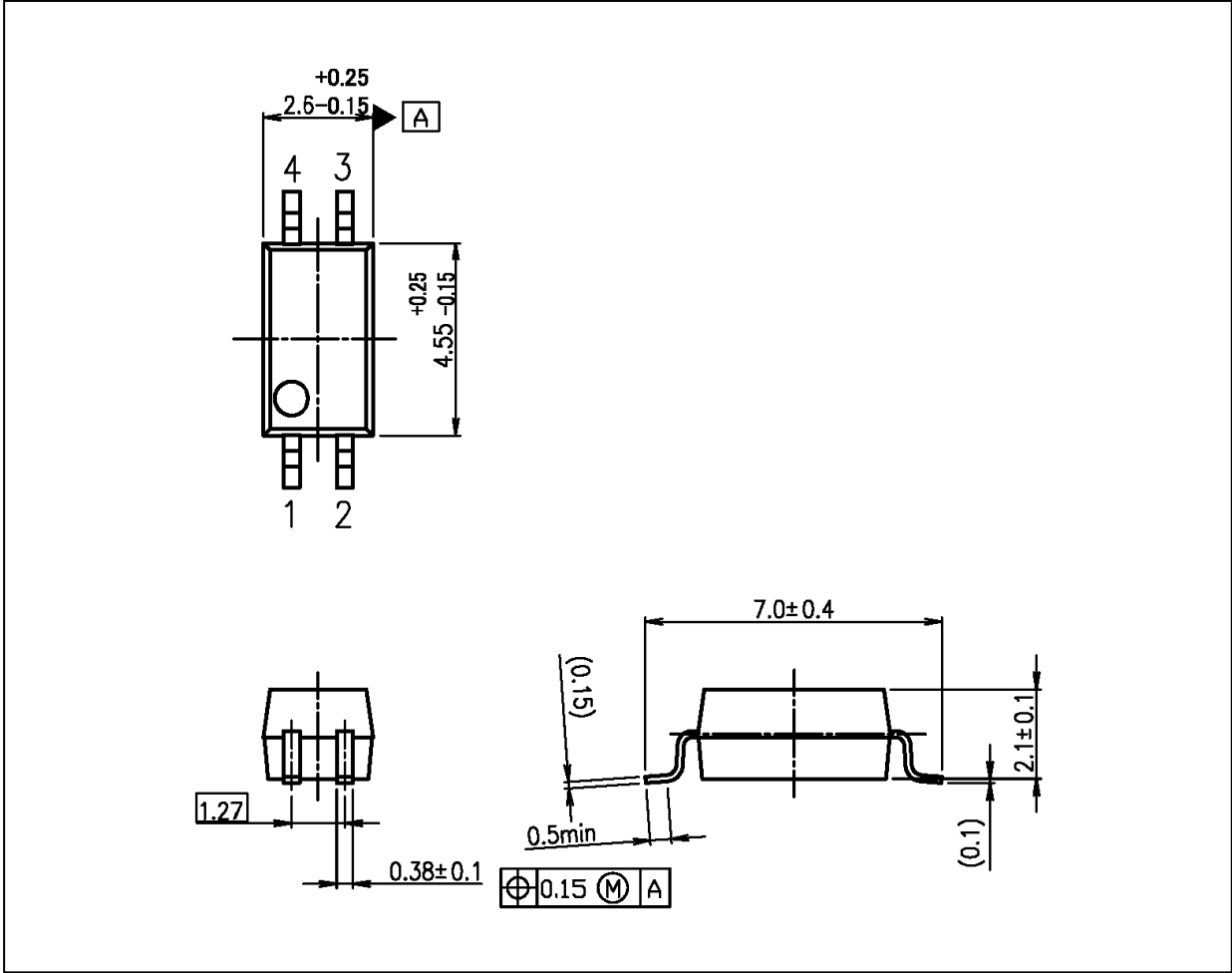
Quantity (must be a multiple of 2500): 2500 pcs

Note 1: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Package Dimensions

Unit: mm



Weight: 0.05 g (typ.)

Package Name(s)
TOSHIBA: 11-3C1

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Hardware, software and systems described in this document are collectively referred to as "Product".

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