

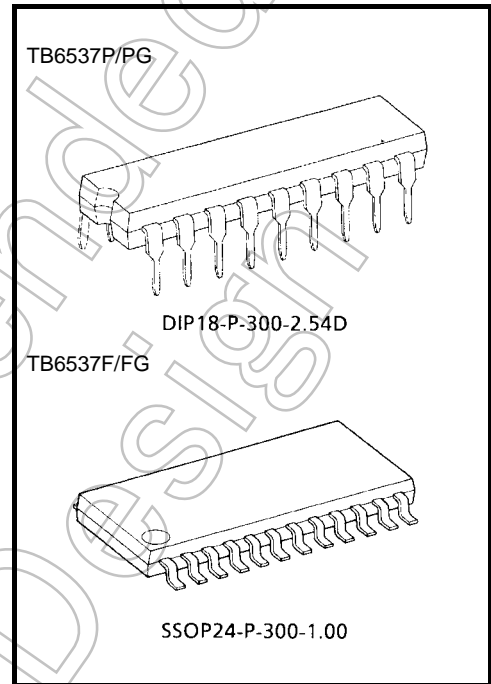
TB6537P/PG, TB6537F/FG

3-PHASE FULL-WAVE SENSORLESS CONTROLLER FOR BRUSHLESS DC MOTORS

The TB6537P/PG/F/FG is a 3-phase full-wave sensorless controller for brushless DC motors. It is capable of controlling voltage through PWM signal input. When combined with various drive circuits, it can be used for various types of motors.

Features

- 3-phase full-wave sensorless drive
- PWM control (PWM signal is supplied from external sources.)
- Turn-on signal output current: 20 mA
- Over-current protection function
- Forward/reverse modes
- Lead angle control function (0°, 7.5°, 15° and 30°)
- Built-in lap turn-on function
- Two types of PWM output (upper PWM and upper/lower alternate PWM)



Weight
 DIP18-P-300-2.54D: 1.47 g (typ.)
 SSOP24-P-300-1.00: 0.32 g (typ.)

TB6537PG/FG:

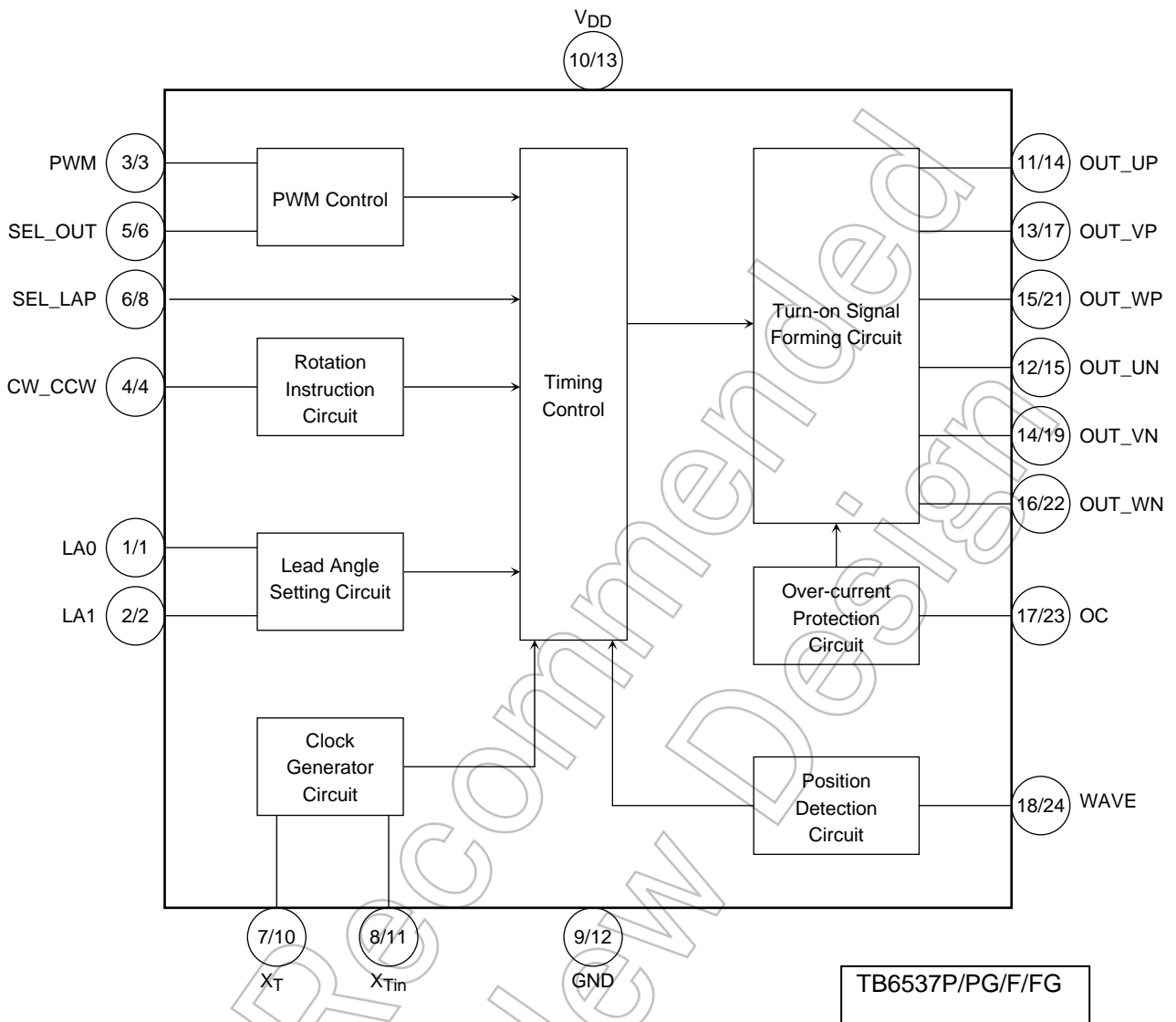
The TB6537PG/FG is a Pb-free product.

The following conditions apply to solderability:

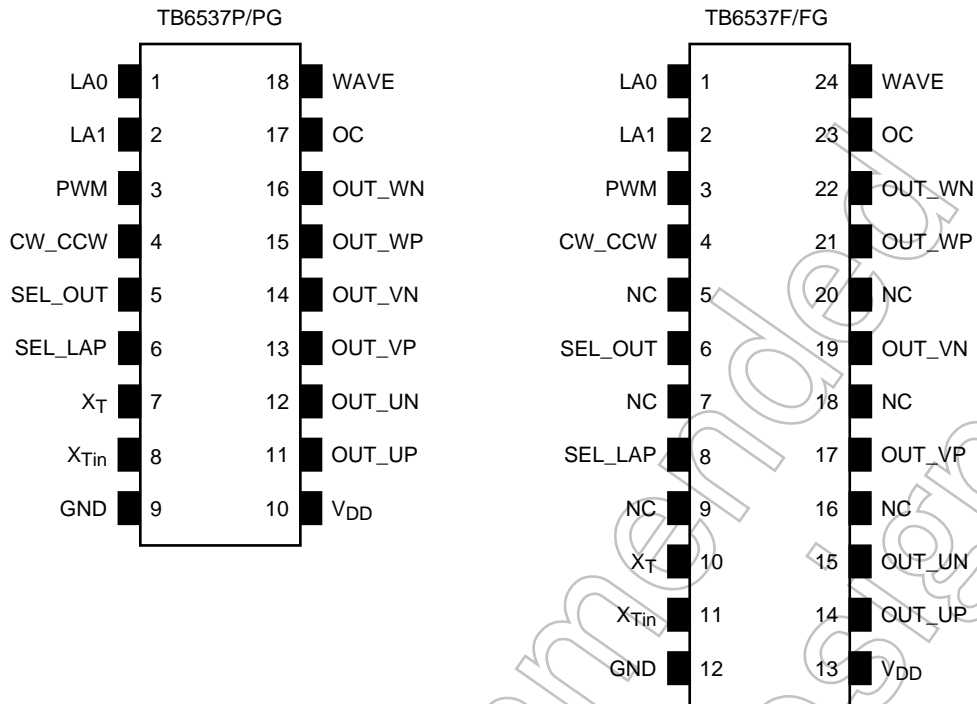
*Solderability

1. Use of Sn-37Pb solder bath
 - *solder bath temperature = 230°C
 - *dipping time = 5 seconds
 - *number of times = once
 - *use of R-type flux
2. Use of Sn-3.0Ag-0.5Cu solder bath
 - *solder bath temperature = 245°C
 - *dipping time = 5 seconds
 - *number of times = once
 - *use of R-type flux

Block Diagram



Pin Assignment



Not Recommended for New Design

Pin Description

Pin No.		Symbol	I/O	Description
TB6537P/PG	TB6537F/FG			
1	1	LA0	I	Lead angle setting signal input pin <ul style="list-style-type: none"> LA0 = Low, LA1 = Low: Lead angle 0° LA0 = High, LA1 = Low: Lead angle 7.5°
2	2	LA1	I	<ul style="list-style-type: none"> LA0 = Low, LA1 = High: Lead angle 15° LA0 = High, LA1 = High: Lead angle 30° Built-in pull-down resistor
3	3	PWM	I	PWM signal input pin <ul style="list-style-type: none"> Inputs Low-active PWM signal Built-in pull-up resistor Disables input of duty-100% (Low) signal High for 250 ns or longer is required.
4	4	CW_CCW	I	Rotation direction signal input pin <ul style="list-style-type: none"> High: Reverse (U → W → V) Low, Open: Forward (U → V → W) Built-in pull-down resistor
—	5	NC	—	Not connected
5	6	SEL_OUT	I	Pin to select the synthesis method of the burn-in signal and PWM signal <ul style="list-style-type: none"> Low: Upper PWM High: Upper/Lower alternate PWM Built-in pull-down resistor
—	7	NC	—	Not connected
6	8	SEL_LAP	I	Lap turn-on select pin <ul style="list-style-type: none"> Low: Lap turn-on High: 120° turn-on Built-in pull-up resistor
—	9	NC	—	Not connected
7	10	X _T	—	Resonator connecting pin
8	11	X _{Tin}	—	<ul style="list-style-type: none"> Selects starting commutation frequency. Starting commutation frequency $f_{st} = \text{Resonator frequency } f_{xt}/(6 \times 2^{17})$
9	12	GND	—	Connected to GND.
10	13	V _{DD}	—	Connected to 5-V power supply.
11	14	OUT_UP	O	U-phase upper turn-on signal output pin <ul style="list-style-type: none"> U-phase winding wire positive ON/OFF switching pin ON: Low, OFF: High
12	15	OUT_UN	O	U-phase lower turn-on signal output pin <ul style="list-style-type: none"> U-phase winding wire negative ON/OFF switching pin ON: High, OFF: Low
—	16	NC	—	Not connected
13	17	OUT_VP	O	V-phase upper turn-on signal output pin <ul style="list-style-type: none"> V-phase winding wire positive ON/OFF switching pin ON: Low, OFF: High
—	18	NC	—	Not connected
14	19	OUT_VN	O	V-phase lower turn-on signal output pin <ul style="list-style-type: none"> V-phase winding wire negative ON/OFF switching pin ON: High, OFF: Low

Pin No.		Symbol	I/O	Description
TB6537P/PG	TB6537F/FG			
—	20	NC	—	Not connected
15	21	OUT_WP	O	W-phase upper turn-on signal output pin <ul style="list-style-type: none"> W-phase winding wire positive ON/OFF switching pin ON: Low, OFF: High
16	22	OUT_WN	O	W-phase lower turn-on signal output pin <ul style="list-style-type: none"> W-phase winding wire negative ON/OFF switching pin ON: High, OFF: Low
17	23	OC	I	Over-current signal input pin <ul style="list-style-type: none"> High on this pin can put constraints on the turn-on signal that is performing PWM control. Built-in pull-up resistor
18	24	WAVE	I	Positional signal input pin <ul style="list-style-type: none"> Inputs majority logic synthesis signal of three-phase pin voltage. Built-in pull-up resistor

Functional Description

1. Sensorless Drive

On receipt of PWM signal start instruction turn-on signal for forcible commutation (commutation irrespective of the rotor position of the motor) is output and the motor starts to rotate. The rotation of the motor causes induced voltage on the winding wire pin for each phase.

When signals indicating positive or negative for pin voltage (including induced voltage) for each phase are input on the respective positional signal input pins, the turn-on signal for forcible commutation is automatically switched to the turn-on signal for the positional signal (induced voltage).

Thereafter the turn-on signal is formed according to the induced voltage contained in the pin voltage so as to drive the brushless DC motor.

2. Starting commutation frequency (resonator pin and counter bit select pin)

The forcible commutation frequency at the time of start is determined by the resonator frequency and the number of counter bits (within the IC).

Starting commutation frequency $f_{st} = \text{Resonator frequency } f_{xt} / (6 \times 2^{(\text{bit} + 3)})$ bit = 14.

The forcible commutation frequency at the time of start can be adjusted using the inertia of the motor and load.

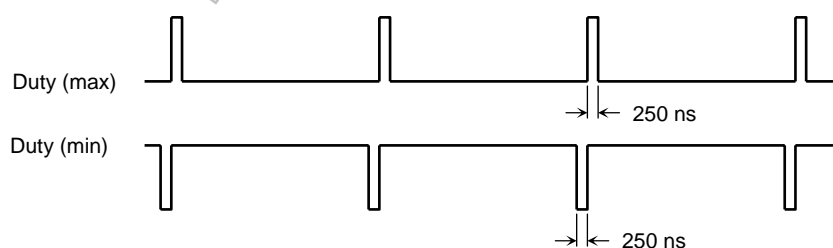
- The forcible commutation frequency should be set higher as the number of magnetic poles increases.
- The forcible commutation frequency should be set lower as the inertia of the load increases.

3. PWM Control

The PWM signal can be reflected in the turn-on signal by supplying the PWM signal from external sources.

The frequency of the PWM signal should be set sufficiently high with regard to the electrical frequency of the motor and in accordance with the switching characteristics of the drive circuit.

As positional detection is performed in synchronization with the rising edges of PWM signal, positional



detection cannot be performed with 0% duty or 100% duty.

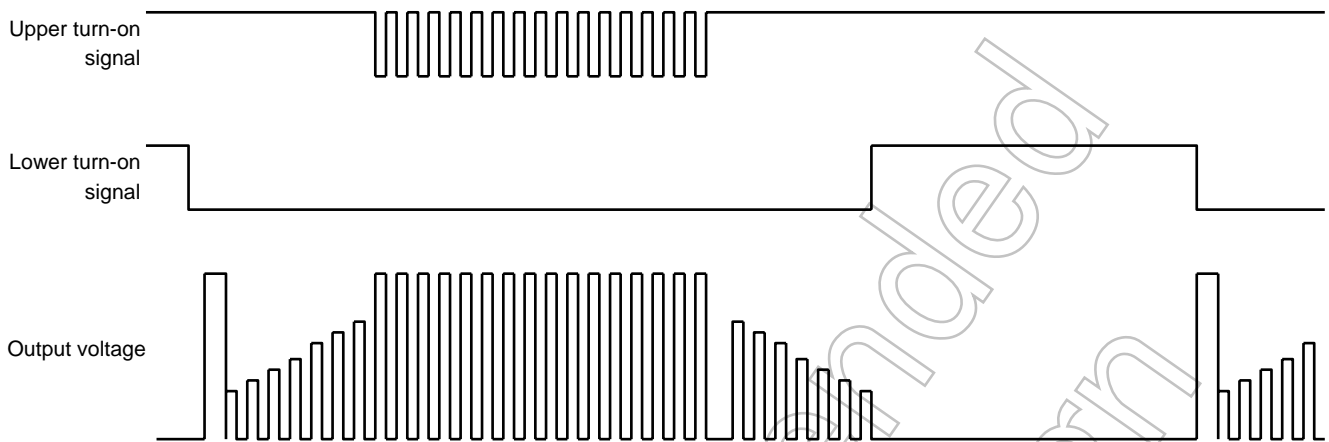
Even if the duty is 99%, the duty of the voltage applied to the motor is 100% owing to the storage time of the drive circuit.

Not Recommended
for New Design

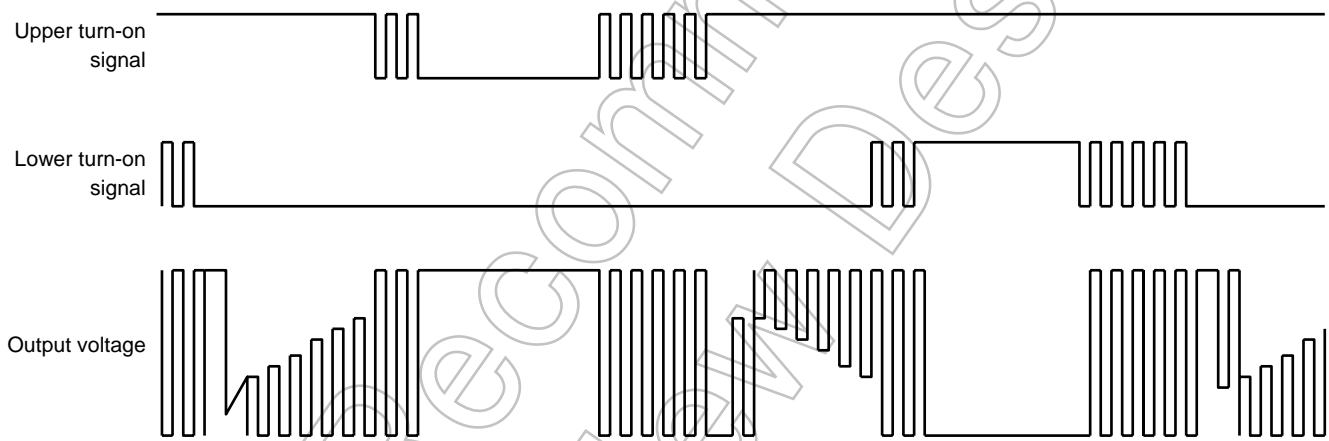
4. Selecting PWM Output Form

The PWM output form can be selected using SEL_OUT.

SEL_OUT = Low



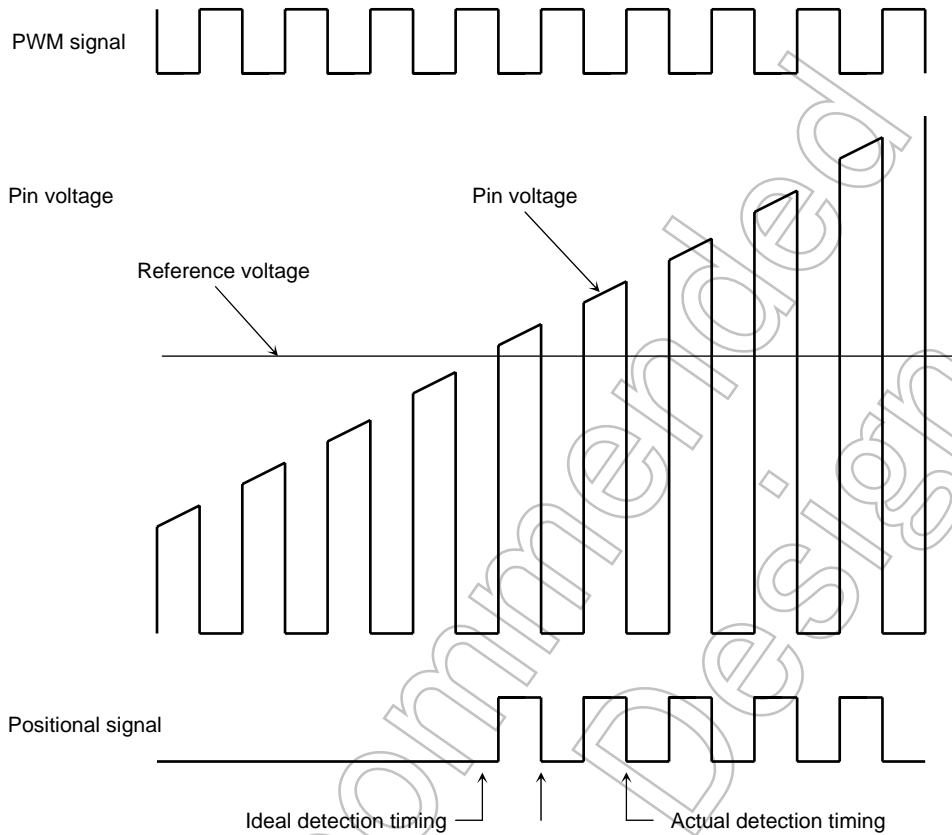
SEL_OUT = High



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5. Positional Variation

Since positional detection is performed in synchronization with PWM signal, positional variation occurs in connection with the frequency of PWM signal. Be especially careful when the IC is used for high-speed motors.



Variation is calculated through detection at two consecutive rising edges of the PWM signal.
 $1/f_p < \text{Detection time variation} < 2/f_p$ f_p : PWM frequency.

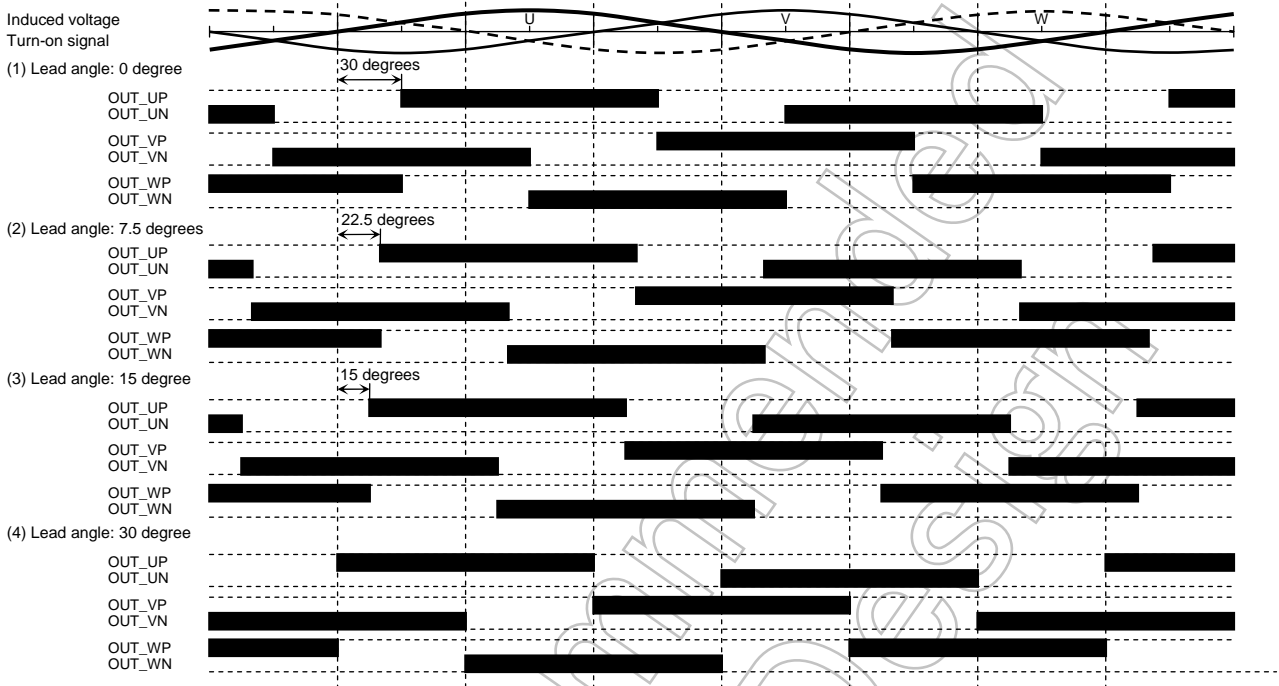
6. Over-current protection function

The active phase that controls the PWM is turned off by the rising-edge of the OC signal. The inactive phase is turned on by the timing of the next PWM signal.

Not for New

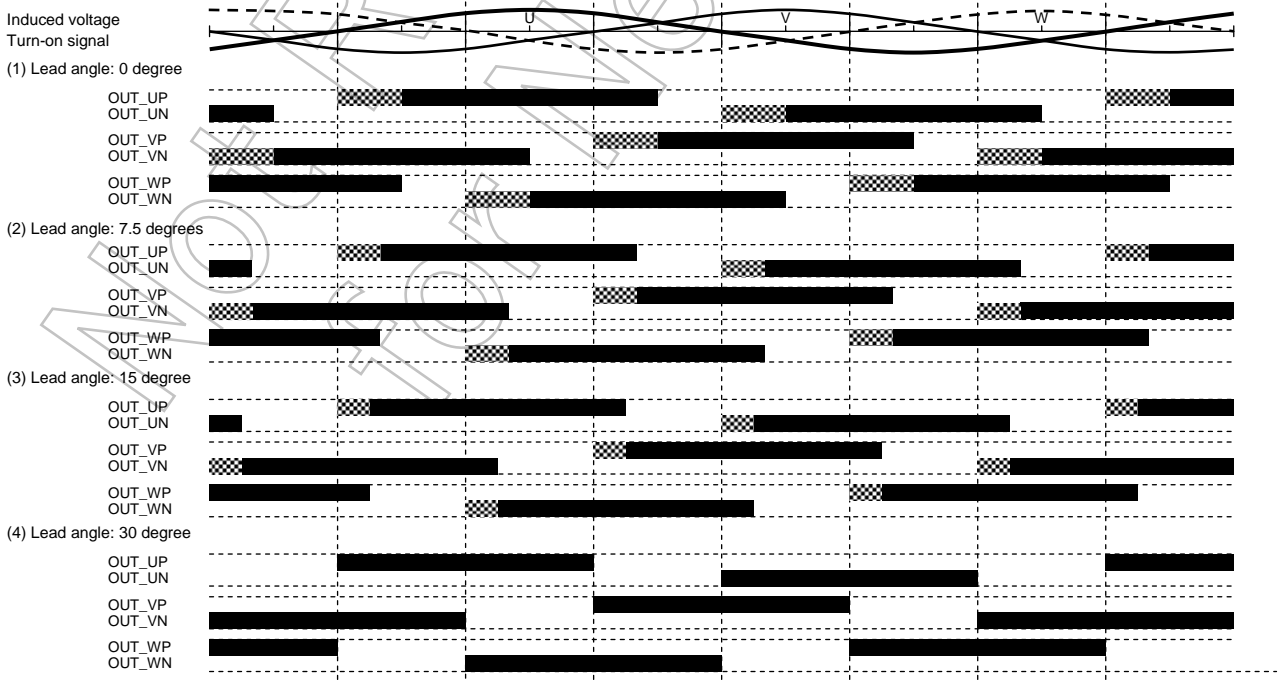
7. Lead Angle Control

The lead angle is 0° during the starting forcible commutation and, when normal commutation is started, automatically changes to the lead angle that was set using LA0 and LA1. However, if both LA0 and LA1 are set for High, the lead angle is 30° in the starting forcible commutation as well as in normal commutation.



8. Lap Turn-on Control

When SEL_LAP = High, the turn-on angle is 120°. When SEL_LAP = Low, the Lap Turn-on Mode starts. In Lap Turn-on Mode, the time between zero-cross point and the 120° turn-on timing becomes longer (see the shaded area in the chart below) so as to create some overlap when switching turn-on signals. The lap time differs depending on the lead angle setting.

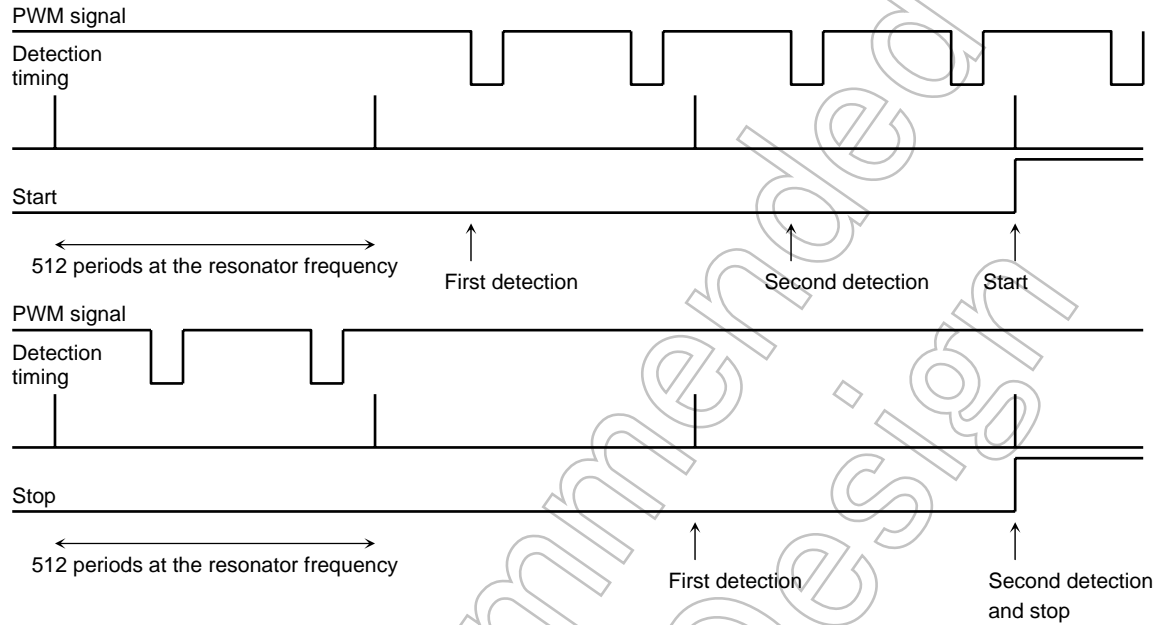


9. Start/Stop Control

Start/Stop operation is controlled using the PWM signal input pin.

A stop is acknowledged when the PWM signal duty is 0, and a start is acknowledged when the ON-signal of a frequency four times higher than the resonator frequency or greater is input continuously.

Timing chart



Note: Take sufficient care regarding noise on the PWM signal input pin.

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Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V _{DD}	5.5	V	
Input voltage	V _{in}	-0.3 to V _{DD} + 0.3	V	
Turn-on signal output current	I _{OUT}	20	mA	
Power dissipation	P _D	TB6537P/ PG	1.25	W
		TB6537F/ FG	0.59	
Operating temperature	T _{opr}	-30 to 85	°C	
Storage temperature	T _{stg}	-55 to 150	°C	

Recommended Operating Conditions (Ta = -30 to 85°C)

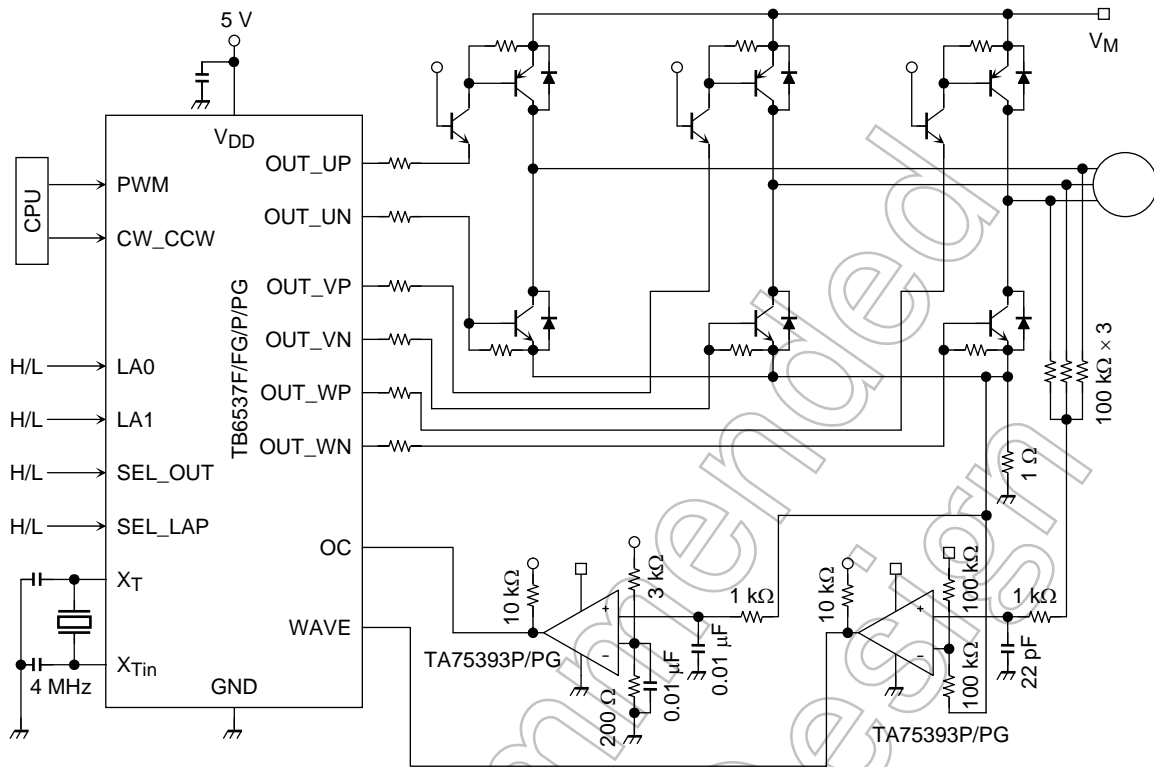
Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Power supply voltage	V _{DD}	—	4.5	5.0	5.5	V
Input voltage	V _{in}	—	-0.3	—	V _{DD} + 0.3	V
PWM frequency	f _{PWM}	—	—	16	—	kHz
Oscillation frequency	f _{osc}	—	1.0	—	10	MHz

Not Recommended for New Design

Electrical Characteristics (Ta = 25°C, VDD = 5 V)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Static power supply current	I _{DD}	—	PWM = H, X _{Tin} = H	—	0.1	0.3	mA
Dynamic power supply current	I _{DD (opr)}	—	PWM = 50% Duty, X _{Tin} = 4 MHz	—	1	3	mA
Input current	I _{IN-1 (H)}	—	V _{IN} = 5 V, PWM, OC, WAVE_U, SEL_LAP	—	0	1	μA
	I _{IN-1 (L)}	—	V _{IN} = 0 V, PWM, OC, WAVE_U, SEL_LAP	-75	-50	—	
	I _{IN-2 (H)}	—	V _{IN} = 5 V, CW_CCW, LA0, LA1, SEL_OUT	—	50	75	
	I _{IN-2 (L)}	—	V _{IN} = 0 V, CW_CCW, LA0, LA1, SEL_OUT	-1	0	—	
Input voltage	V _{IN (H)}	—	PWM, OC, SEL_LAP, CW_CCW WAVE_U, LA0, LA1, SEL_OUT	3.5	—	5	V
	V _{IN (L)}	—	PWM, OC, SEL_LAP, CW_CCW WAVE_U, LA0, LA1, SEL_OUT	GND	—	1.5	
Input hysteresis voltage	V _H	—	PWM, OC, SEL_LAP, CW_CCW WAVE_U, LA0, LA1, SEL_OUT	—	0.6	—	V
Output voltage	V _{O-1 (H)}	—	I _{OH} = -1 mA OUT_UP, OUT_VP, OUT_WP	4.3	—	V _{DD}	V
	V _{O-1 (L)}	—	I _{OH} = 20 mA OUT_UP, OUT_VP, OUT_WP	GND	—	0.5	
	V _{O-2 (H)}	—	I _{OH} = -20 mA OUT_UN, OUT_VN, OUT_WN	4.0	—	V _{DD}	
	V _{O-2 (L)}	—	I _{OH} = 1 mA OUT_UN, OUT_VN, OUT_WN	GND	—	0.5	
Output leak current	I _{L (H)}	—	V _{DD} = 5.5 V, V _{OUT} = 0 V OUT_UP, OUT_VP, OUT_WP OUT_UN, OUT_VN, OUT_WN	—	0	10	μA
	I _{L (L)}	—	V _{DD} = 5.5 V, V _{OUT} = 5.5 V OUT_UP, OUT_VP, OUT_WP OUT_UN, OUT_VN, OUT_WN	—	0	10	
Output delay time	t _{pLH}	—	PWM-Output	—	0.5	1	μs
	t _{pHL}			—	0.5	1	

Application Circuit Example



Note 1: Utmost care is necessary in the design of the output, VCC, VM, and GND lines since the IC may be destroyed by short-circuiting between outputs, air contamination faults, or faults due to improper grounding, or by short-circuiting between contiguous pins.

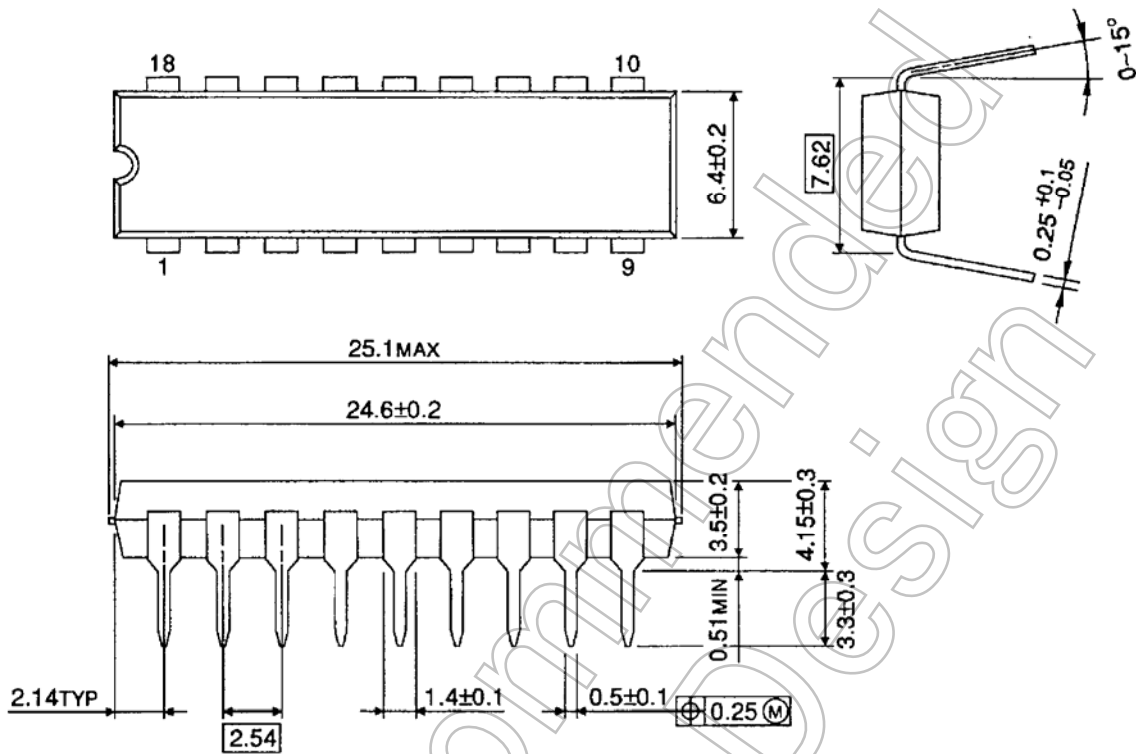
Note 2: The above application circuit and values mentioned are an example provided for reference purposes only. Since the values may vary depending on the motor to be used, appropriate values must be determined through experiments before the device is used.

Not Recommended for New

Package Dimensions

DIP18-P-300-2.54D

Unit : mm



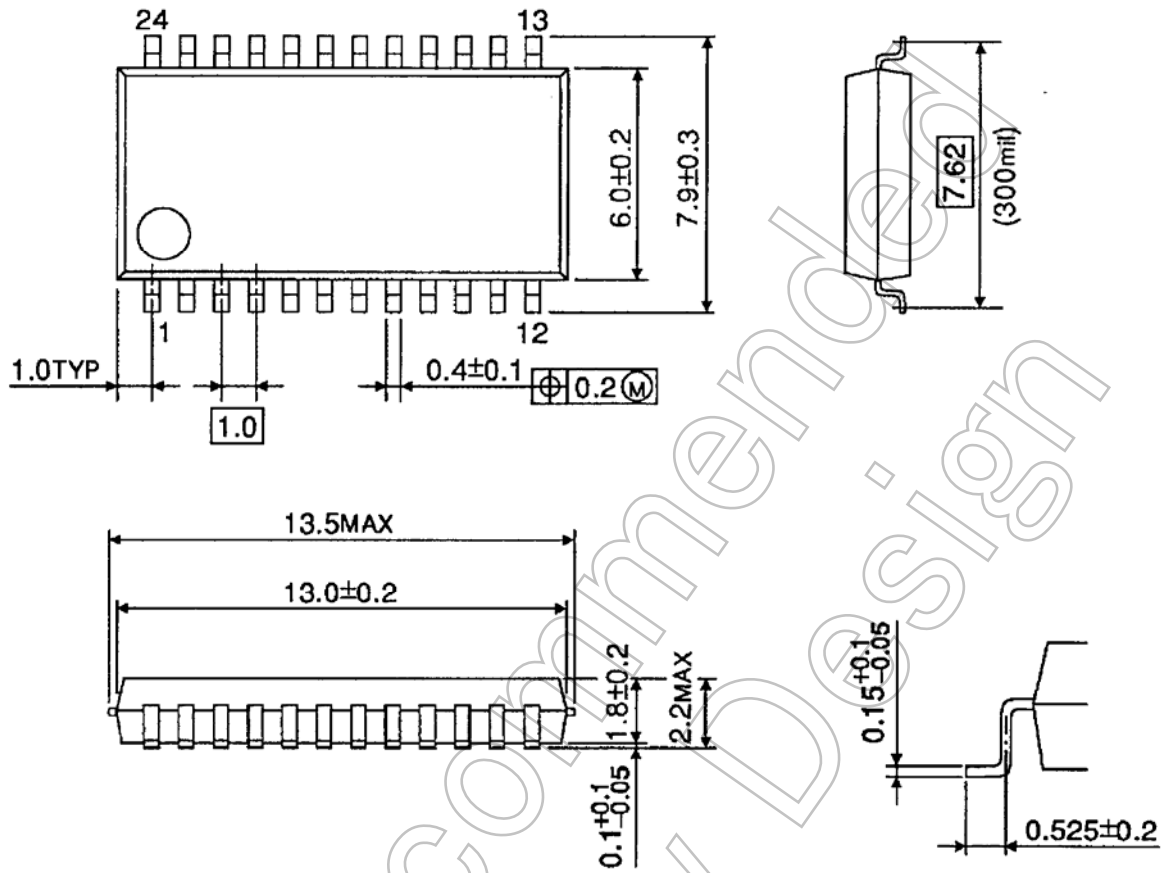
Weight: 1.47 (typ.)

Not Recommended for New Design

Package Dimensions

SSOP24-P-300-1.00

Unit : mm



Weight: 0.32 (typ.)

Not Recommended for New Design

Notes on Contents

1. Block Diagrams

Some of the functional blocks, circuits, or constants in the block diagram may be omitted or simplified for explanatory purposes.

2. Equivalent Circuits

The equivalent circuit diagrams may be simplified or some parts of them may be omitted for explanatory purposes.

3. Timing Charts

Timing charts may be simplified for explanatory purposes.

4. Application Circuits

The application circuits shown in this document are provided for reference purposes only. Thorough evaluation is required, especially at the mass production design stage.

Toshiba does not grant any license to any industrial property rights by providing these examples of application circuits.

5. Test Circuits

Components in the test circuits are used only to obtain and confirm the device characteristics. These components and circuits are not guaranteed to prevent malfunction or failure from occurring in the application equipment.

IC Usage Considerations

Notes on handling of ICs

- [1] The absolute maximum ratings of a semiconductor device are a set of ratings that must not be exceeded, even for a moment. Do not exceed any of these ratings.
Exceeding the rating(s) may cause the device breakdown, damage or deterioration, and may result injury by explosion or combustion.
- [2] Do not insert devices in the wrong orientation or incorrectly.
Make sure that the positive and negative terminals of power supplies are connected properly.
Otherwise, the current or power consumption may exceed the absolute maximum rating, and exceeding the rating(s) may cause the device breakdown, damage or deterioration, and may result injury by explosion or combustion.
In addition, do not use any device that is applied the current with inserting in the wrong orientation or incorrectly even just one time.

Points to remember on handling of ICs

(1) Back-EMF

When a motor rotates in the reverse direction, stops or slows down abruptly, a current flow back to the motor's power supply due to the effect of back-EMF. If the current sink capability of the power supply is small, the device's motor power supply and output pins might be exposed to conditions beyond maximum ratings. To avoid this problem, take the effect of back-EMF into consideration in system design.

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