

# TLP628, TLP628-2, TLP628-4

Unit: mm

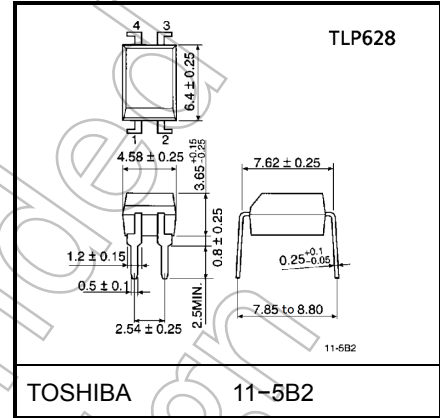
Programmable Controllers  
DC-Output Module  
Telecommunication

The TOSHIBA TLP628, -2, and -4 consists of an infrared emitting diode optically coupled to a phototransistor which has a 350V high voltage of collector-emitter breakdown voltage.

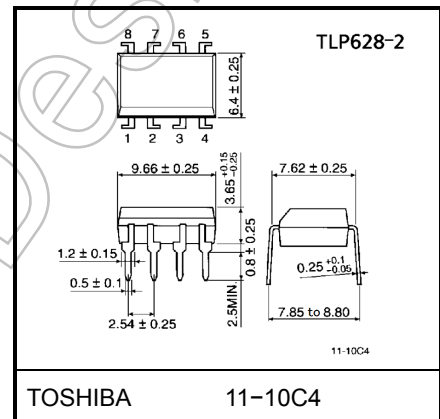
The TLP628-2 offers two isolated channels in a eight lead plastic DIP package, while the TLP628-4 provide four isolated channels per package.

- Collector-emitter voltage: 350 V (min.)
- Current transfer ratio: 50% (min.)
- Isolation voltage: 5000Vrms (min.)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A  
File No.E67349
- VDE-approved: EN 60747-5-5 (Note 1)

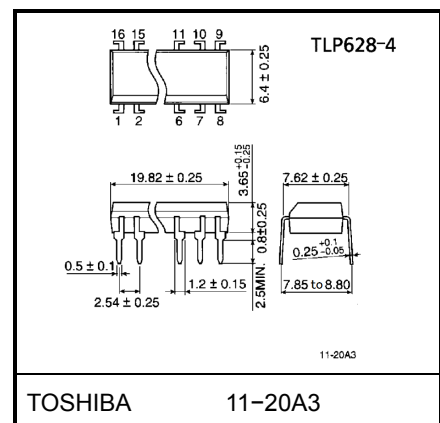
Note 1 : When a VDE approved type is needed,  
please designate the **Option(D4)**.



Weight: 0.26 g (typ.)



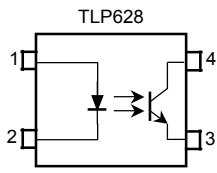
Weight: 0.54 g (typ.)



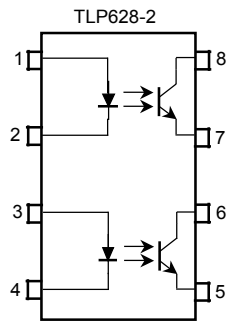
Weight: 1.1 g (typ.)

Start of commercial production  
1993-10

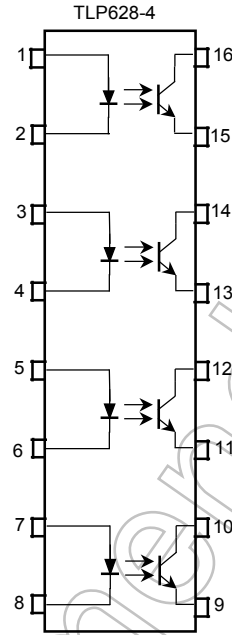
## Pin Configurations (top view)



- 1: Anode
- 2: Cathode
- 3: Emitter
- 4: Collector



- 1, 3: Anode
- 2, 4: Cathode
- 5, 7: Emitter
- 6, 8: Collector



- 1, 3, 5, 7: Anode
- 2, 4, 6, 8: Cathode
- 9, 11, 13, 15: Emitter
- 10, 12, 14, 16: Collector

Not Recommended for New Design

### Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating		Unit
			TLP628	TLP628-2 TLP628-4	
LED	Forward current	I <sub>F</sub>	60	50	mA
	Forward current derating	ΔI <sub>F</sub> / °C	-0.7 (Ta ≥ 39°C)	-0.5 (Ta ≥ 25°C)	mA / °C
	Pulse forward current	I <sub>FP</sub>	1 (100μs pulse, 100pps)		A
	Reverse voltage	V <sub>R</sub>	5		V
	Input power dissipation	P <sub>D</sub>	100	70	mW
	Input power dissipation derating (Ta ≥ 25°C, 1 circuit)	ΔP <sub>D</sub> / °C	-1.0	-0.7	mW / °C
Detector	Collector-emitter voltage	V <sub>CEO</sub>	350		V
	Emitter-collector voltage	V <sub>ECO</sub>	7		V
	Collector current	I <sub>C</sub>	50		mA
	Collector power dissipation (1 circuit)	P <sub>C</sub>	150	100	mW
	Collector power dissipation derating (Ta ≥ 25°C, 1 circuit)	ΔP <sub>C</sub> / °C	-1.5	-1.0	mW / °C
	Junction temperature	T <sub>j</sub>	125		°C
Storage temperature range		T <sub>stg</sub>	-55 to 125		°C
Operating temperature range		T <sub>opr</sub>	-55 to 100		°C
Lead soldering temperature		T <sub>sol</sub>	260 (10 s)		°C
Total package power dissipation (1 circuit)		P <sub>T</sub>	200	150	mW
Total package power dissipation derating (Ta ≥ 25°C, 1 circuit)		ΔP <sub>T</sub> / °C	-2.0	-1.5	mW / °C
Isolation voltage		BV <sub>S</sub>	5000 (AC, 60 s., R.H. ≤ 60 %) (Note 1)		V <sub>rms</sub>

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1) Device considered a two terminal device: LED side pins shorted together and detector side pins shorted together.

### Recommended Operating Conditions

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Supply voltage	V <sub>CC</sub>	—	—	200	V
Forward current	I <sub>F</sub>	—	16	25	mA
Collector current	I <sub>C</sub>	—	—	10	mA
Operating temperature	T <sub>opr</sub>	-25	—	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

### Individual Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min.	Typ.	Max.	Unit
LED	Forward voltage	$V_F$	$I_F = 10 \text{ mA}$	1.0	1.15	1.3	V
	Reverse current	$I_R$	$V_R = 5 \text{ V}$	—	—	10	$\mu\text{A}$
	Capacitance	$C_T$	$V = 0 \text{ V}, f = 1 \text{ MHz}$	—	30	—	pF
Detector	Collector-emitter breakdown voltage	$V_{(BR) CEO}$	$I_C = 0.1 \text{ mA}$	350	—	—	V
	Emitter-collector breakdown voltage	$V_{(BR) ECO}$	$I_E = 0.1 \text{ mA}$	7	—	—	V
	Collector dark current	$I_{CEO}$	$V_{CE} = 300 \text{ V}$	—	10	200	nA
			$V_{CE} = 300 \text{ V}, T_a = 85^\circ\text{C}$	—	—	50	$\mu\text{A}$
Capacitance collector to emitter	$C_{CE}$	$V = 0 \text{ V}, f = 1 \text{ MHz}$	—	10	—	pF	

### Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Current transfer ratio	$I_C / I_F$	$I_F = 5 \text{ mA}, V_{CE} = 5 \text{ V}$ Rank GB	50	—	600	%
			100	—	600	
Saturated CTR	$I_C / I_F (\text{sat})$	$I_F = 1 \text{ mA}, V_{CE} = 0.4 \text{ V}$ Rank GB	—	60	—	%
			30	—	—	
Collector-emitter saturation voltage	$V_{CE} (\text{sat})$	$I_C = 2.4 \text{ mA}, I_F = 8 \text{ mA}$ $I_C = 0.2 \text{ mA}, I_F = 1 \text{ mA}$ Rank GB	—	—	0.4	V
			—	0.2	—	
			—	—	0.4	

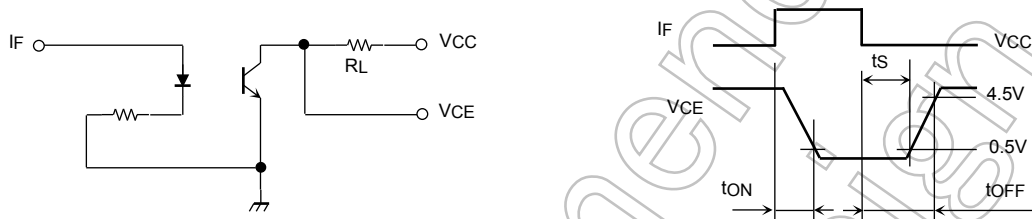
### Isolation Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Capacitance input to output	$C_S$	$V_S = 0 \text{ V}, f = 1 \text{ MHz}$	—	0.8	—	pF
Isolation resistance	$R_S$	$V_S = 500 \text{ V R.H.} \leq 60 \%$	$5 \times 10^{10}$	$10^{14}$	—	$\Omega$
Isolation voltage	$BV_S$	AC, 60 s	5000	—	—	$V_{\text{rms}}$

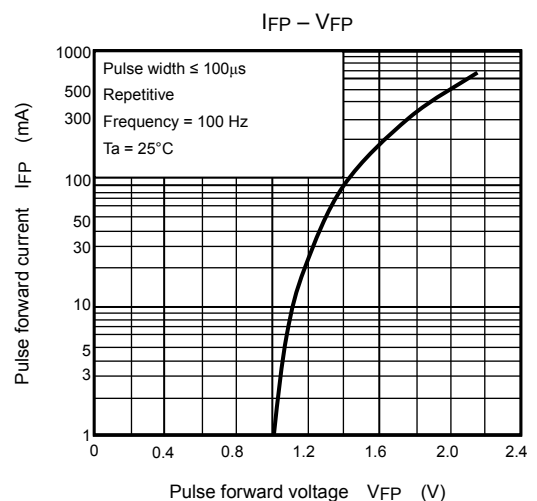
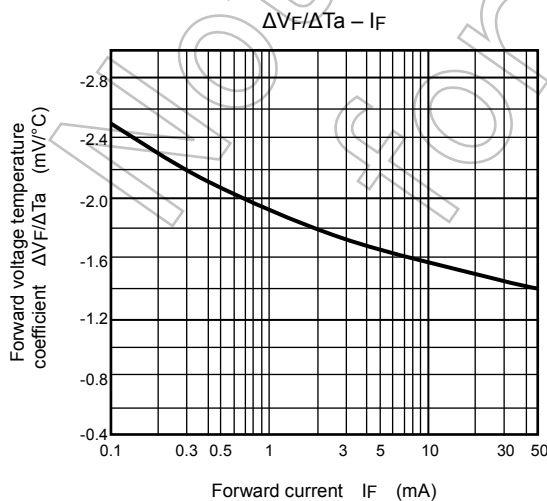
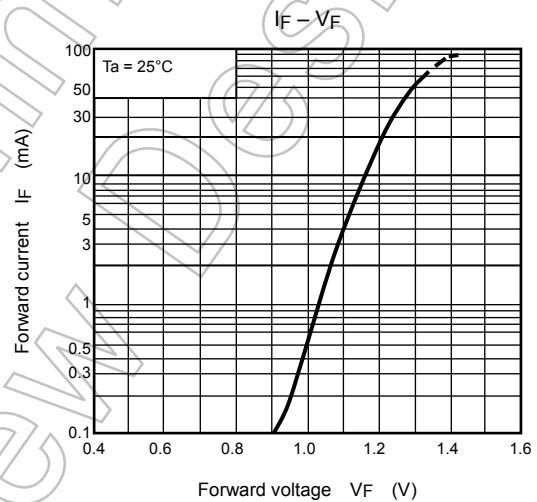
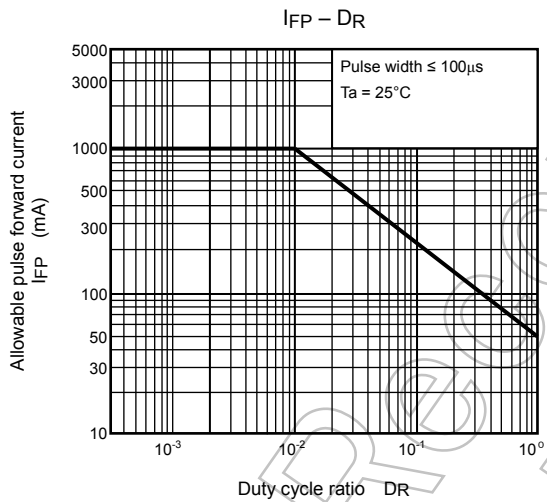
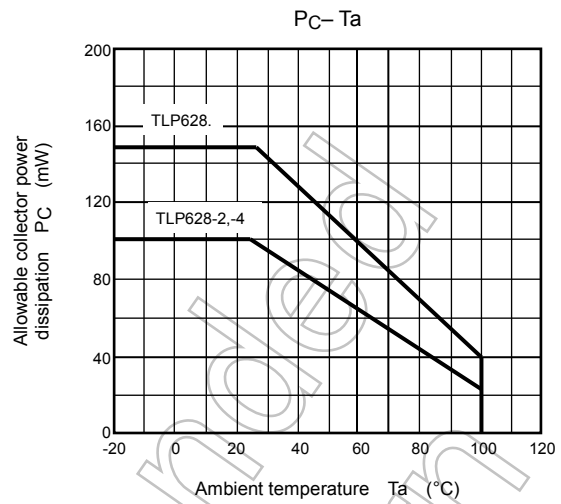
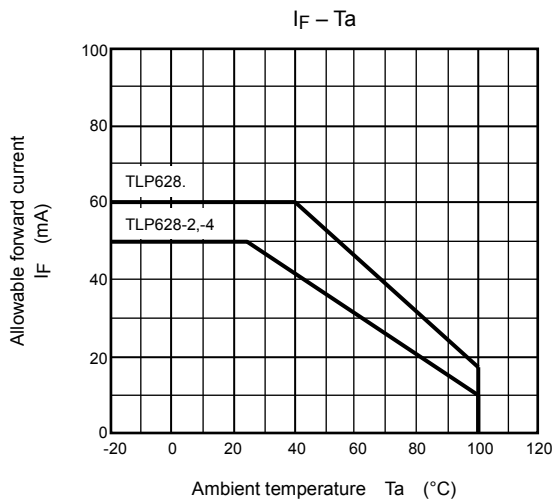
### Switching Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Rise time	$t_r$	$V_{CC} = 10\text{ V}, I_C = 2\text{ mA}$ $ARL = 100\ \Omega$	—	2	—	$\mu\text{s}$
Fall time	$t_f$		—	3	—	
Turn-on time	$t_{on}$		—	3	—	
Turn-off time	$t_{off}$		—	3	—	
Turn-on time	$t_{ON}$	$R_L = 1.9\text{ k}\Omega$ (Note 1) $V_{CC} = 5\text{ V}, I_F = 16\text{ mA}$	—	3	—	$\mu\text{s}$
Storage time	$t_s$		—	40	—	
Turn-off time	$t_{OFF}$		—	90	—	

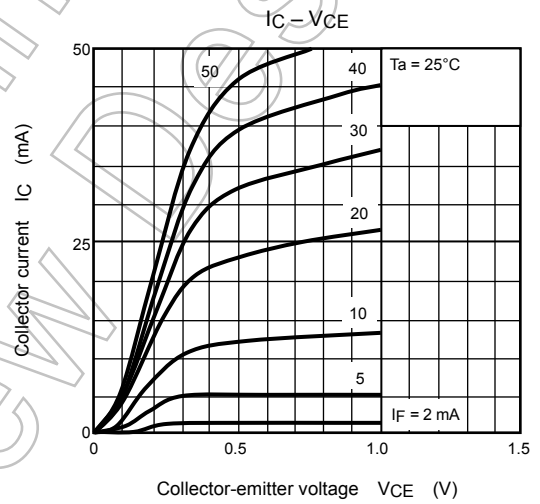
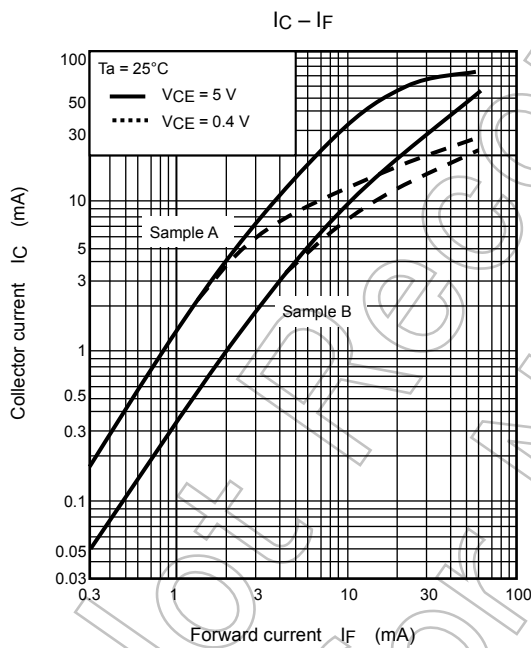
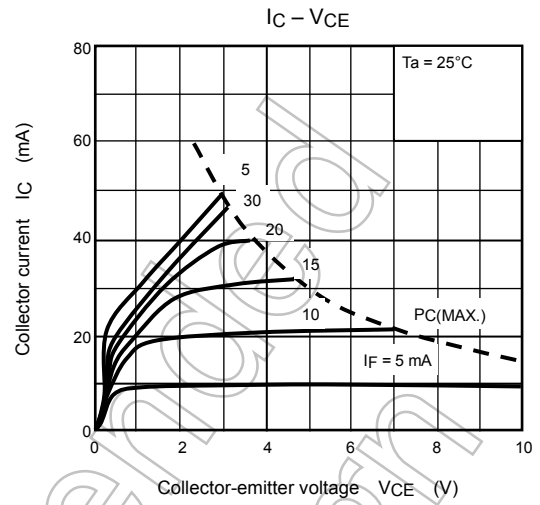
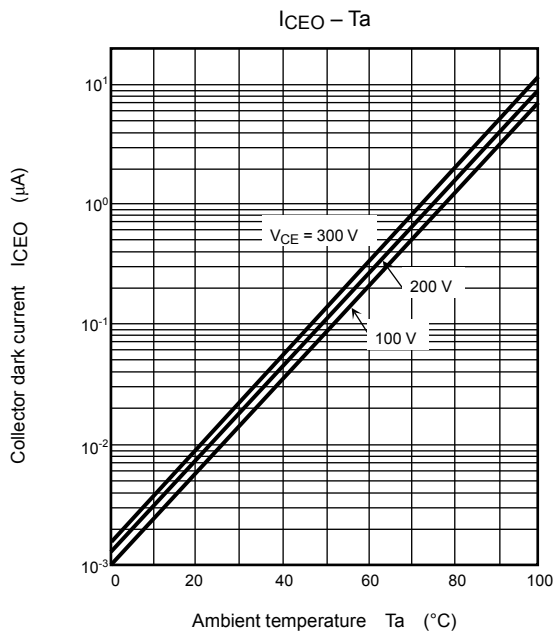
Note 1 Switching time test circuit



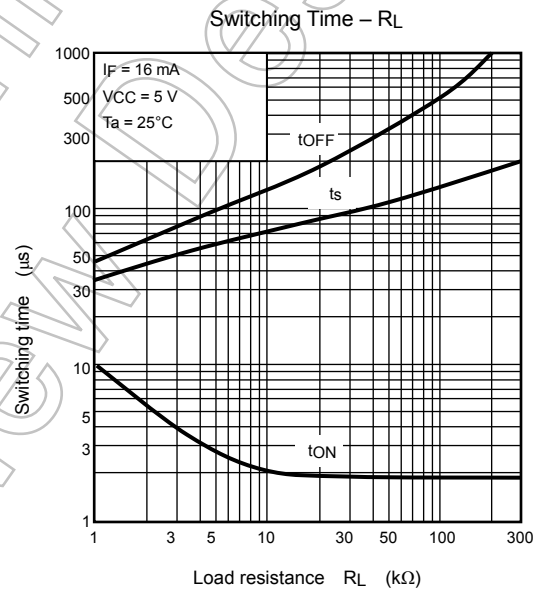
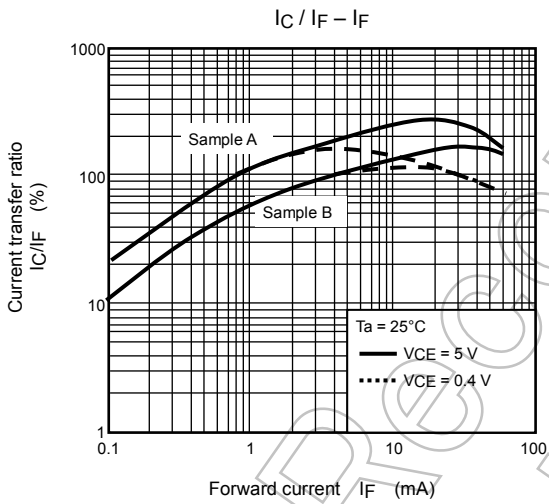
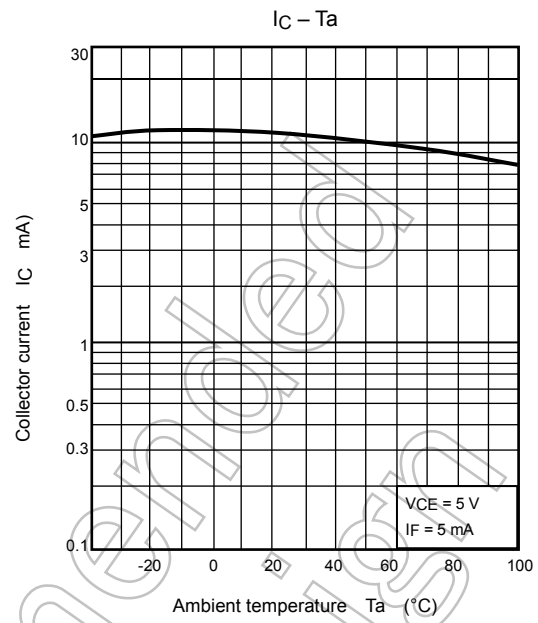
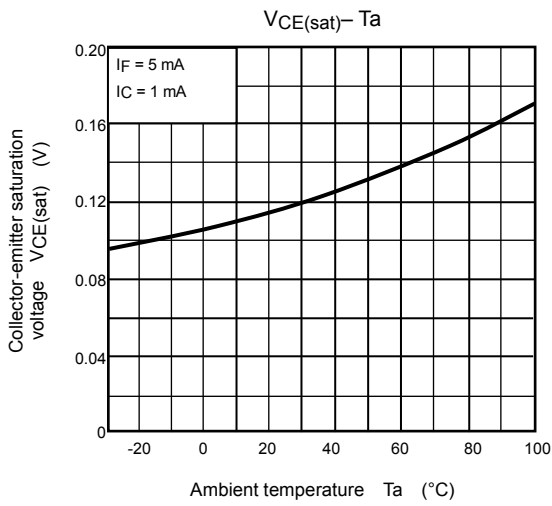
Not Recommended for New Design



NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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