Unit: mm

TOSHIBA Photocoupler GaAlAs Ired & Photo-IC

TLP557

Industrial Inverter Inverter for Air Conditioner Power Transistor Base Drive

The TOSHIBA TLP557 consists of a GaAlAs light emitting diode and an integrated photodetector.

This unit is 8-lead DIP package.

TLP557 is suitable for base driving circuit of power transistor module up to 20A.

External resistor needs to connect between pin 6 and pin 7.

This is for constant current driving.

• Input threshold current: IF = 5 mA (max)

• Guaranteed performance temperature range: -30 to 70°C

• Supply voltage: 16 V (max)

• Output current: ±0.3 A (max)

• Switching time (tpLH/tpHL): 5 µs (max)

• Isolation voltage: 2500 Vrms (min)

UL approved: UL1577, file No. E67349

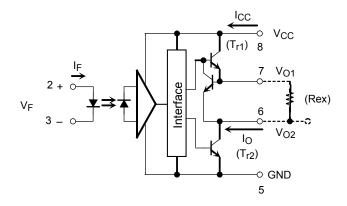
• c-UL approved : CSA Component Acceptance Service

No. 5A, File No.E67349

1.2 ± 0.15 0.5 ± 0.11 2.54 ± 0.25 1.2 ± 0.25 1.2 ± 0.15 0.25 ± 0.05 1.2 ± 0.15 0.25 ± 0.05 7.85 - 8.80

Weight: 0.54 g (typ.)

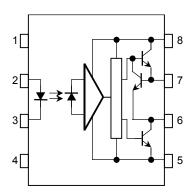
Schematic



Truth Table

		Tr1	Tr2
Input	ON	ON	OFF
LED	OFF	OFF	ON

Pin Configuration (top view)



1 : N.C.

2 : Anode

3 : Cathode

4 : N.C.

5 : GND

6: V_{O2}(Output)

7: V_{O1}(Rex Terminal)

8: V_{CC}

Start of commercial production 1987-06

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Absolute Maximum Ratings

	Characteristic	Symbol	Rating	Unit
	Forward current	lF	25	mA
	Peak transient forward current (Pw ≤ 1 µs, 300 pps)	I _{FPT}	1	А
LED	Reverse voltage	VR	5	V
	Diode power dissipation	PD	45	mW
	Junction temperature	Tj	125	°C
	Output current (f ≤ 5kHz, Duty ≤ 50%)	Io	+0.32/-0.32	А
	Peak output current (P _W ≤ 10µs, f ≤ 5kHz)	IOP	+2/-0.5	Α
	Output voltage	Vo	16	V
Detecto	Supply voltage	Vcc	16	V
	O ₁ terminal to O ₂ terminal (pin 7-pin 6) voltage	V ₁ -2	1.5	V
	O ₂ terminal to O ₁ terminal (pin 6-pin 7) voltage	V ₂ -1	5	V
	Power dissipation	Po	0.5	W
	Power dissipation derating (Ta > 50 °C)	ΔΡ _Ο /ΔΤα	-6.7	mW/°C
	Junction temperature	Tj	125	°C
Total	package power dissipation	POT	0.55	W
Total package power dissipation derating (Ta > 50 °C)		ΔΡΟΤ/ΔΤα	-7.4	mW/°C
Operating temperature range		T _{opr}	-30 to 70	°C
Stora	ge temperature range	T _{stg}	-55 to 125	°C
Lead	solder temperature (10 s)	T _{sol}	260	°C
Isola	tion voltage (AC, 60 s, R.H.≤ 60%, Ta=25°C) (Note 1)	BVS	2500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

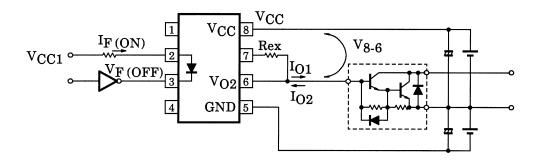
Note 1: Device considered a two terminal device: Pins 1, 2, 3 and 4 shorted together, and pins 5, 6, 7 and 8 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min	Тур.	Max	Unit
Input current on	I _F (ON)	7	8	20	mA
Input voltage off	V _F (OFF)	0	_	0.8	V
Supply voltage	Vcc	5	6	13	V
I _{B1} Drive current	lo ₁	_	0.15	0.25	Α
I _{B2} Drive current	l _{O2}	_	_	0.5	Α
External resistance	Rex	2.7	4.3	_	Ω
V _{CC} -V _{O2} (pin 8-pin 6) ON voltage (Note 1)	V ₈₋₆	2.3	3 (I _{O1} = 0.15A)	2.5 (I _{O1} = 0.25A)	V
Operating temperature	Topr	-30	25	70	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Note 1:





Electrical Characteristics (Ta = -30 to 70°C, unless otherwise specified)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit	
Input forward voltage	VF	_	I _F = 5mA , Ta = 25°C	_	1.55	1.7	V	
Temperature coefficient of forward voltage	ΔV _F /ΔTa	_	I _F = 5mA		_	-2.0	_	mV/°C
Input reverse current	IR	_	V _R = 5V, Ta = 25°C		_	_	10	μA
Input capacitance	Ст	_	V = 0 V, f = 1MHz , Ta =	25°C	_	_	250	pF
O ₁ Output leakage current	I _{O1L}	1	V _{CC} = 16V, V _{O1} = 0 V, V	_F = 0.8V	_	0.01	200	μΑ
O ₂ Output leakage current	l _{O2L}	2	VCC = 16V, VO2 = 16V, I	_F = 5mA	_	0.2	200	μA
		_	V ₈₋₆ = 2.3V	V _C C = 6V	0.22	0.27	0.32	
O ₁ Output current	lo	3	Rex = 2.7Ω I _F = 5mA, Ta = 25° C	Vcc = 16V	0.22	0.27	0.32	Α
O ₂ High level output voltage	Voн	4	V_{CC} = 6V, Rex = 2.7 Ω IF = 5mA	1	3.5	5.5	_	٧
		5	$V_F = 0.8V$, Rex = 2.7 Ω	V _C C = 6V	_	0.2	0.4	V
	VoL		I _O = 0.25A, Ta = 25°C	V _C C = 16V	_	0.2	0.4	
O ₂ Low level output voltage			V _F = 0.8V, Rex = 2.7Ω I _O = 0.5A (Note 1) Ta = 25°C	V _C C = 6V	_	0.4	_	V
				V _C C = 16V	_	0.4	_	
	Іссн	_	V _{CC} = 6V, I _F = 5mA Rex = 2.7Ω, Ta = 25°C		_	3.8	10	mA
High level supply current			V_{CC} = 6V, I _F = 5mA, Rex = 2.7 Ω		_	_	13	
			$V_{CC} = 16V$, $I_F = 5mA$, $Rex = 2.7\Omega$		_	5.2	17	
	ICCL	_	V _{CC} = 6V, I _F = 0mA Rex = 2.7Ω, Ta = 25°C		_	11	17	
Low level supply current			$V_{CC} = 6V$, $I_F = 0mA$, $Rex = 2.7\Omega$		_	_	22	mA
			$V_{CC} = 16V$, $I_F = 0mA$, $Rex = 2.7\Omega$		_	13	25	
"Output L→H" threshold	I _{FLH} –		Rex = 2.7Ω	V _{CC} = 6V	_	2.5	5	mA
input current		_	I _O = 0.25A V _{O2} > 3V	Vcc = 16V	_	_	5	
"Output H→L" threshold	VFHL —		Rex = 2.7Ω	V _C C = 6V	0.8	_	_	V
input current		_	I _O = 0.25A V _{O2} < 0.4V	V _{CC} = 16V	0.8	_	_	
Input current hysteresis	IHYS	_	V _{CC} = 6V, Rex = 2.7Ω, Ta = 25°C		_	0.05	_	mA
Supply voltage	Vcc	_	_		5	_	16	V
Capacitance (input-output)	Cs	_	V _S = 0 V, f = 1MHz, Ta = 25°C		_	1.0	2.0	pF
Resistance (input-output)	Rs	_	V _S = 500V , Ta = 25°C, I	5×10 ¹⁰	10 ¹²	_	Ω	

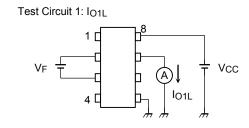
Note: All typical values are at Ta = 25°C

Note 1: Duration of IO time $\leq 100 \mu s$

Switching Characteristics (Ta = -30 to 70°C unless otherwise specified)

Characteristic	Symbol	Test Circuit	Toet Condition		Тур.	Max	Unit	
Propagation delay time, L→H	tpLH		V _{CC} = 6V, I _F = 8mA Rex = 2.7Ω	_	1	5	μs	
Propagation delay time, H→L	tpHL	6		_	1	5	μs	
Output rise time	tr		6	0	f = 5kHz, Duty = 10%	_	0.05	_
Output fall time	t _f			_	0.05	_	μs	
Common mode transient immunity at high level output	СМн	7	$V_{CM} = 600V, I_F = 8mA$ $V_{CC} = 6V, Rex = 270\Omega$ $R = 1k\Omega, Ta = 25^{\circ}C$	-2000	_	_	V/µs	
Common mode transient immunity at low level output	CML	7	$V_{CM} = 600V, I_F = 0mA$ $V_{CC} = 6V, Rex = 270\Omega$ $R = 1k\Omega, Ta = 25^{\circ}C$	2000	_	_	V/µs	

Note: All typical values are at Ta = 25°C.



Test Circuit 5: VOL

VF

VF

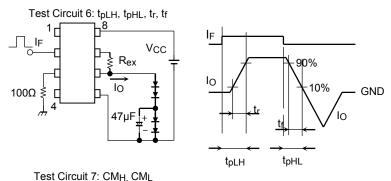
VCC

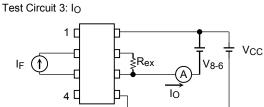
Test Circuit 2: I_{O2L}

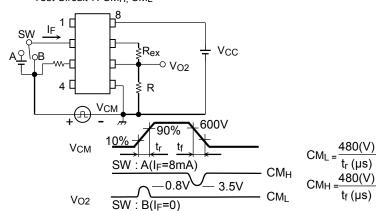
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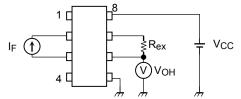
VCC





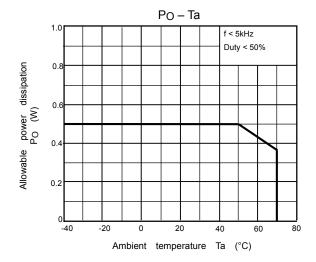


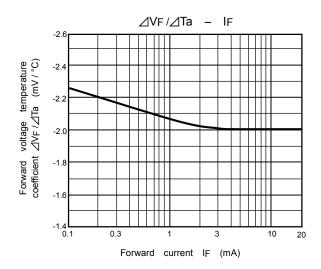


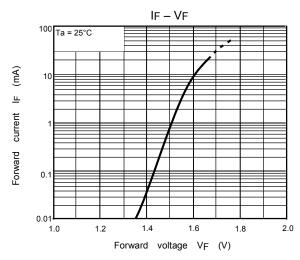


Note: CM_L (CM_H) is the maximum rate of rise (fall) of the common mode voltage that can be sustained with the output voltage in the low (high) state.

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