

## TLP200D

### Measurement Instrument

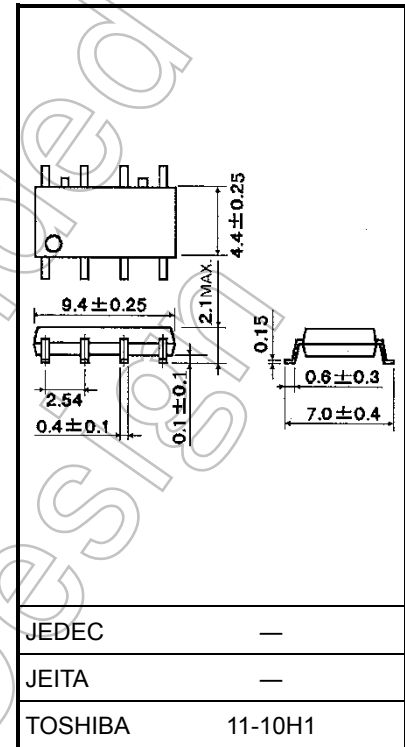
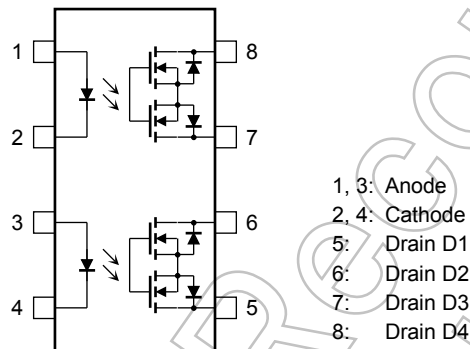
Unit: mm

The TOSHIBA TLP200D consists of an infrared emitting diode optically coupled to a photo-MOS FET in an 8-pin SOP.

The TLP200D is a 2-form-A switch which is suitable for replacement of mechanical relays in many applications which require space savings.

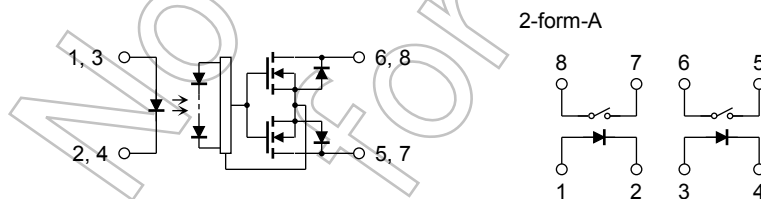
- SOP 8 pin (2.54SOP8): 2-form-A
- Peak off-state voltage: 200 V (min)
- Trigger LED current: 3 mA (max)
- On-state current: 200 mA (max)
- On-state resistance: 8 Ω (max)
- Isolation voltage: 1500 Vrms (min)
- UL-recognized: UL 1577, File No.E67349

### Pin Configurations (top view)



Weight: 0.2 g (typ.)

### Schematic



Start of commercial production  
 1997-11

**Absolute Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit
LED	Forward current	I <sub>F</sub>	50	mA
	Forward current derating (Ta ≥ 25°C)	ΔI <sub>F</sub> /°C	-0.5	mA/°C
	Pulse forward current (100 μs pulse, 100 pps)	I <sub>FP</sub>	1	A
	Reverse voltage	V <sub>R</sub>	5	V
	Diode power dissipation	P <sub>D</sub>	50	mW
	Diode power dissipation derating (Ta ≥ 25°C)	ΔP <sub>D</sub> /°C	-0.5	mW/°C
	Junction temperature	T <sub>j</sub>	125	°C
Detector	Off-state output terminal voltage	V <sub>OFF</sub>	200	V
	On-state current	I <sub>ON</sub>	200	mA
	On-state RMS current derating (Ta ≥ 25°C)	ΔI <sub>ON</sub> /°C	-2.0	mA/°C
	Output power dissipation	P <sub>O</sub>	180	mW
	Output power dissipation derating (Ta ≥ 25°C)	ΔP <sub>O</sub> /°C	-1.8	mW/°C
	Junction temperature	T <sub>j</sub>	125	°C
Storage temperature range		T <sub>stg</sub>	-55 to 125	°C
Operating temperature range		T <sub>opr</sub>	-40 to 85	°C
Lead soldering temperature (10 s)		T <sub>sol</sub>	260	°C
Isolation voltage (AC, 60 s, R.H. ≤ 60 %) (Note 1)		BVS	1500	V <sub>rms</sub>

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two-terminal device: Pins 1, 2, 3 and 4 shorted together and pins 5, 6, 7 and 8 shorted together.

Note 2: Two channels operating simultaneously.

**Recommended Operating Conditions**

Characteristics	Symbol	Min	Typ.	Max	Unit
Supply voltage	V <sub>DD</sub>	—	150	200	V
Forward current	I <sub>F</sub>	5	7.5	25	mA
On-state current	I <sub>ON</sub>	—	—	130	mA
Operating temperature	T <sub>opr</sub>	-20	—	65	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

### Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
LED	Forward voltage	$V_F$	$I_F = 10 \text{ mA}$	1.0	1.15	1.3	V
	Reverse current	$I_R$	$V_R = 5 \text{ V}$	—	—	10	$\mu\text{A}$
	Capacitance	$C_T$	$V_F = 0 \text{ V}, f = 1 \text{ MHz}$	—	30	—	pF
Detector	Off-state current	$I_{OFF}$	$V_{OFF} = 200 \text{ V}$	—	—	1	$\mu\text{A}$
	Capacitance	$C_{OFF}$	$V = 0 \text{ V}, f = 1 \text{ MHz}$	—	100	—	pF

### Coupled Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Trigger LED current	$I_{FT}$	$I_{ON} = 200 \text{ mA}$	—	1	3	mA
On-state resistance	$R_{ON}$	$I_{ON} = 200 \text{ mA}, I_F = 5 \text{ mA}$	—	5	8	$\Omega$
Return LED current	$I_{FC}$	$I_{OFF} = 100 \mu\text{A}$	0.1	—	—	mA

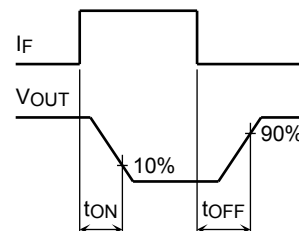
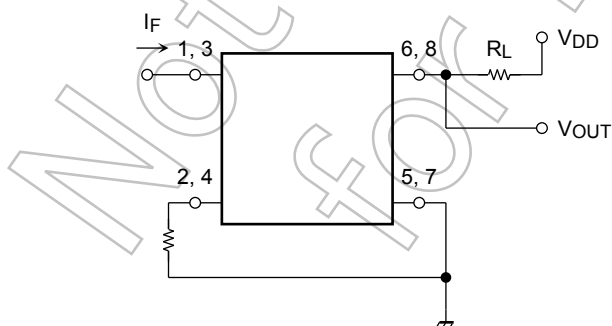
### Isolation Characteristics (Ta = 25°C)

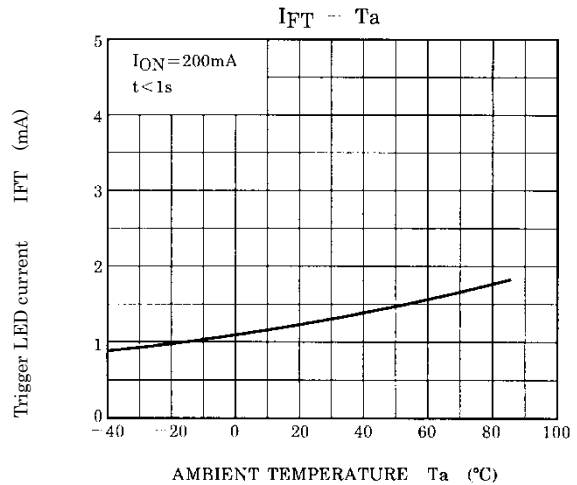
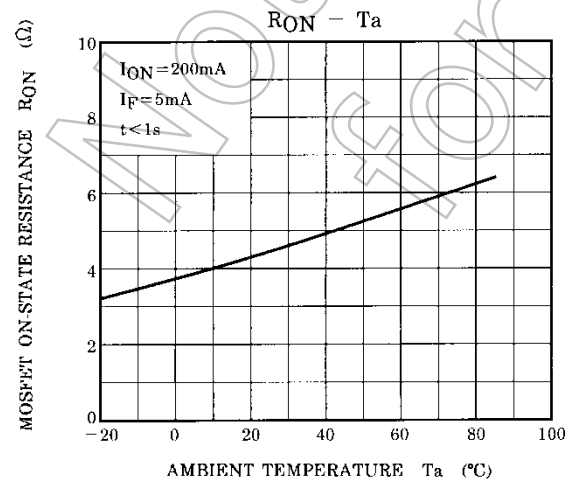
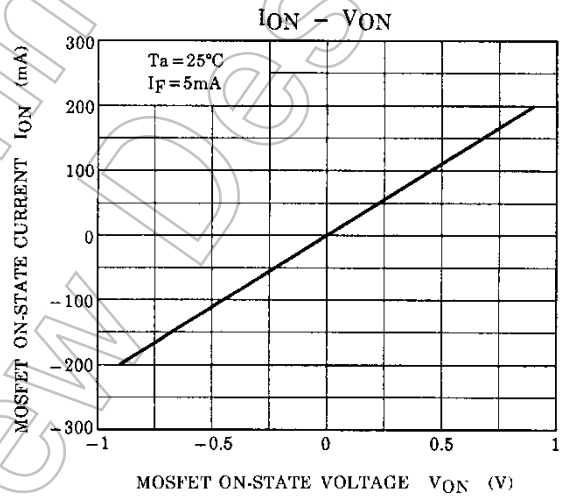
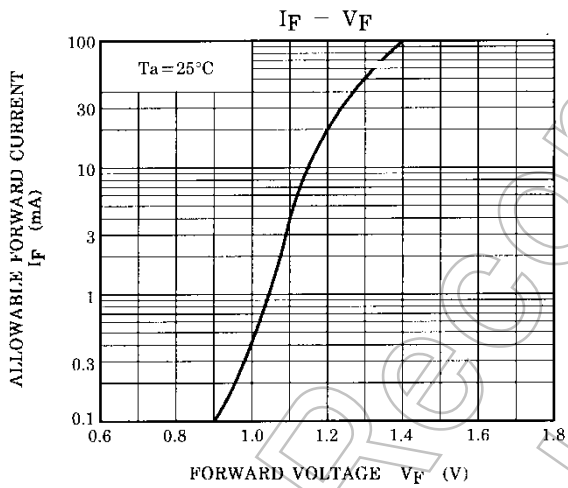
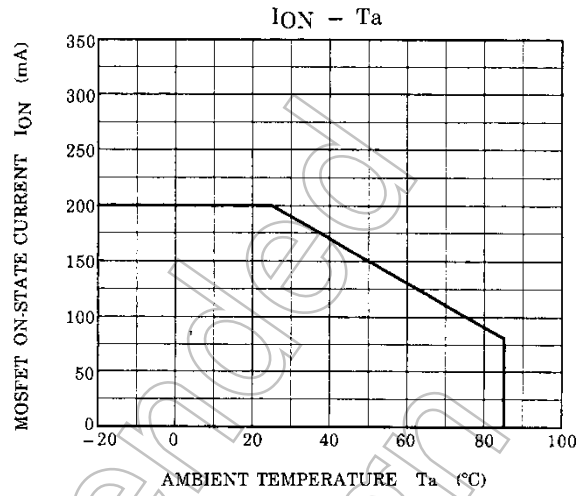
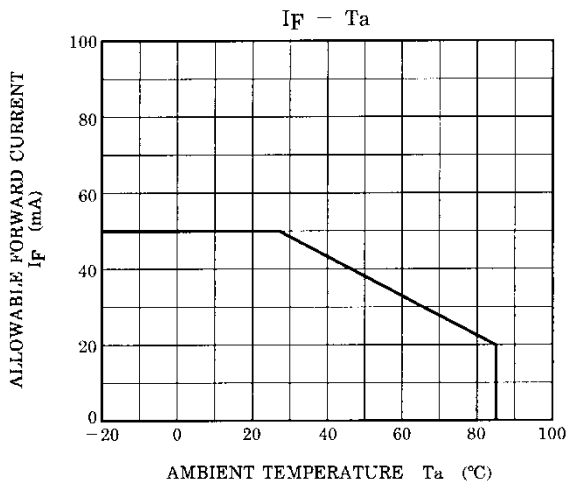
Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Capacitance input to output	$C_S$	$V_S = 0 \text{ V}, f = 1 \text{ MHz}$	—	0.8	—	pF
Isolation resistance	$R_S$	$V_S = 500 \text{ V}, \text{R.H.} \leq 60 \%$	$5 \times 10^{10}$	$10^{14}$	—	$\Omega$
Isolation voltage	$BV_S$	AC, 60 s	1500	—	—	Vrms

### Switching Characteristics (Ta = 25°C)

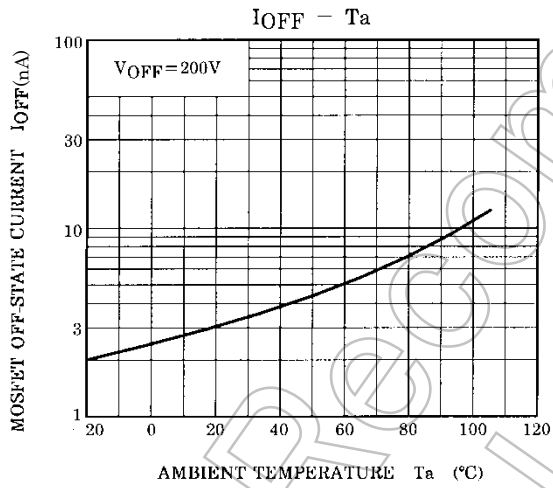
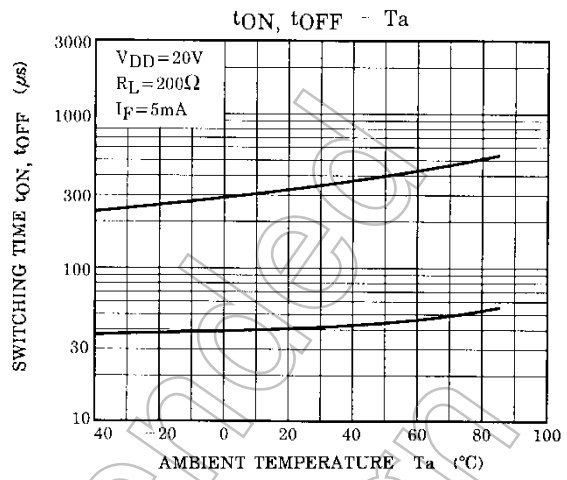
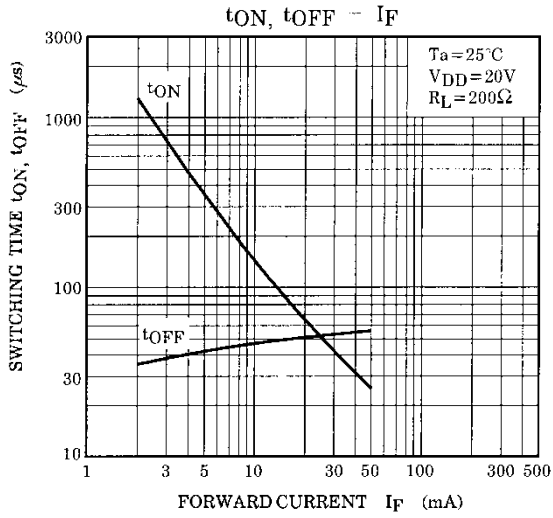
Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Turn-on time	$t_{ON}$	$R_L = 200 \Omega$ $V_{DD} = 20 \text{ V}, I_F = 5 \text{ mA}$ (Note)	—	0.6	1.5	ms
Turn-off time	$t_{OFF}$		—	0.1	1.0	ms

Note: Switching time test circuit





NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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