

Application Note

FLASH Data

Arm and Keil are registered trademarks of Arm Limited (or its subsidiaries) in the US and/or elsewhere.

All other company names, product names, and service names mentioned herein may be trademarks of their respective companies.

Table of Contents

Table of Contents	2
1. Preface	3
2. Technical Term	3
3. Reference Document	3
4. Target Sample Program	4
5. Configuration Diagram	4
6. Sample Program: FLASH_Data	5
6.1. Outlines of Operation	5
6.2. Function to Use	5
6.3. Interrupt to Use	5
6.4. Configuration	5
6.5. Example of Terminal Emulator Output	6
6.5.1. Normal Operation	6
6.5.2. Case of Erasing Error Occurrence for entire Data area	6
6.5.3. Case of Erasing Error Occurrence for Data area A	6
6.5.4. Case of Erasing Error Occurrence for Data area C	6
6.5.5. Case of Writing Error Occurrence	7
7. Activity diagram	8
7.1. main	8
7.2. Interrupt	10
8. Revision History	11
RESTRICTIONS ON PRODUCT USE	12

1. Preface

This application note describes sample software FLASH_Data that uses FLASH Memory(FLASH).
This document helps the user check operation of a product under development and develop its program.

2. Technical Term

Term/Abbreviation	Definition
BSP	Board Support Package
CG	Clock Control and Operation Mode
FLASH	Flash Memory
Timer	T32A:32-bit Timer Event Counter
UART	Universal Asynchronous Receiver Transmitter

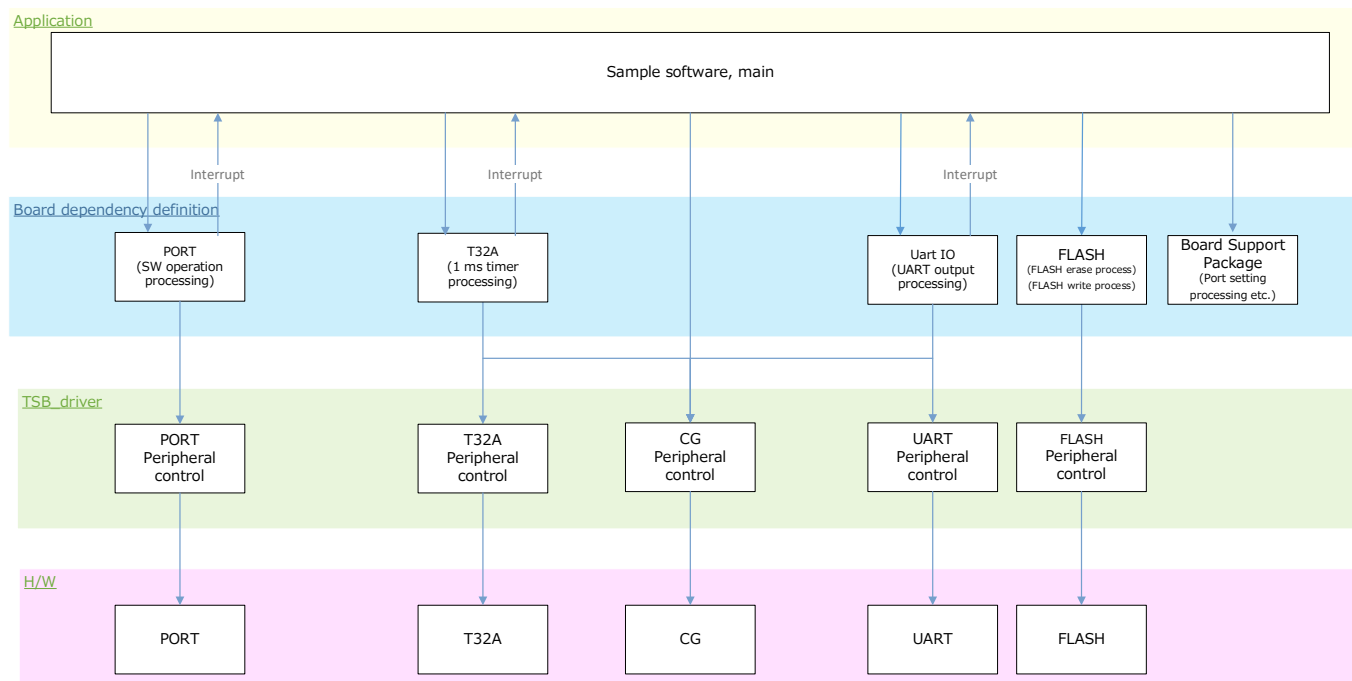
3. Reference Document

Document	Notes
Data sheet	Refer to the data sheet of MCU to be used.
Reference manual	Refer to the reference manual of each IP to be used.
Application note MCU User Guide	Refer to the MCU user guide to be used.

4. Target Sample Program

Sample Program	Outline
FLASH_Data	Sample program of FLASH_DATA function

5. Configuration Diagram



6. Sample Program: FLASH_Data

This sample software performs Data Flash access (erase and write) each time BSP_PSW_1 is pressed.

6.1. Outlines of Operation

When BSP_PSW_1 is pressed, all Data areas are erased. After that, starting from the beginning address of Data Area A, data of size Data Size A is written sequentially from 0x00 to 0xFF, followed by writing the same sequence from the beginning address of Data Area B for Data Size A.

Then Data Area A is erased, and after that Data Area C is erased.

Note: Since writing occurs in 128-byte units, if the specified size is less than 128 bytes, undefined data will be written.

6.2. Function to Use

The functions to use are as follows:

For the Port assignment of each BSP channel, refer to the MCU user guide.

IP	Channel	Objective
PORT (Push-Switch)	BSP_PSW_1	Event trigger
T32A	BSP_T32A_TIMER_1	Interval timer
UART	BSP_UART_1	Terminal communication

6.3. Interrupt to Use

For details, please refer to the "Interrupt List by Sample" in the MCU User's Manual.

6.4. Configuration

Configuration setting.

Configuration	Soft Definition Name	Description
Data entire area	-	-
Data Area A	FLASH_DATA_1	-
Data Area B	FLASH_DATA_2	-
Data Area C	-	-
Data Size A	__size_demo1__ __size_demo2__	Data Flash size allocated (used) for write operations to Data Area A and B

6.5. Example of Terminal Emulator Output

6.5.1. Normal Operation

```
Please Press the BSP_PSW_1
Area4 Erasing
Rewriting
Page0 Erasing
Block0 Erasing
Finished
Please Press the BSP_PSW_1
```

6.5.2. Case of Erasing Error Occurrence for entire Data area

```
Please Press the BSP_PSW_1
Area4 Erasing
Erasing Error!!
Please Press the BSP_PSW_1
```

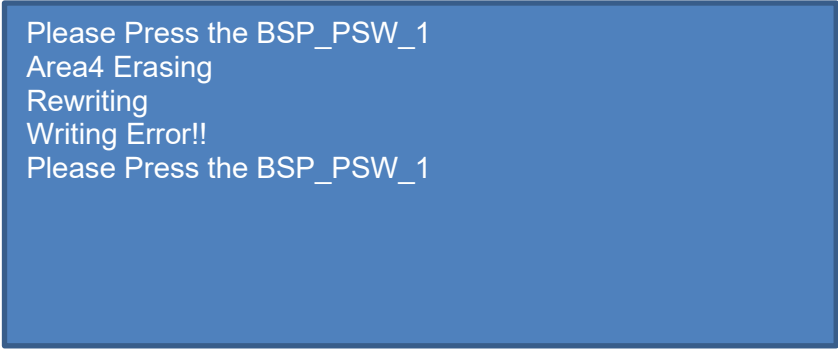
6.5.3. Case of Erasing Error Occurrence for Data area A

```
Please Press the BSP_PSW_1
Area4 Erasing
Rewriting
Page0 Erasing
Erasing Error!!
Please Press the BSP_PSW_1
!
```

6.5.4. Case of Erasing Error Occurrence for Data area C

```
Please Press the BSP_PSW_1
Area4 Erasing
Rewriting
Page0 Erasing
Block0 Erasing
Erasing Error!!
Please Press the BSP_PSW_1
```

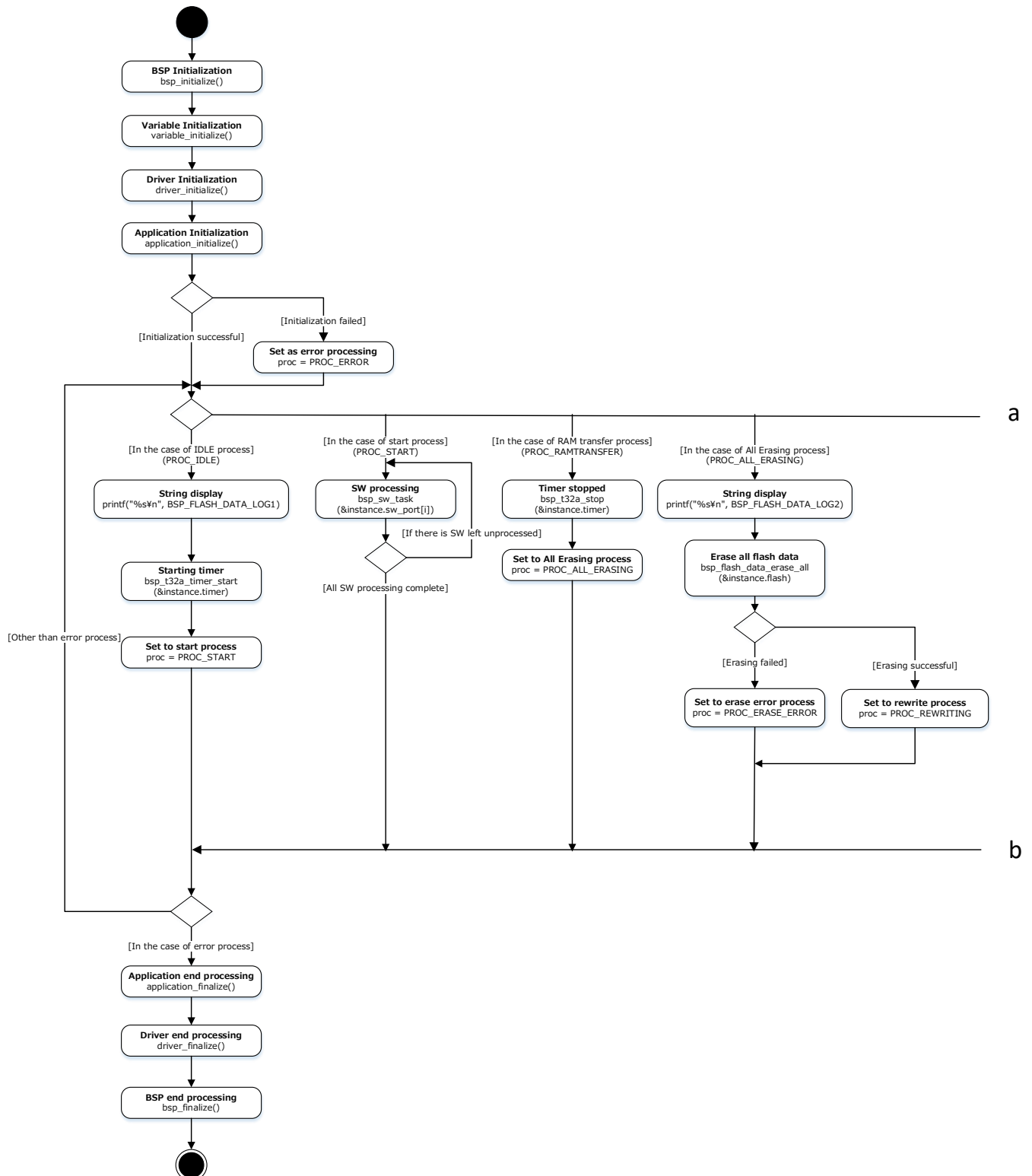
6.5.5. Case of Writing Error Occurrence

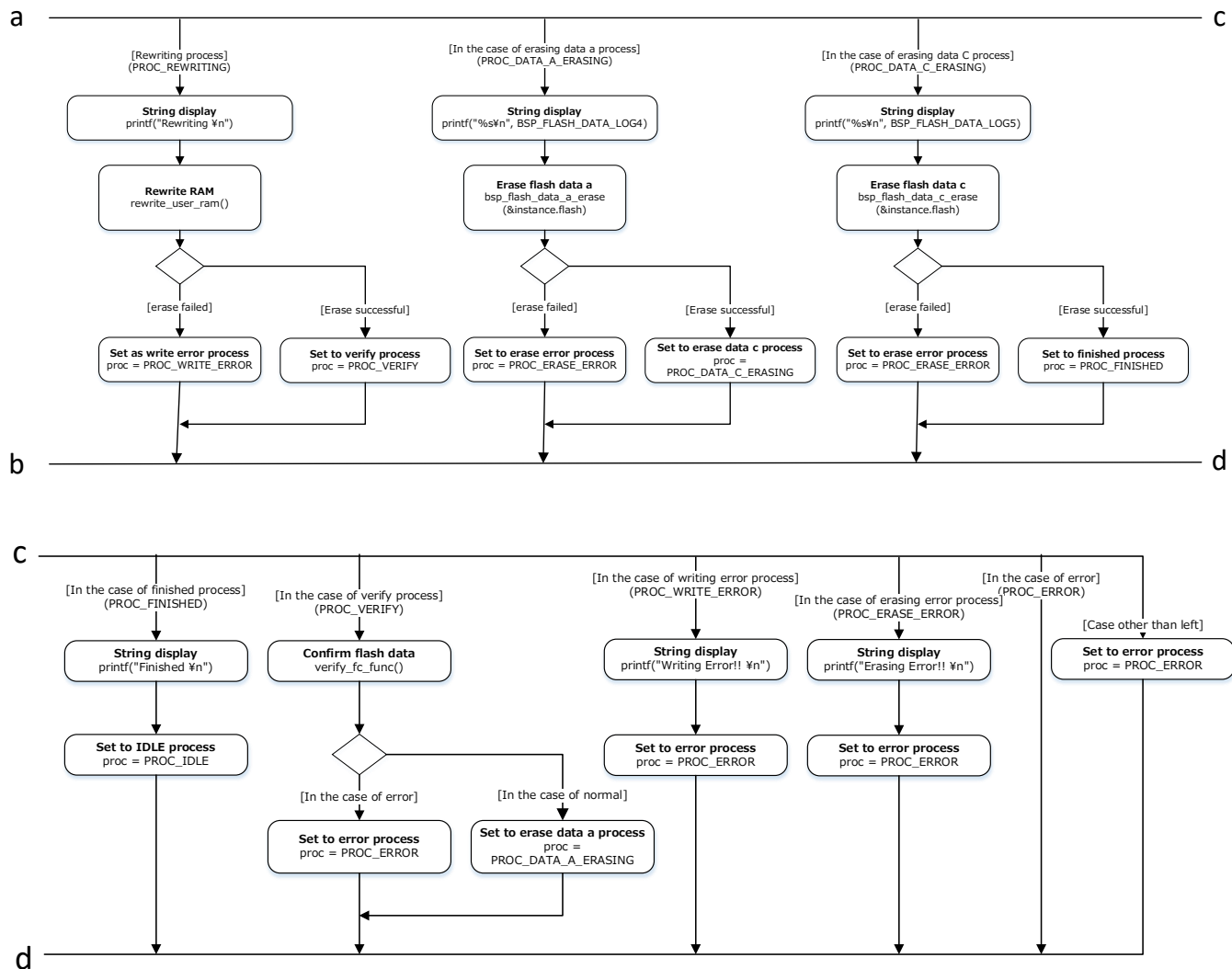


Please Press the BSP_PSW_1
Area4 Erasing
Rewriting
Writing Error!!
Please Press the BSP_PSW_1

7. Activity diagram

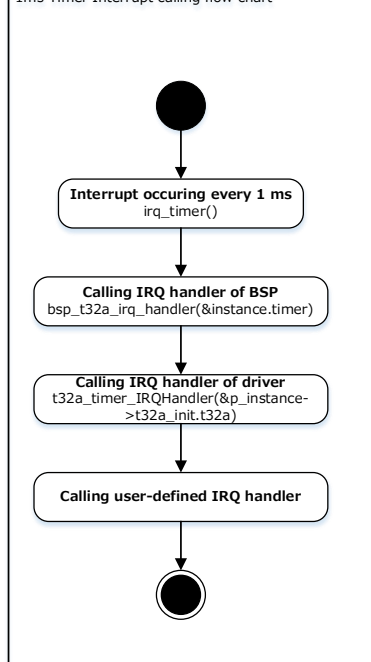
7.1. main



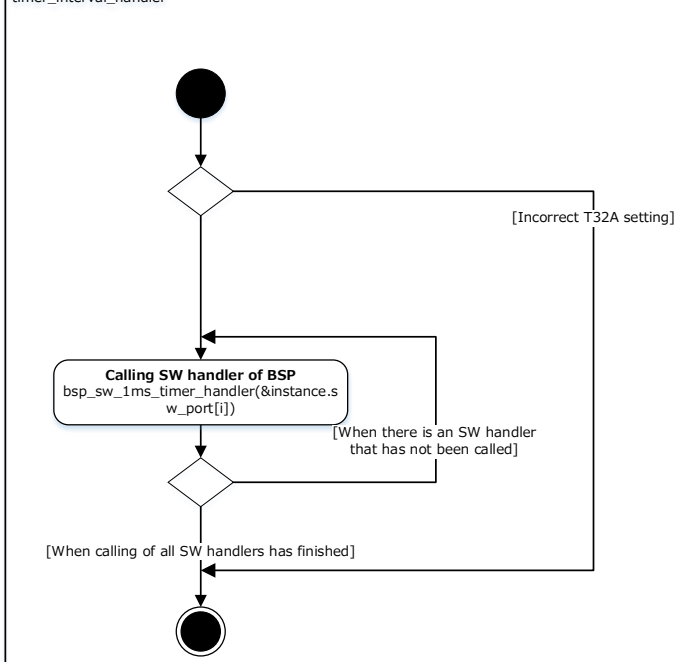


7.2. Interrupt

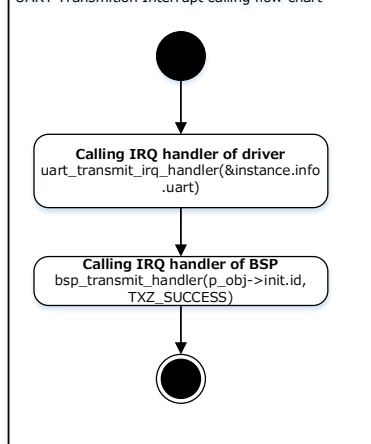
1ms Timer Interrupt calling flow chart



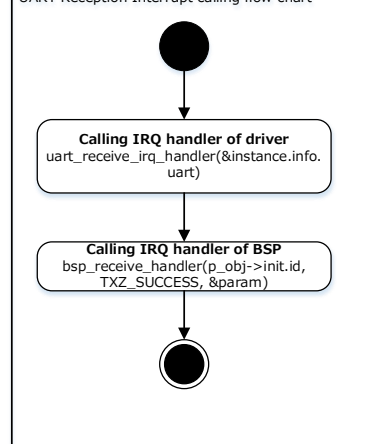
timer_interval_handler



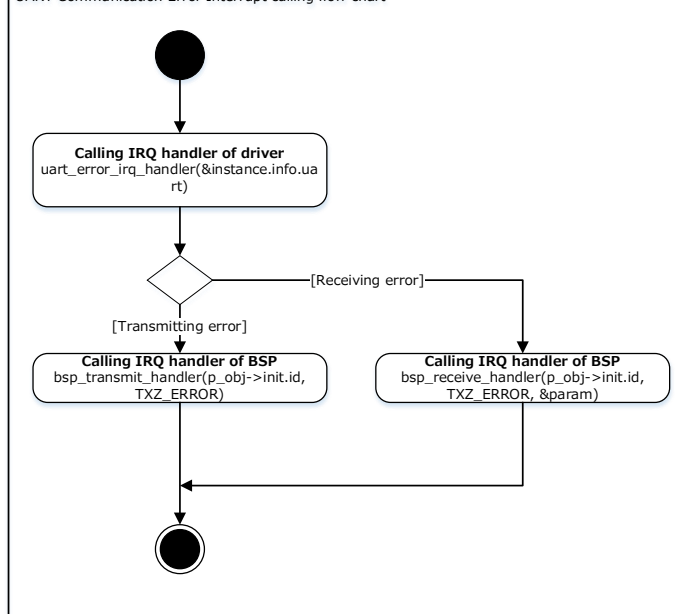
UART Transmission Interrupt calling flow chart



UART Reception Interrupt calling flow chart



UART Communication Error Interrupt calling flow chart



8. Revision History

Revision	Date	Description
1.0	2025-01-20	First release
1.1	2025-10-30	6.3 Interrupt to Use Added M3H Interrupt Name. 6.4 Configuration Added M3H-related descriptions. 6.5 Example of Terminal Emulator Output Added M3H-related descriptions.
1.2	2026-07-30	6.3 Interrupt to Use Detailed information is transcribed into the User's Manual 6.4 Configuration Detailed information is transcribed into the User's Manual 6.5 Example of Terminal Emulator Output Deleted M3H-related descriptions

RESTRICTIONS ON PRODUCT USE

Toshiba Corporation and its subsidiaries and affiliates are collectively referred to as "TOSHIBA". Hardware, software and systems described in this document are collectively referred to as "Product".

- TOSHIBA reserves the right to make changes to the information in this document and related Product without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA's written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product's quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before customers use the Product, create designs including the Product, or incorporate the Product into their own applications, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application with which the Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications. **TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.**
- **PRODUCT IS NEITHER INTENDED NOR WARRANTED FOR USE IN EQUIPMENTS OR SYSTEMS THAT REQUIRE EXTRAORDINARILY HIGH LEVELS OF QUALITY AND/OR RELIABILITY, AND/OR A MALFUNCTION OR FAILURE OF WHICH MAY CAUSE LOSS OF HUMAN LIFE, BODILY INJURY, SERIOUS PROPERTY DAMAGE AND/OR SERIOUS PUBLIC IMPACT ("UNINTENDED USE").** Except for specific applications as expressly stated in this document, Unintended Use includes, without limitation, equipment used in nuclear facilities, equipment used in the aerospace industry, Class 3 medical devices, equipment used for automobiles, and military vehicles and munitions. **IF YOU USE PRODUCT FOR UNINTENDED USE, TOSHIBA ASSUMES NO LIABILITY FOR PRODUCT.** For details, please contact your TOSHIBA sales representative or contact us via our website.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- **ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE FOR PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (2) DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO SALE, USE OF PRODUCT, OR INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.**
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). Product and related software and technology may be controlled under the applicable export laws and regulations including, without limitation, the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export Administration Regulations. Export and re-export of Product or related software or technology are strictly prohibited except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. Please use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. **TOSHIBA ASSUMES NO LIABILITY FOR DAMAGES OR LOSSES OCCURRING AS A RESULT OF NONCOMPLIANCE WITH APPLICABLE LAWS AND REGULATIONS.**