

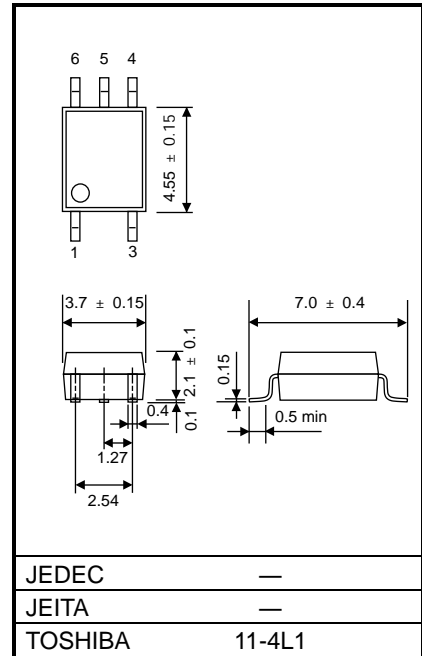
TLP151

Industrial Inverter
 MOS FET / IGBT Gate Driver
 IH(Induction Heating)
 Operate at high ambient temperatures up to 110°C

Unit: mm

The Toshiba TLP151 consists of an infrared emitting diodes and integrated high gain, high-speed photodetectors. The TLP151 is housed in the SO6 package.

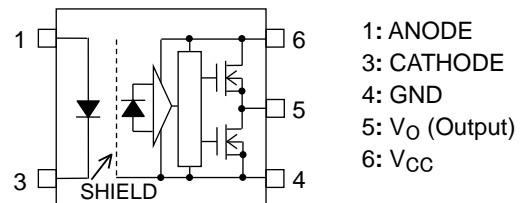
The photodetector has an internal Faraday shield that provides a guaranteed common-mode transient immunity of ± 15 kV/ μ s. TLP151 is suitable for direct gate driving circuit for IGBTs or power MOSFETs.



Weight: 0.08 g (typ.)

- Buffer logic type (Totem pole output)
- Package type: SO6
- Peak Output Current : $I_{OP} = \pm 0.6$ A (max)
- Guaranteed performance over temperature: -40 to 110°C
- Power supply voltage: 10 to 30 V
- Threshold Input Current: $I_{FLH} = 5.0$ mA (max)
- Propagation delay time : $t_{pLH} / t_{pHL} = 700$ ns (max)
- Common mode transient immunity : ± 15 kV/ μ s (min)
- Isolation voltage : 3750 V_{rms} (min)
- UL-recognized : UL 1577, File No.E67349
- cUL-recognized : CSA Component Acceptance Service No.5A File No.E67349
- VDE-approved : EN 60747-5-5 ,EN 62368-1 (Note 1)

Pin Configuration (Top View)



Note 1 : When a VDE approved type is needed, please designate the **Option(V4)**.

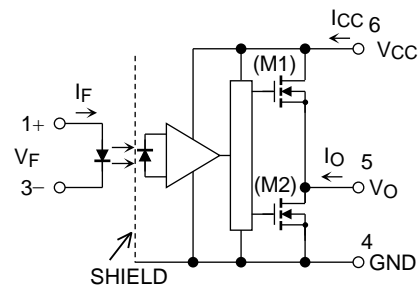
Truth Table

Input	LED	M1	M2	Output
H	ON	ON	OFF	H
L	OFF	OFF	ON	L

Construction Mechanical Ratings

Creepage distance	5.0 mm (min)
Clearance distance	5.0 mm (min)
Insulation thickness	0.4 mm (min)

Schematic



Start of commercial production
 2009-10

Absolute Maximum Ratings (Ta = 25 °C)

Characteristic		Symbol	Rating	Unit
LED	Forward Current	I_F	25	mA
	Forward Current Derating (Ta ≥ 95°C)	$\Delta I_F / ^\circ\text{C}$	-0.67	mA/°C
	Peak Transient Forward Current (Note 1)	I_{FPT}	50	mA
	Peak Transient Forward Current Derating (Ta ≥ 95°C)	$\Delta I_{FPT} / ^\circ\text{C}$	-1.34	mA/°C
	Diode Power Dissipation	P_D	40	mW
	Diode Power Dissipation Derating (Ta ≥ 95°C)	$\Delta P_D / \Delta T_a$	-1.3	mW/°C
	Reverse Voltage	V_R	5	V
Detector	"H" Peak Output Current (Note 2)	I_{OPH}	-0.6	A
	"L" Peak Output Current (Note 2)	I_{OPL}	0.6	A
	Output Voltage	V_O	35	V
	Supply Voltage	V_{CC}	35	V
	Output Power Dissipation	P_O	80	mW
	Output Power Dissipation Derating (Ta ≥ 95°C)	$\Delta P_O / \Delta T_a$	-2.0	mW/°C
Operating Temperature Range		T_{opr}	-40 to 110	°C
Storage Temperature Range		T_{stg}	-55 to 125	°C
Lead Soldering Temperature (10 s)		T_{sol}	260	°C
Isolation Voltage (AC, 60 s, R.H. ≤ 60 %) (Note 3)		BV_S	3750	V_{rms}

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 1: Pulse width ≤ 1 ms, duty = 50 %.

Note 2: Exponential waveform pulse width $P_W \leq 2 \mu\text{s}$, $f \leq 10 \text{ kHz}$

Note 3: This device is regarded as a two terminal device: pins 1 and 3 are shorted together, as are pins 4, 5 and 6.

Recommended Operating Conditions

Characteristic	Symbol	Min	Typ.	Max	Unit
Input Current, High Level (Note 1)	I_{FLH}	7.5	-	15	mA
Input Voltage, Low Level	V_{FHL}	0	-	0.8	V
Supply Voltage (Note 2)	V_{CC}	10	-	30	V
Peak output current	I_{OPH} / I_{OPL}	-	-	±0.2	A
Operating Temperature	T_{opr}	-40	-	110	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Note 1: Input signal rise time (fall time) < 0.5 μs.

Note 2: This item denotes operating range, not meaning of recommended operating conditions. A steep rising slope of the V_{CC} may cause an unstable operation of the device. Therefore please design the V_{CC} rise slope under 3.0 V/μs.

Electrical Characteristics (Ta = -40 to 110 °C, unless otherwise specified)

Characteristics		Symbol	Test Circuit	Test Condition		Min	Typ.	Max	Unit	
Forward voltage		V _F	—	I _F = 10 mA, Ta = 25 °C		1.45	1.61	1.85	V	
Temperature coefficient of forward voltage		ΔV _F /ΔTa	—	I _F = 10 mA		—	-1.8	—	mV/°C	
Input reverse current		I _R	—	V _R = 5 V, Ta = 25 °C		—	—	10	μA	
Input capacitance		C _T	—	V = 0 V, f = 1 MHz		—	60	—	pF	
Output current (Note 1)	"H" Level	I _{OPH1}	Figure 1	V _{CC} = 15 V I _F = 5 mA	V ₆₋₅ = 4 V	-0.2	-0.4	—	A	
		I _{OPH2}			V ₆₋₅ = 10 V	-0.4	—	—		
	"L" Level	I _{OPL1}	Figure 2	V _{CC} = 15 V I _F = 0 mA	V ₅₋₄ = 2 V	0.2	0.5	—		
		I _{OPL2}			V ₅₋₄ = 10 V	0.4	—	—		
Output voltage	"H" Level	V _{OH}	Figure 3	V _{CC} = 10 V	I _O = -100 mA, I _F = 5 mA	6.0	8.5	—	V	
	"L" Level	V _{OL}	Figure 4		I _O = 100 mA, V _F = 0.8 V	—	0.35	1.0		
Supply current	"H" Level	I _{CCH}	Figure 5	V _{CC} = 10 to 30 V V _O = Open	I _F = 10 mA	—	1.1	2.0	mA	
	"L" Level	I _{CCCL}	Figure 6		I _F = 0 mA	—	1.1	2.0		
Threshold input current		L → H	I _{FLH}	—	V _{CC} = 15 V, V _O > 1 V		—	1.2	5	mA
Threshold input voltage		H → L	V _{FHL}	—	V _{CC} = 15 V, V _O < 1 V		0.8	—	—	V
Supply voltage		V _{CC}	—	—		10	—	30	V	

Note: All typical values are at Ta = 25 °C, V_{CC} = 10 V unless otherwise specified

Note 1: Duration of I_O time ≤ 50 μs, 1 pulse

Isolation Characteristics (Ta = 25 °C)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Capacitance input to output	C _S (Note 1)	V _S = 0 V, f = 1MHz	—	0.8	—	pF
Isolation resistance	R _S (Note 1)	R.H. ≤ 60 %, V _S = 500 V	10 ¹²	10 ¹⁴	—	Ω
Isolation voltage	BV _S (Note 1)	AC, 60 s	3750	—	—	V _{rms}

Note 1: This device is considered as a two-terminal device: Pins 1 and 3 are shorted together, and pins 4, 5 and 6 are shorted together.

Switching Characteristics (Ta = -40 to 110 °C, unless otherwise specified)

Characteristics		Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit		
Propagation delay time	L → H	t_{pLH}	Figure 7	$V_{CC} = 30\text{ V}$ $R_g = 47\ \Omega$ $C_g = 3\text{ nF}$	$I_F = 0 \rightarrow 5\text{ mA}$	100	—	700	ns	
	H → L	t_{pHL}			$I_F = 5 \rightarrow 0\text{ mA}$	100	—	700		
Output rise time (10–90 %)		t_r				$I_F = 0 \rightarrow 5\text{ mA}$	—	50		—
Output fall time (90–10 %)		t_f				$I_F = 5 \rightarrow 0\text{ mA}$	—	50		—
Switching time dispersion between ON and OFF		$ t_{pHL} - t_{pLH} $				$I_F = 0 \leftrightarrow 5\text{ mA}$	—	—		500
Common mode transient immunity at HIGH level output		CM_H			Figure 8	$V_{CM} = 1000\text{ V}_{p-p}$ $V_{CC} = 30\text{ V}$ $T_a = 25\text{ }^\circ\text{C}$	$I_F = 5\text{ mA}$ $V_O(\text{min}) = 26\text{ V}$	-15		—
Common mode transient immunity at LOW level output		CM_L	$I_F = 0\text{ mA}$ $V_O(\text{max}) = 1\text{ V}$	15			—	—		

Note: All typical values are at $T_a = 25\text{ }^\circ\text{C}$.

Note: A ceramic capacitor (0.1 μF) should be connected from pin 6 (V_{CC}) to pin 4 (GND) to stabilize the operation of the high gain linear amplifier. Failure to provide the bypass may impair the switching property. The total lead length between capacitor and coupler should not exceed 1 cm.

Figure 1: IOPH Test Circuit

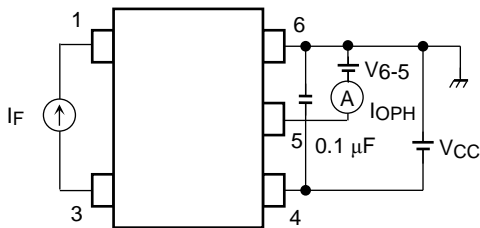


Figure 2: IOPL Test Circuit

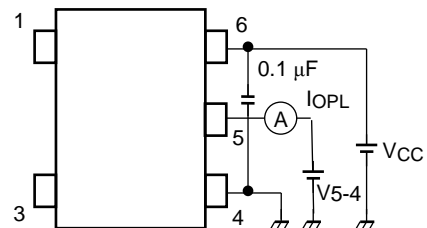
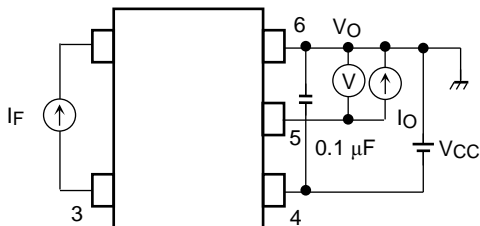


Figure 3: VOH Test Circuit



$$*V_{OH} = V_{CC} - V_O$$

Figure 4: VOL Test Circuit

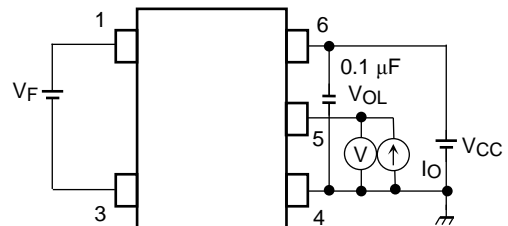


Figure 5: ICCH Test Circuit

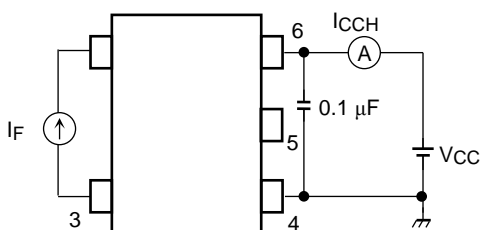


Figure 6: ICCL Test Circuit

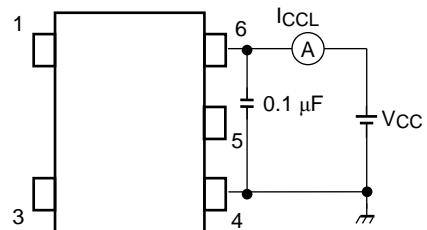


Figure 7: tpLH, tpHL, tr, tf, |tpHL-tpLH| Test Circuit

I_F = 5 mA (P.G)
 (f = 25 kHz, duty = 50%, tr = tf = 5 ns)

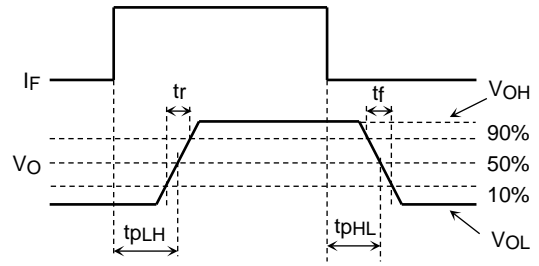
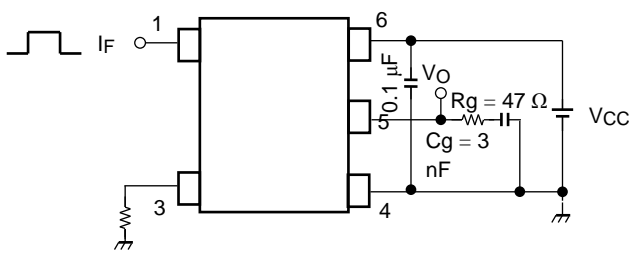
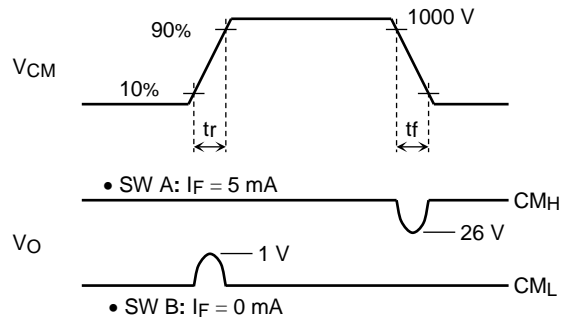
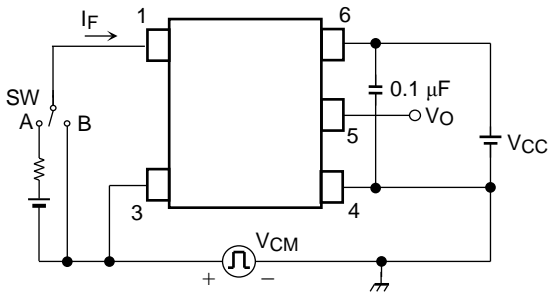


Figure 8: CMH, CML Test Circuit



$$C_{ML} = \frac{800 \text{ V}}{tr (\mu\text{s})}$$

$$C_{MH} = - \frac{800 \text{ V}}{tf (\mu\text{s})}$$

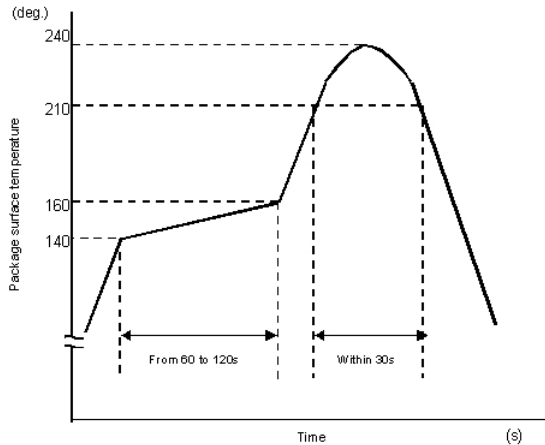
CML (CMH) is the maximum rate of rise (fall) of the common mode voltage that can be sustained with the output voltage in the LOW (HIGH) state.

PRECAUTIONS OF SURFACE MOUNTING TYPE PHOTOCOUPLER SOLDERING & GENERAL STORAGE

(1) Precautions for Soldering

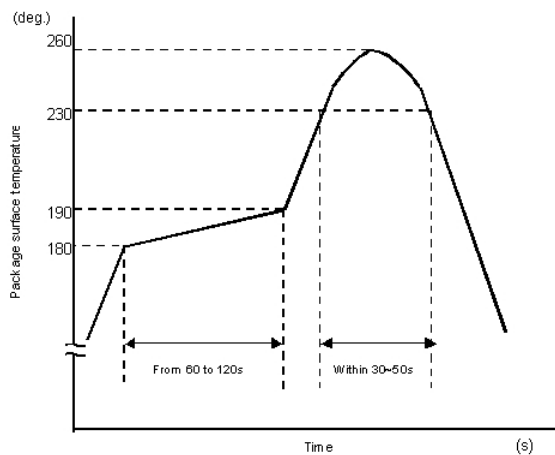
1) When Using Soldering Reflow

- An example of a temperature profile when Sn-Pb eutectic solder is used:



This profile is based on the device's maximum heat resistance guaranteed value.
Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

- An example of a temperature profile when lead(Pb)-free solder is used:



This profile is based on the device's maximum heat resistance guaranteed value.
Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

- Reflow soldering must be performed once or twice.
- The mounting should be completed with the interval from the first to the last mountings being 2 weeks.

2) When using soldering Flow (Applicable to both eutectic solder and Lead(Pb)-Free solder)

- Preheat the device at a temperature of 150 °C (package surface temperature) for 60 to 120 seconds.
- Mounting condition of 260 °C within 10 seconds is recommended.
- Flow soldering must be performed once

3) When using soldering Iron (Applicable to both eutectic solder and Lead(Pb)-Free solder)

- Complete soldering within 10 seconds for lead temperature not exceeding 260 °C or within 3 seconds not exceeding 350 °C.
- Heating by soldering iron must be done only once per lead.

(2) Precautions for General Storage

- 1) Do not store devices at any place where they will be exposed to moisture or direct sunlight.
- 2) When transportation or storage of devices, follow the cautions indicated on the carton box.
- 3) The storage area temperature should be kept within a temperature range of 5 °C to 35 °C, and relative humidity should be maintained at between 45% and 75%.
- 4) Do not store devices in the presence of harmful (especially corrosive) gases, or in dusty conditions.
- 5) Use storage areas where there is minimal temperature fluctuation. Because rapid temperature changes can cause condensation to occur on stored devices, resulting in lead oxidation or corrosion, as a result, the solderability of the leads will be degraded.
- 6) When repacking devices, use anti-static containers.
- 7) Do not apply any external force or load directly to devices while they are in storage.
- 8) If devices have been stored for more than two years, even though the above conditions have been followed, it is recommended that solderability of them should be tested before they are used.

Specification for Embossed-Tape Packing (TPL)(TPR) for SO6 Coupler

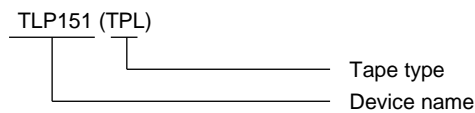
1. Applicable Package

Package	Product Type
SO6	Mini-flat coupler

2. Product Naming System

Type of package used for shipment is denoted by a symbol suffix after a product number. The method of classification is as below.

(Example)



3. Tape Dimensions

3.1 Specification Classification Are as Shown in Table 1

Table 1 Tape Type Classification

Tape type	Classification	Quantity (pcs / reel)
TPL	L direction	3000
TPR	R direction	3000

3.2 Orientation of Device in Relation to Direction of Tape Movement

Device orientation in the recesses is as shown in Figure 1.

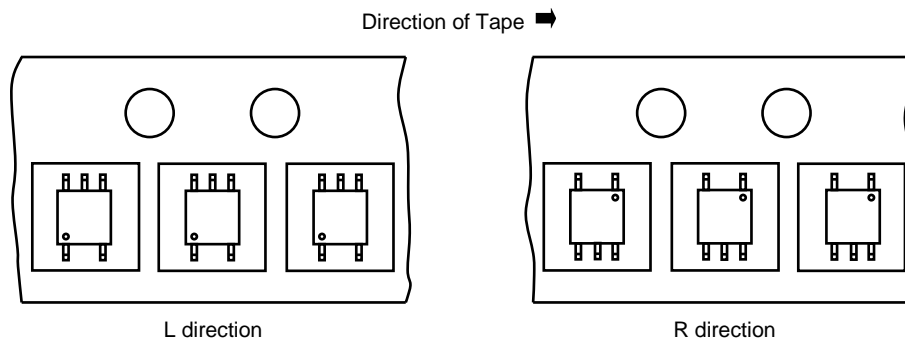


Figure 1 Device Orientation

3.3 Empty Device Recesses Are as Shown in Table 2.

Table 2 Empty Device Recesses

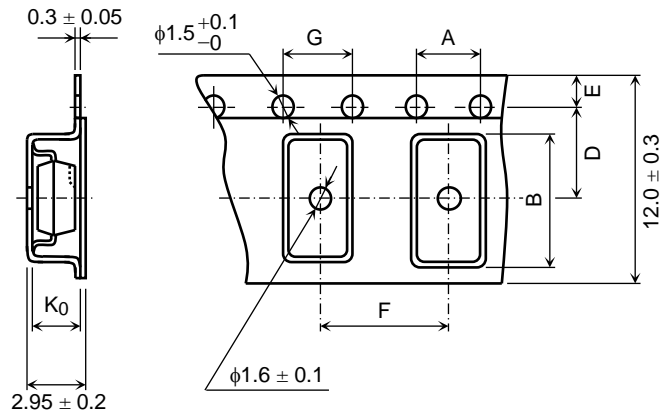
	Standard	Remarks
Occurrences of 2 or more successive empty device recesses	0 device	Within any given 40-mm section of tape, not including leader and trailer
Single empty device recesses	6 devices (max) per reel	Not including leader and trailer

3.4 Start and End of Tape

The start of the tape has 50 or more empty holes. The end of tape has 50 or more empty holes and two empty turns only for a cover tape.

3.5 Tape Specification

- (1) Tape material: Plastic (protection against electrostatics)
- (2) Dimensions: The tape dimensions are as shown in Figure 2 and Table 3.



Unit: mm

Figure 2 Tape Forms

Table 3 Tape Dimensions

Unit: mm
Unless otherwise specified: ±0.1

Symbol	Dimension	Remark
A	4.0	—
B	7.6	—
D	5.5	Center line of indented square hole and sprocket hole
E	1.75	Distance between tape edge and hole center
F	8.0	Cumulative error $\begin{matrix} +0.1 \\ -0.3 \end{matrix}$ (max) per 10 feed holes
G	4.0	Cumulative error $\begin{matrix} +0.1 \\ -0.3 \end{matrix}$ (max) per 10 feed holes
K ₀	2.6	Internal space

3.6 Reel

- (1) Material: Plastic (protection against electrostatics)
- (2) Dimensions: The reel dimensions are as shown in Figure 3 and Table 4.

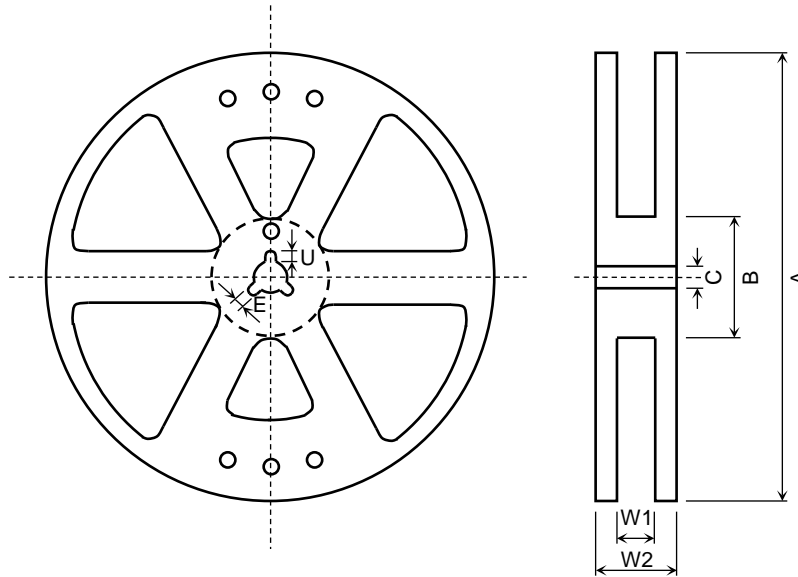


Figure 3 Reel Form

Table 4 Reel Dimensions

Unit: mm

Symbol	Dimension
A	$\Phi 380 \pm 2$
B	$\Phi 80 \pm 1$
C	$\Phi 13 \pm 0.5$
E	2.0 ± 0.5
U	4.0 ± 0.5
W1	13.5 ± 0.5
W2	17.5 ± 1.0

4. Packing

Packed in a shipping carton.

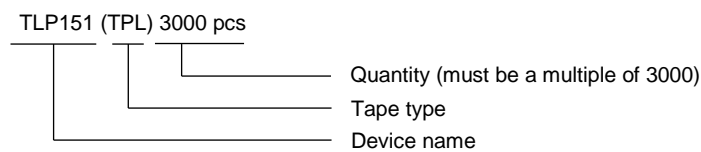
5. Label Indication

The carton bears a label indicating the product number, the symbol representing classification of standard, the quantity, the lot number and the Toshiba company name.

6. Ordering Method

When placing an order, please specify the product number, the tape type and the quantity as shown in the following example.

(Example)



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